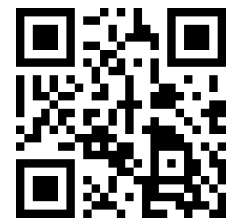
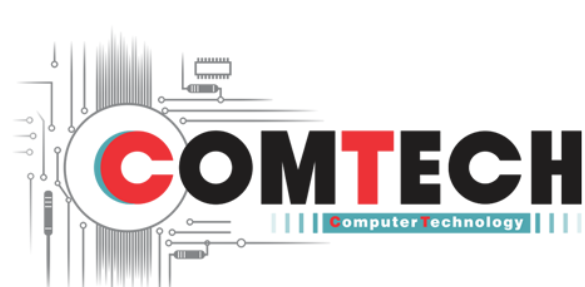
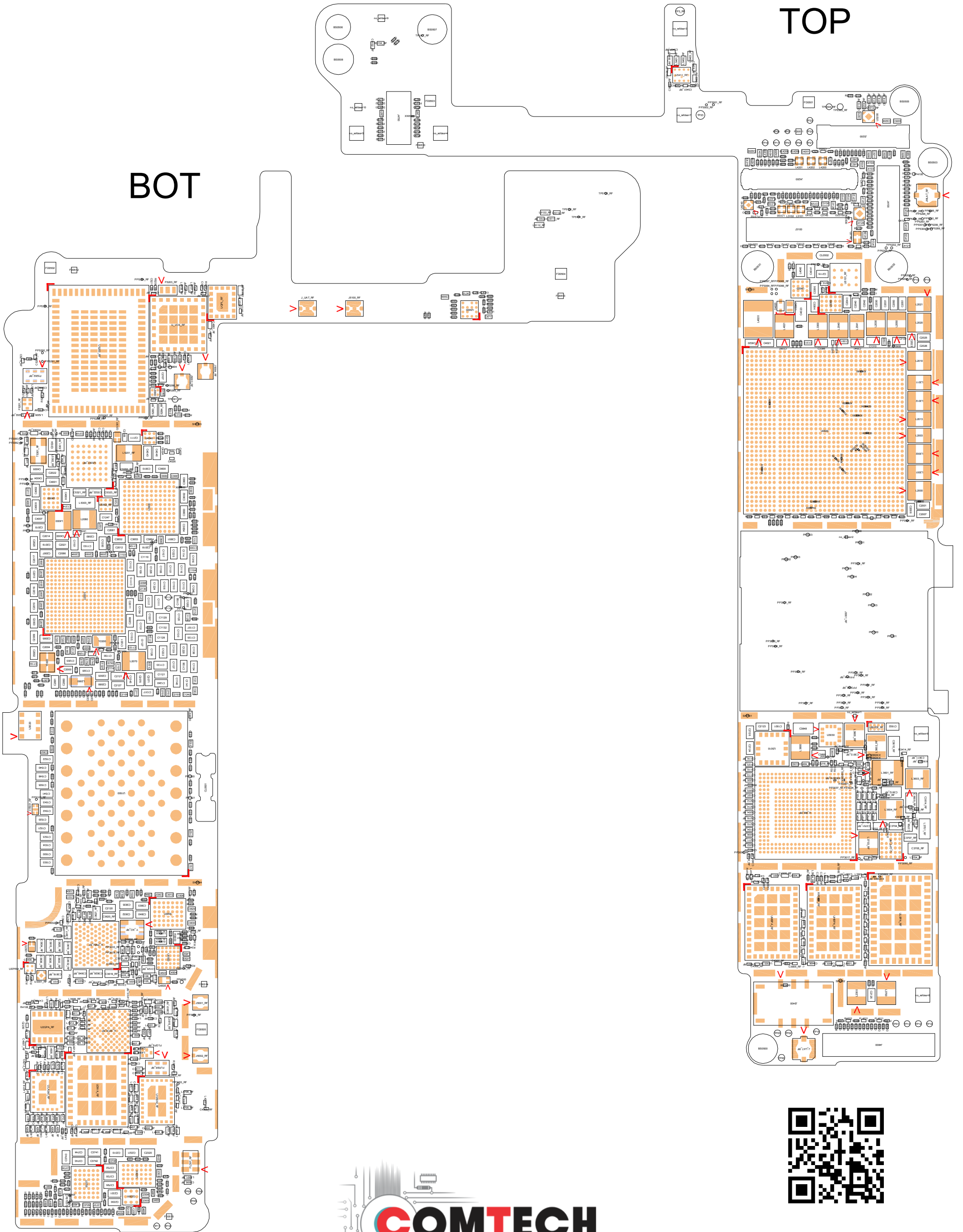


N71位置图



1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	0004536627	PRODUCTION RELEASED		2015-07-21

N71 MLB - PVT OK2FAB

LAST_MODIFICATION= Tue Jul 21 11:39:02 2015

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22	35	CAMERA:STROBE DRIVER		
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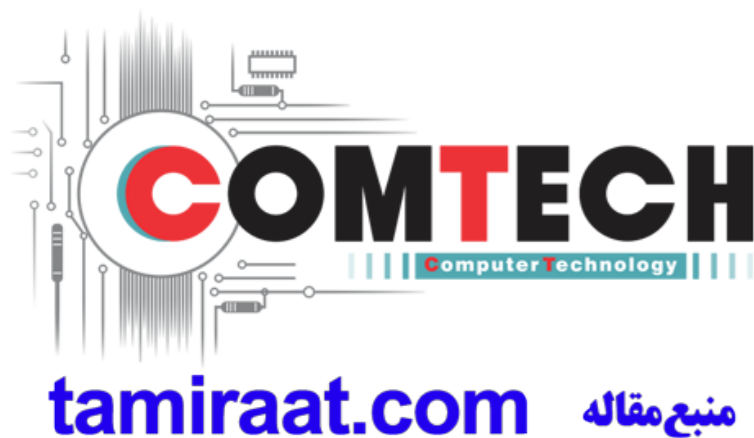
PAGE	<CSA>	CONTENTS	SYNC	DATE
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36		FE: ANT CONNECTORS AND UAT TUNER		
37		WLAN LAT 2.4GHZ BAW BPF		
38		DEBUG CONN & TEST POINTS		
		CELLULAR BASEBAND: POWER1		
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44		CELLULAR PMU: SWITCHERS AND LDOS		
		CELLULAR PMU: ET MODULATOR		
46		CELLULAR TRANSCEIVER: POWER		
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48		CELLULAR TRANSCEIVER: DRX/GPS PORTS		
49		CELLULAR TRANSCEIVER: TX PORTS		
50		CELLULAR FRONT END: LB PAD		
51		CELLULAR FRONT END: MB PAD		
52		CELLULAR FRONT END: HB PAD		
53		CELLULAR FRONT END: 2G PA		
54		CELLULAR FRONT END: LB ASM		
55		CELLULAR FRONT END: MB-HB ASM		
56		CELLULAR FRONT END: DIVERSITY		
57		SIM		
58		WIFI/BT: WIFI/BT MODULE		
59		STOCKHOLM		

TABLE

- SCH 051-1902
- BRD 820-5507
- MCO 056-01060
- BOM 639-00263 (BETTER, DB30)
- BOM 639-00265 (ULTRA, DB30)
- BOM 639-00266 (SUPREME, DB30)
- BOM 639-01056 (BETTER, B30)
- BOM 639-01057 (ULTRA, B30)
- BOM 639-01058 (SUPREME, B30)
- BOM 639-01098 (BETTER, DB30C)
- BOM 639-01100 (ULTRA, DB30C)
- BOM 639-01099 (SUPREME, DB30C)
- BOM 939-01627 (BETTER, DARWIN)

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DRAWING TITLE		SCHEM, SINGLE, BRD, N71	
Apple Inc.	DRAWING NUMBER	051-1902	SIZE
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SCHEMATIC & PCB BOM CALLOUTS

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists components like SCH_SINGLE_BRD_N71, PCB_SINGLE_BRD_N71, and various EEEE CODE FOR parts.

S3E NAND BOM OPTIONS

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Lists NAND options like NAND_16G, NAND_64G, and NAND_128G.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists NAND options with part numbers like 335S00074, 335S00078, etc.

CARBON/ACCEL BOM OPTIONS

Table with 6 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Lists carbon/accel options like IC_ACCEL_3-AXIS_DIG, RES_MF_20 OHM, and IC_CARBON MPU.

ALTERNATE BOM OPTIONS

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists alternate options for various components like C0610, C0610, FL3100, etc.

NOT ALL REFERENCE DESIGNATORS LISTED. USED ~116 TIMES IN DESIGN. USED ~116 TIMES IN DESIGN. USED ~7 TIMES IN DESIGN. USED ~63 TIMES IN DESIGN. USED ~63 TIMES IN DESIGN. USED ~3 TIMES IN DESIGN. USED ~19 TIMES IN DESIGN. USED ~61 TIMES IN DESIGN. USED ~9 TIMES IN DESIGN. USED ~8 TIMES IN DESIGN. USED ~11 TIMES IN DESIGN. USED ~17 TIMES IN DESIGN. USED ~17 TIMES IN DESIGN. USED ~12 TIMES IN DESIGN. USED ~2 TIMES IN DESIGN. USED ~9 TIMES IN DESIGN. USED ~7 TIMES IN DESIGN. USED ~4 TIMES IN DESIGN.

POWER INDUCTOR ALTERNATES

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists power inductor alternates like L2070, L3700.

ACTIVE DIODE ALTERNATE

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists active diode alternate like Q2300.

SHIELD PART NUMBERS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Lists shield part numbers like SH0500, SH0501, SH0502, SH0503, SH0504.

SOC/PMU SUB BOMS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Lists SOC/PMU sub boms like SUBBOM_SINGLE_BRD_MAU1_N71, IC_PMU_ARMADA, RES_MF_100 OHM, etc.

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Lists SOC/PMU sub boms like IC_PMU_ARMADA, RES_MF_3.0KOHM, CAP_CER_NPO, etc.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists SOC/PMU sub boms with part numbers like 685-00070, 685-00069.

SOC ALTERNATES

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists SOC alternates like 339S00113, 339S00114.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists SOC alternates like 339S00125, 339S00126, 339S00127, 339S00128, 339S00129.

INDUCTOR SUB BOMS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Lists inductor sub boms like SUBBOM_SINGLE_BRD_CYNTEC_N71, IHD_PWR_SHLD, etc.

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Lists inductor sub boms like IHD_PWR_SHLD, IHD_PWR_SHLD, etc.

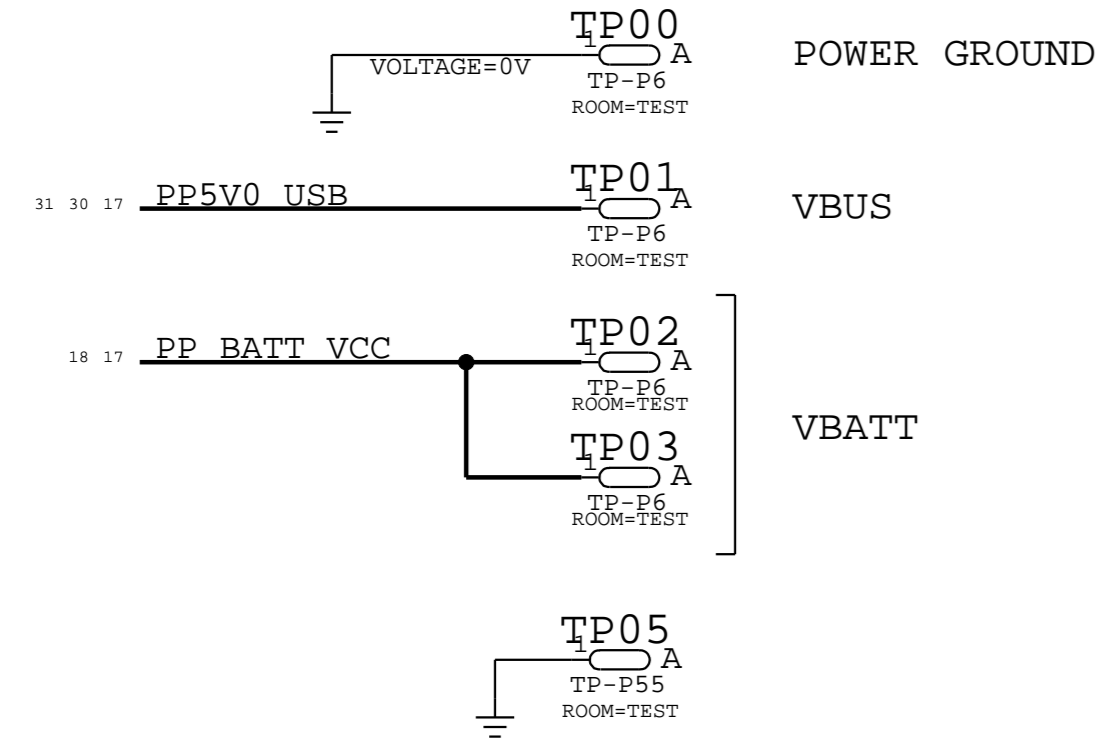
Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Lists inductor sub boms with part numbers like 685-00080, 685-00081.



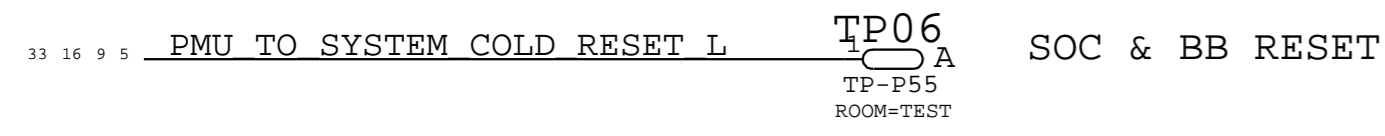
Page title: SYSTEM:BOM TABLES. Apple Inc. logo. Drawing number: 051-1902. Revision: A.0.0. Page: 3 OF 49. Sheet: 2 OF 59. NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED

TESTPOINTS

POWER



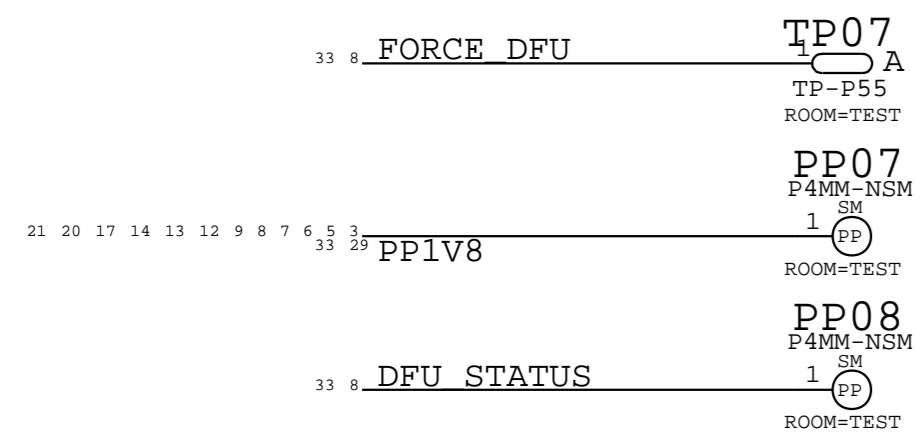
RESET



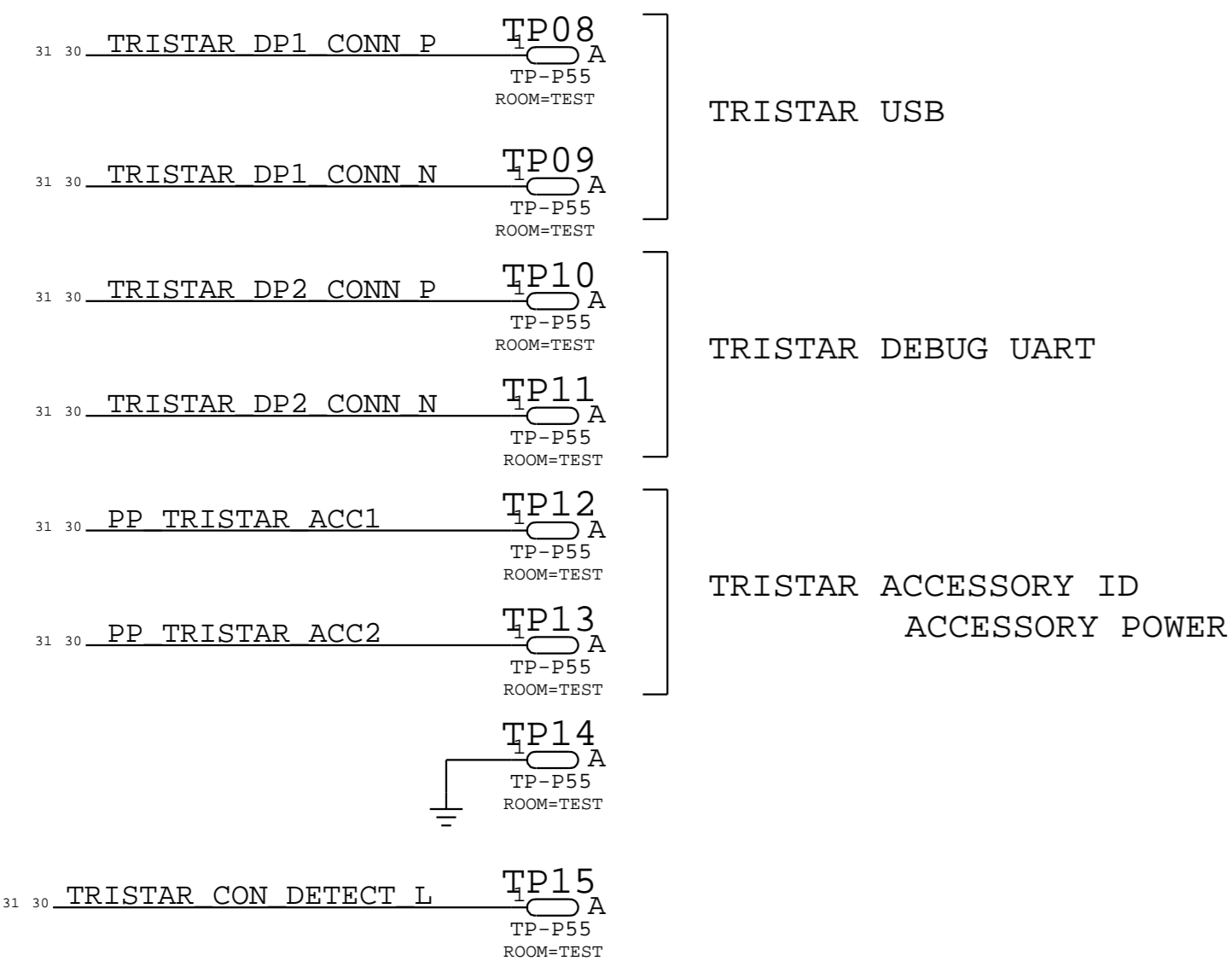
DFU

FORCE DFU PROCEDURE:

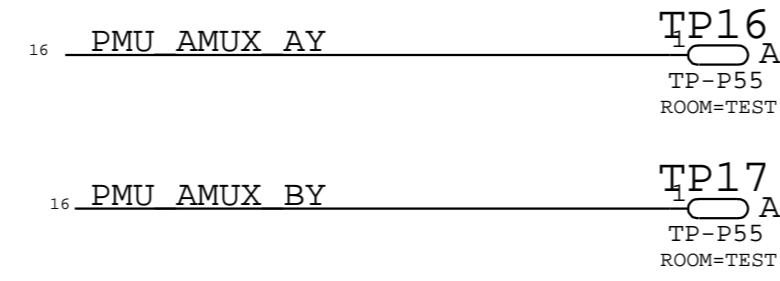
1. FROM OFF MODE SHORT TP07 TO PP07
2. PLUG IN E75 CABLE TO FORCE DFU



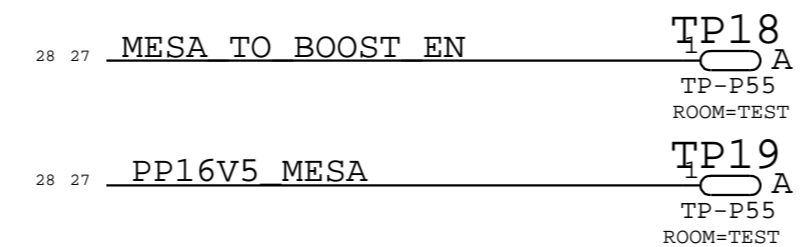
E75



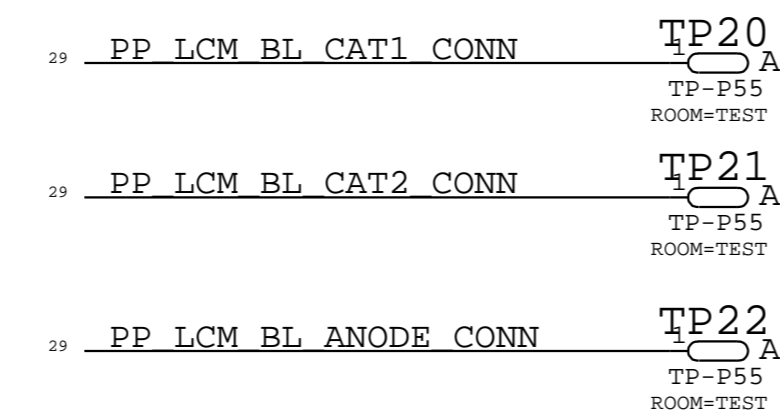
AMUX



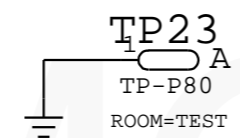
MOJAVE



LCM



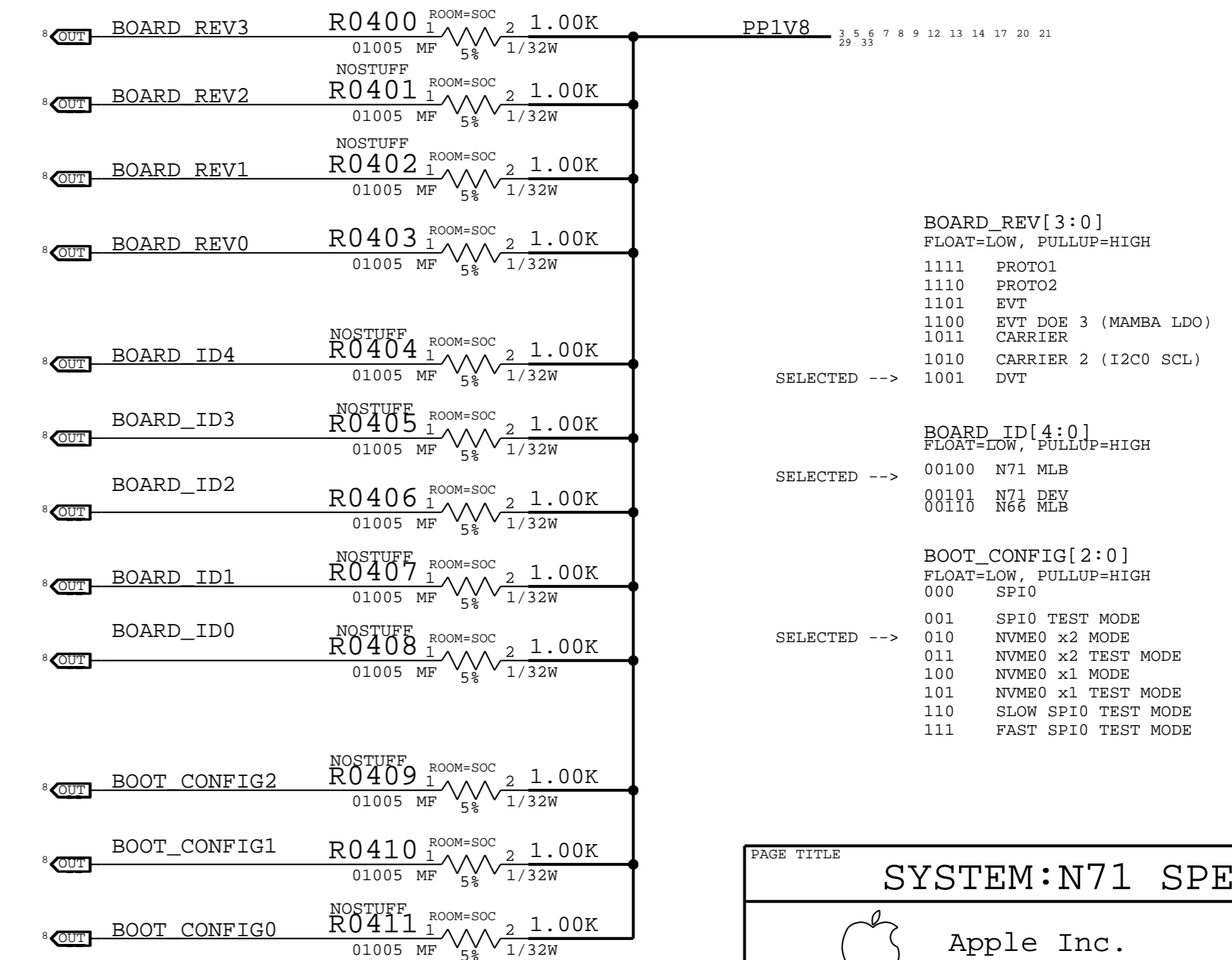
SUPER SCREW



N71 I2C DEVICE MAP

I2C BUS	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
I2C0	ANTIGUA PMU	1110100X	0X74	0XE8
	CHESTNUT	0100111X	0X27	0X4E
	MUON	1100010X	0X62	0XC4
I2C1	TIGRIS	1110101X	0X75	0XE8
	ARC DRIVER	1000001X	0X41	0X82
	SPEAKER AMP	1000000X	0X40	0X80
	TRISTAR	0011010X	0X1A	0X34
I2C2	ALS	0101001X	0X29	0X52
	DISP EEPROM	1010001X	0X51	0XA2
OWL	UNUSED	N/A	N/A	N/A
ISP I2C0	REAR CAM	TBD	TBD	TBD
	LED DRIVER	1100011X	0X63	0XC6
ISP I2C1	FRONT CAM	0010000X	0X10	0X20
TOUCH I2C	MESON	1000000X	0x40	0x80
	MAMBA	1100000X	0x60	0xC0
	DOPPLER	1011000X	0x58	0xB0
SEP I2C	SEP EEPROM	1010001X	0x51	0xA2

BOOTSTRAPPING: BOARD REV BOARD ID BOOT CONFIG

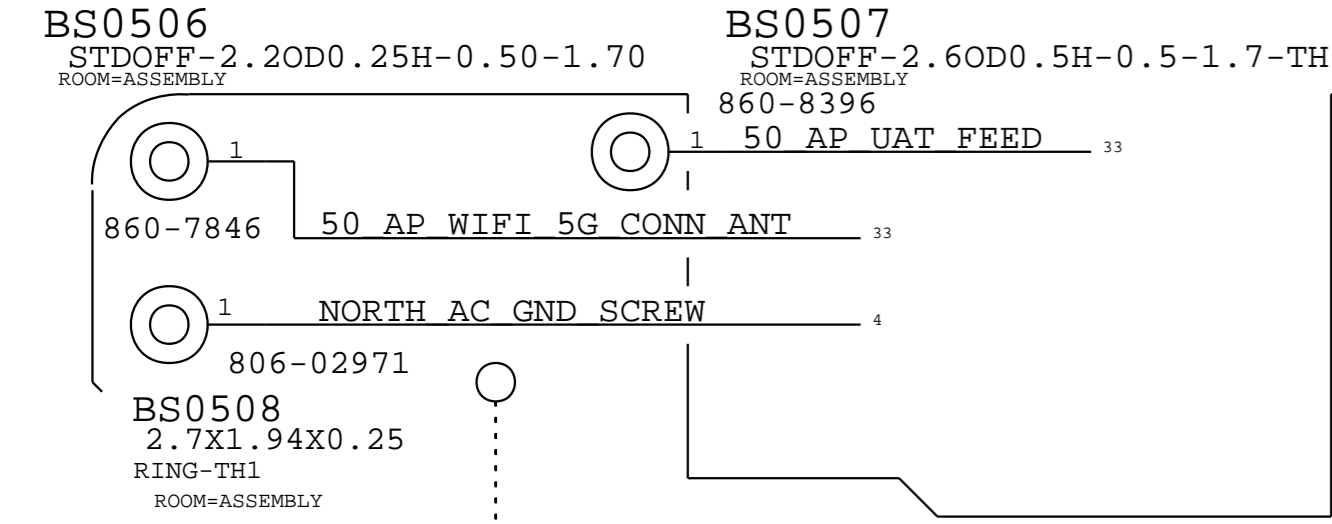


RESISTOR STUFF = HIGH '1'
RESISTOR NOSTUFF = LOW '0'



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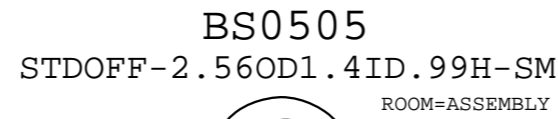
PENINSULA STANDOFFS



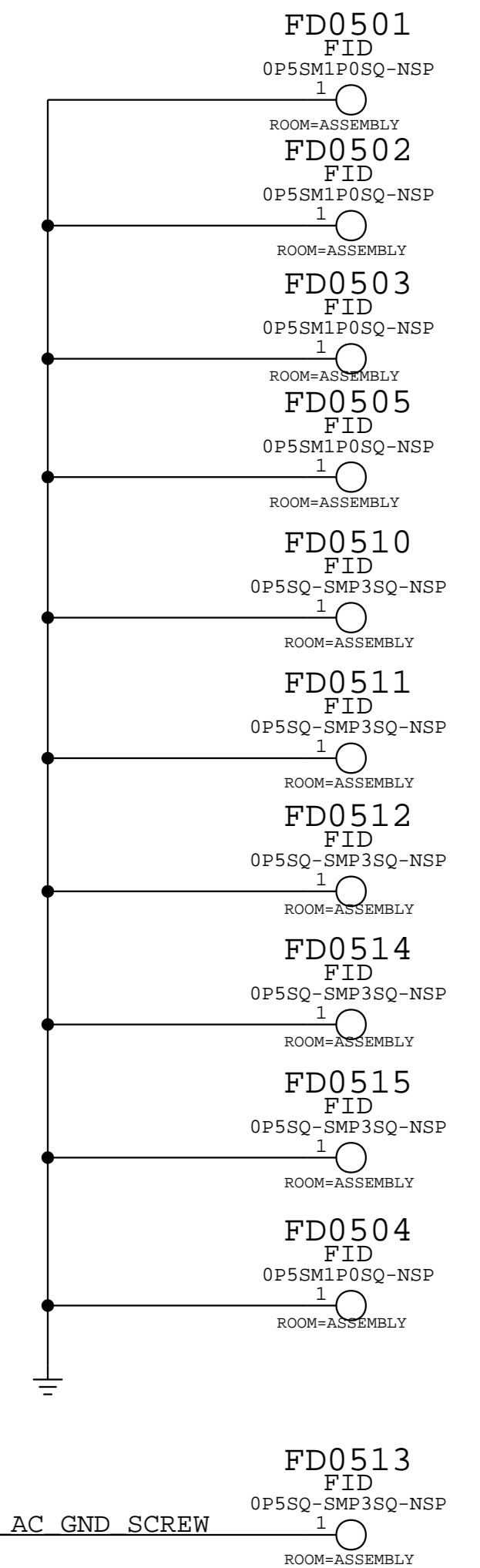
TOP-SIDE

BOTTOM-SIDE

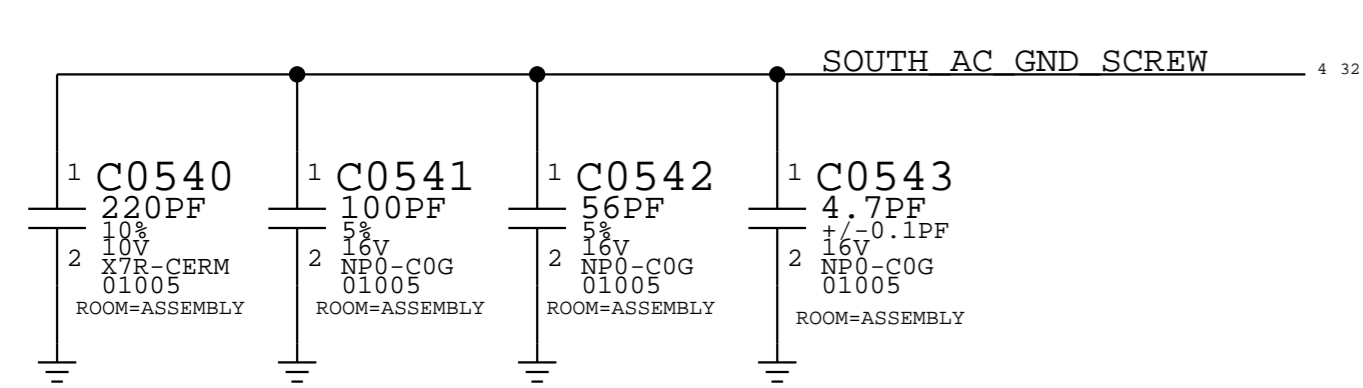
STOCKHOLM FEED



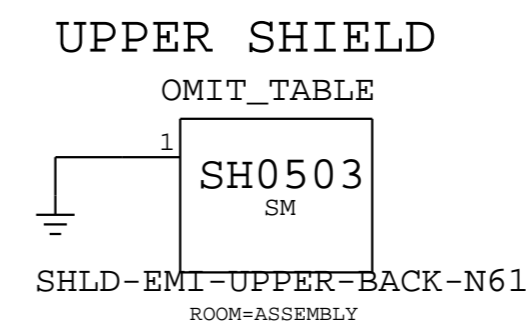
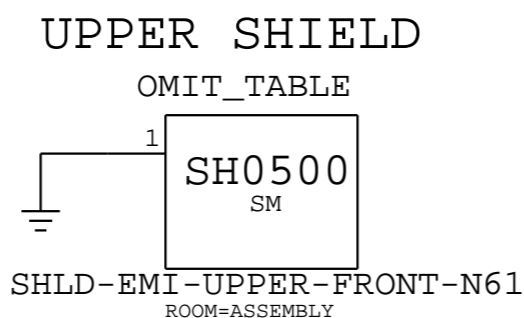
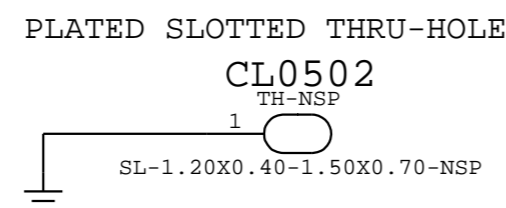
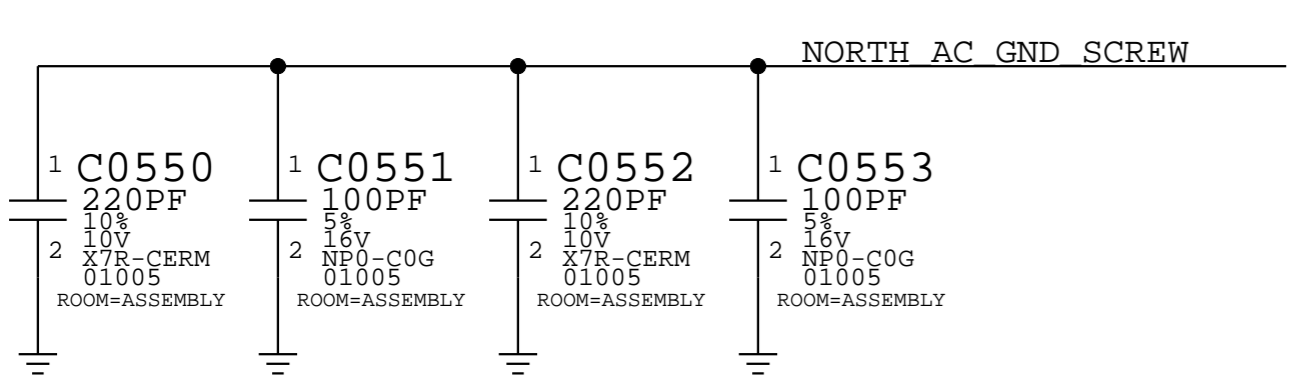
FIDUCIALS



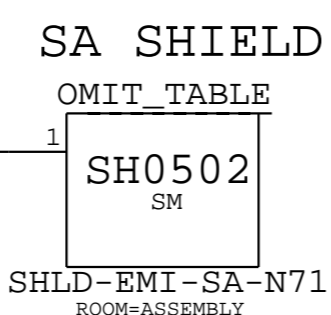
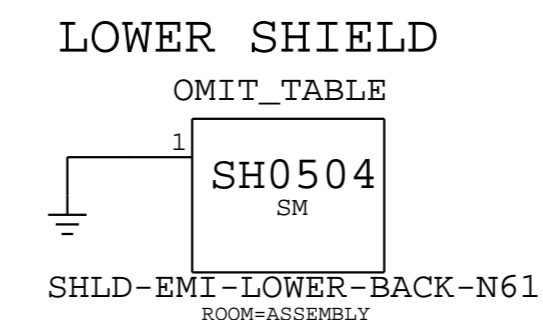
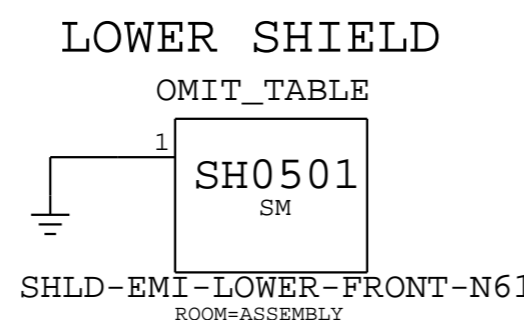
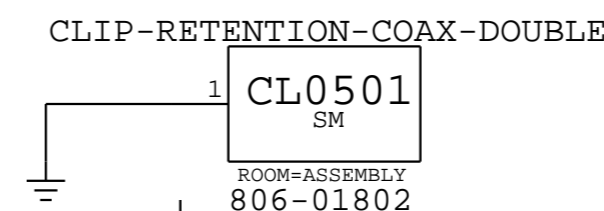
SOUTH DC CURRENT BLOCKING CAPS



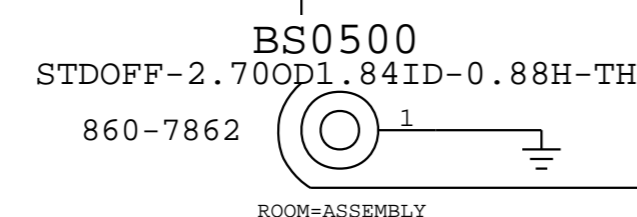
NORTH DC CURRENT BLOCKING CAPS



DUAL RF COAX CLIP



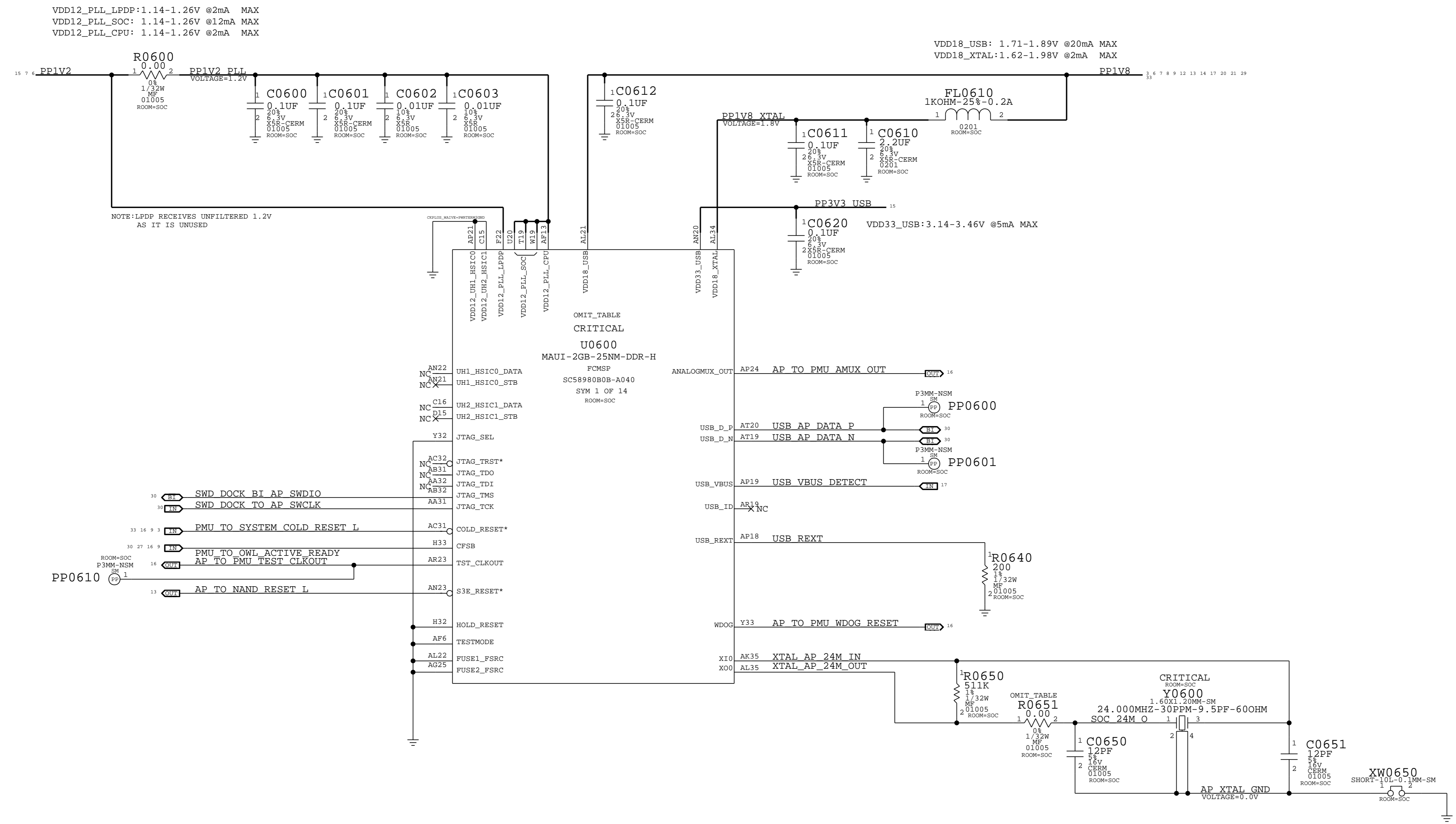
SOUTH TUBE STANDOFF



TODO:UPDATE REF DES

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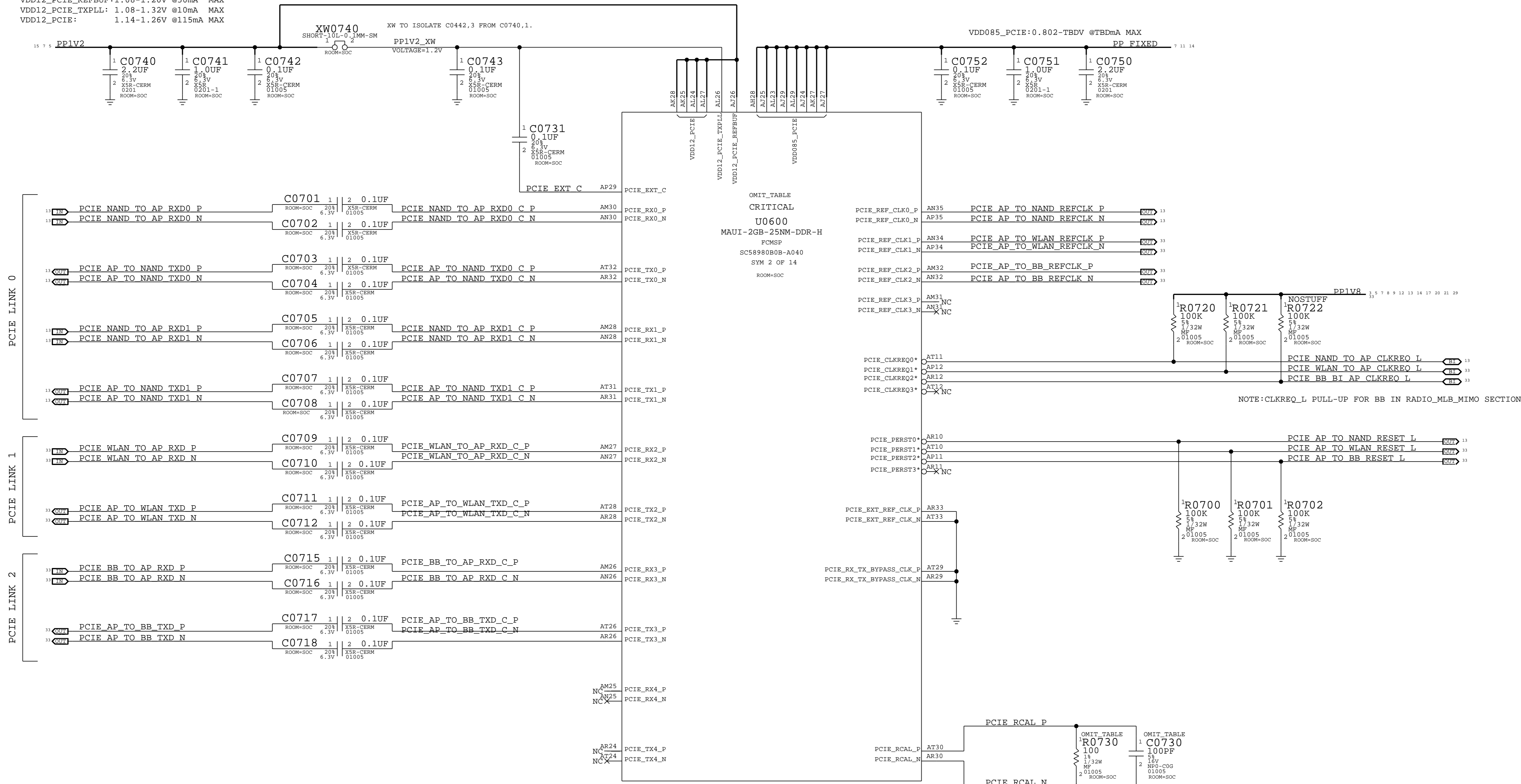
MAUI - USB, JTAG, XTAL



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PAGE TITLE SOC : JTAG , USB , XTAL			
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	051-1902	D	
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	BRANCH		
	PAGE	6 OF 49	
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MAUI - PCIE INTERFACES

VDD12_PCIE_REFBUF:1.08-1.26V @50mA MAX
 VDD12_PCIE_TXPLL: 1.08-1.32V @10mA MAX
 VDD12_PCIE: 1.14-1.26V @115mA MAX



MAUI - CAMERA & DISPLAY INTERFACES

8

7

6

5

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3

2

1

D

D

C

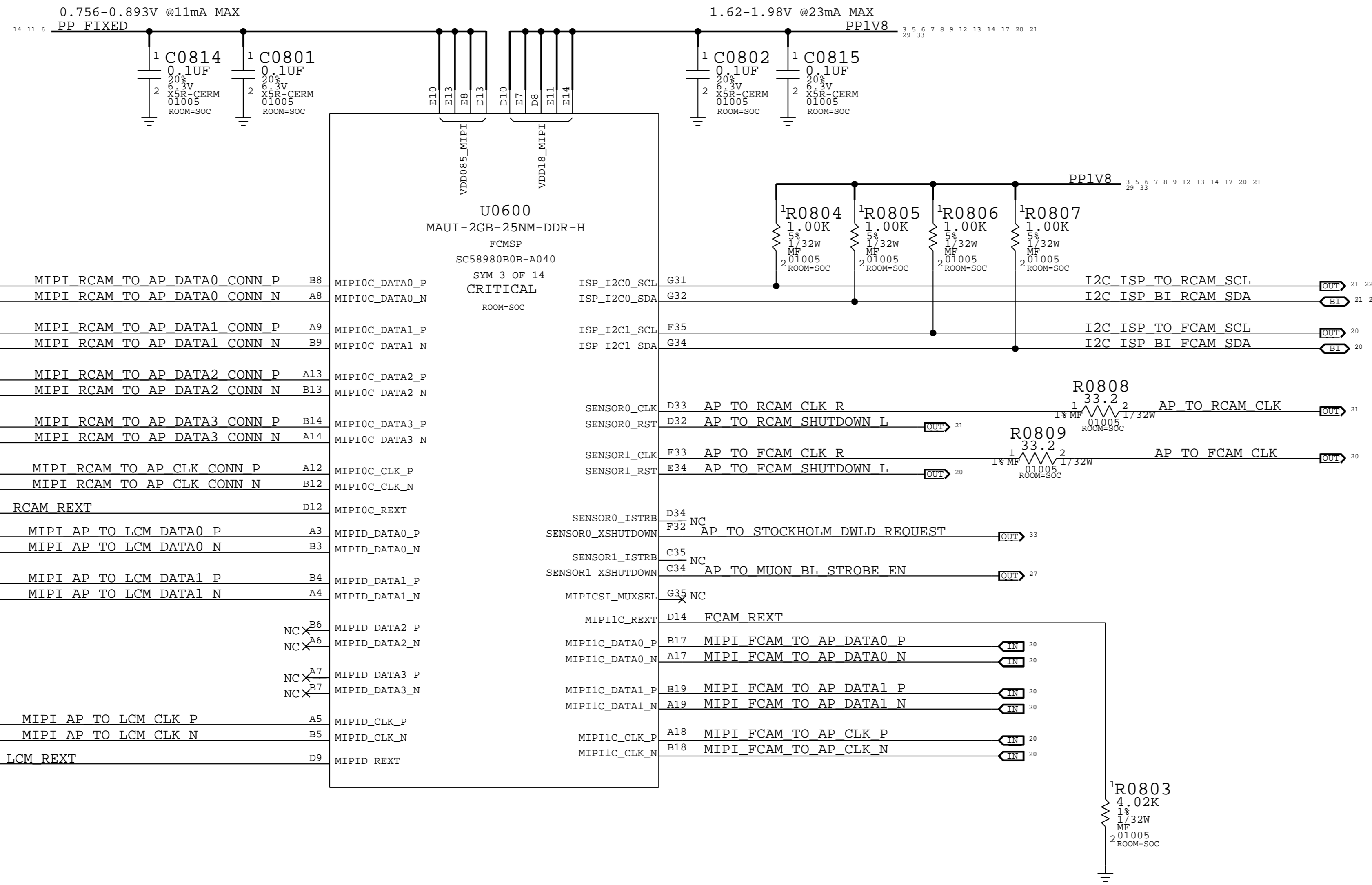
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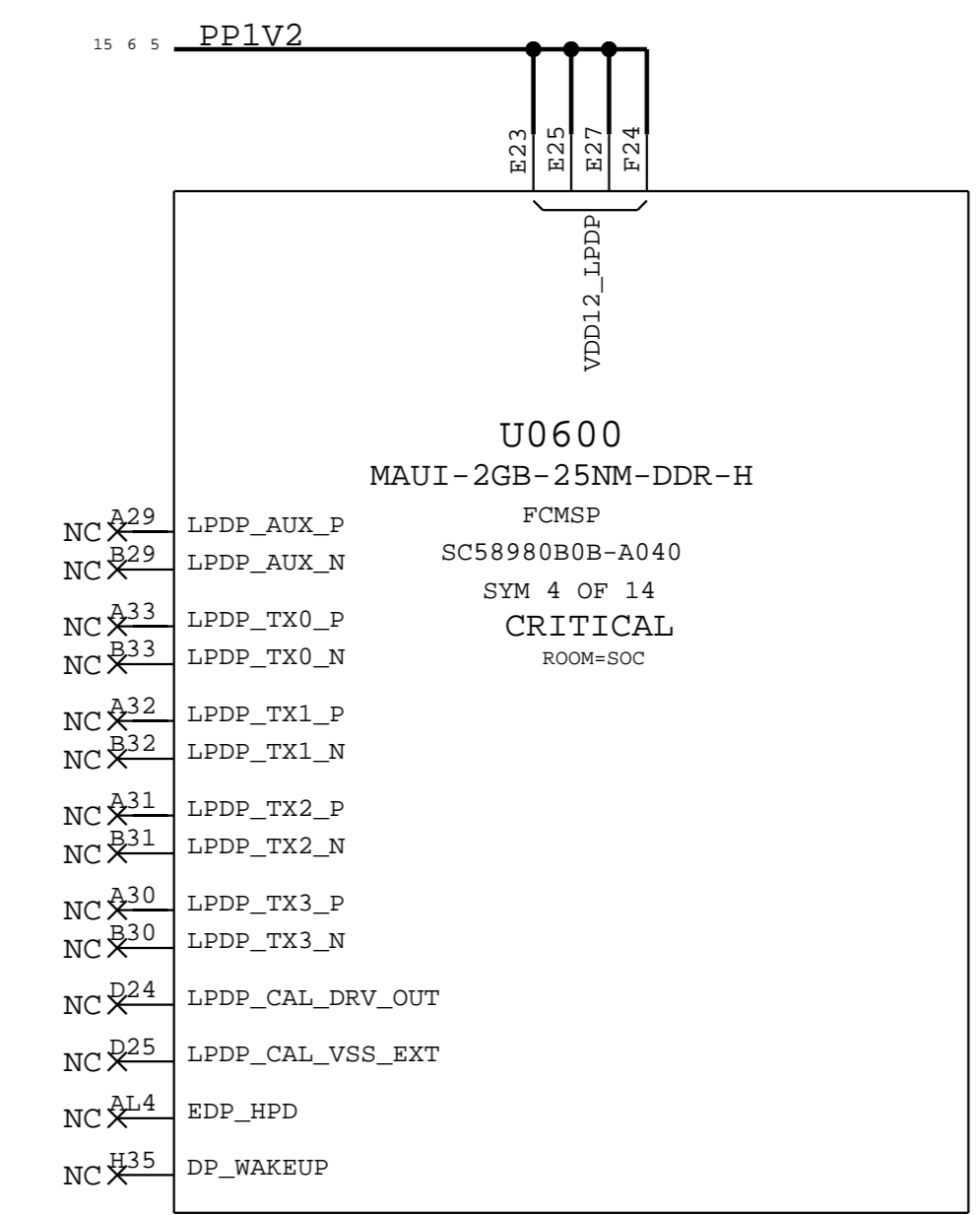
B

A

A



NOTE:VDD12_LPDP SHOULD BE POWERED
EVEN WHEN LPDP IS NOT USED



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PAGE TITLE			
SOC:CAMERA & DISPLAY			
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8

7

6

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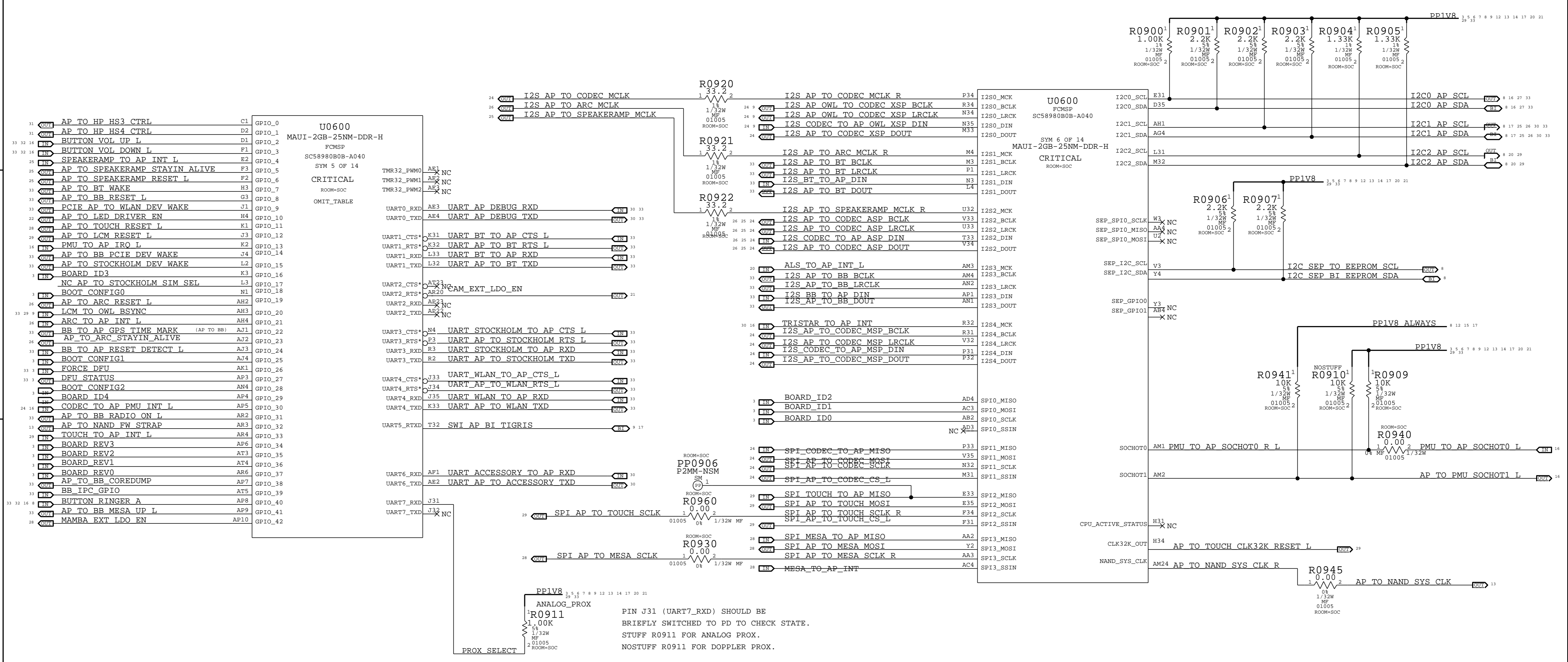
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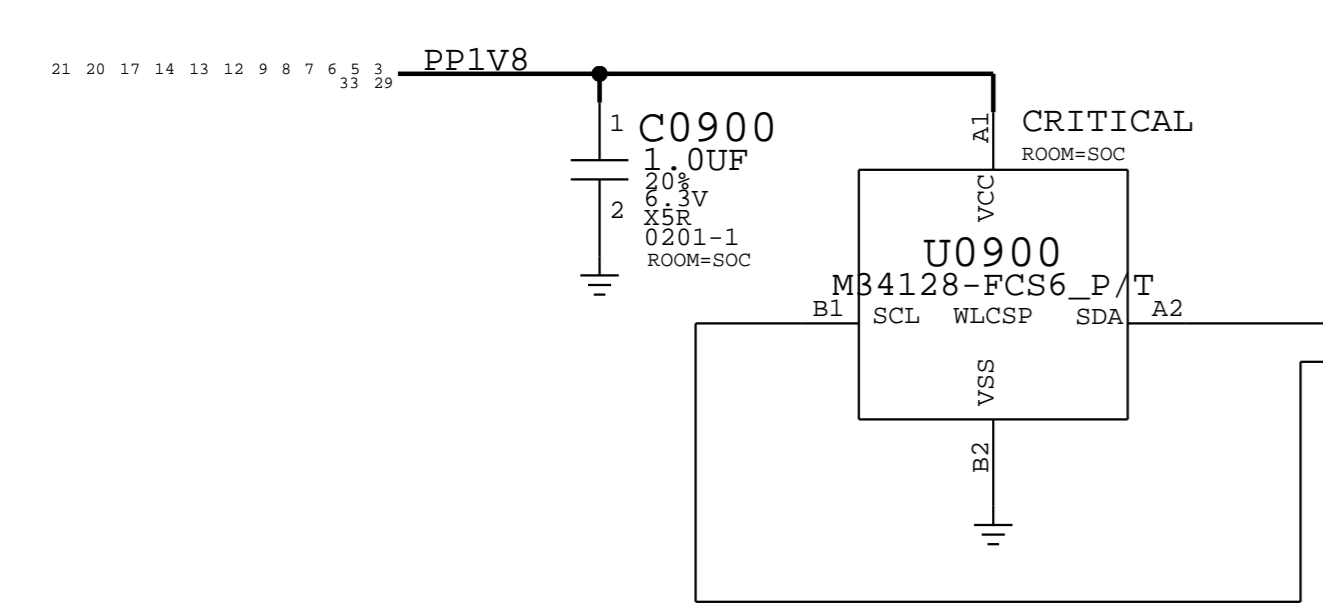
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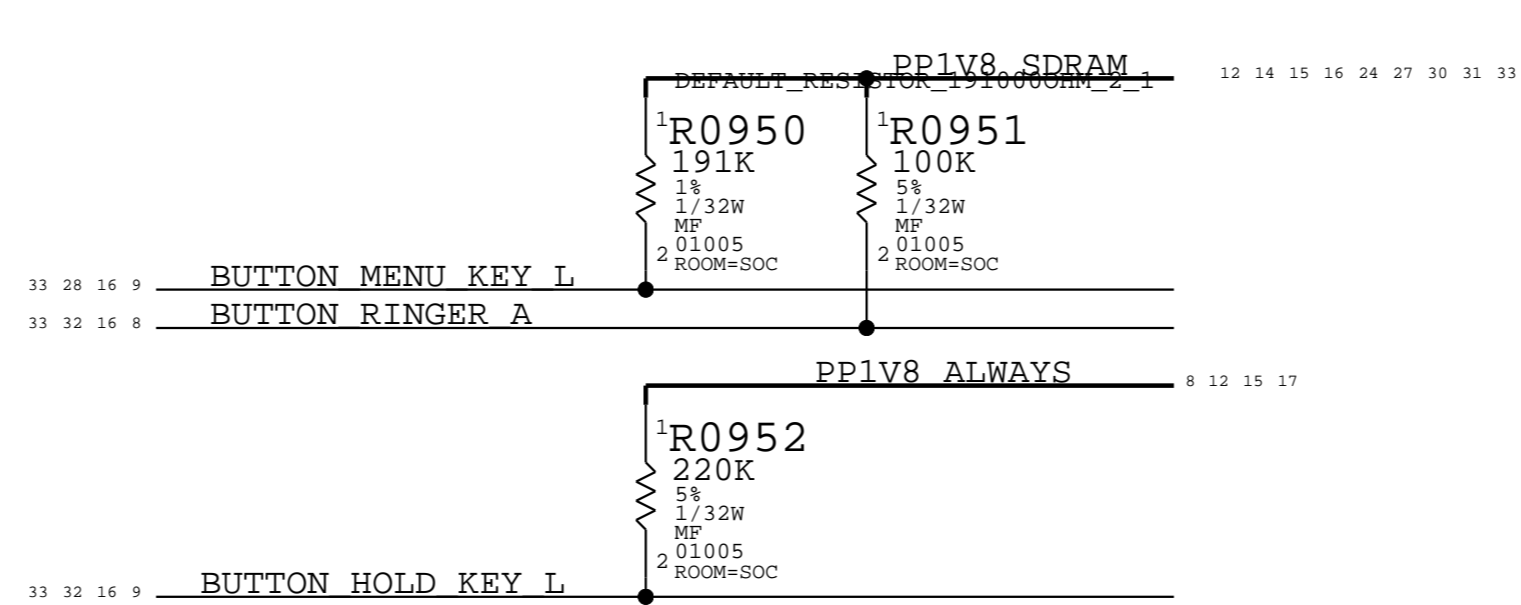
MAUI - GPIO & SERIAL INTERFACES



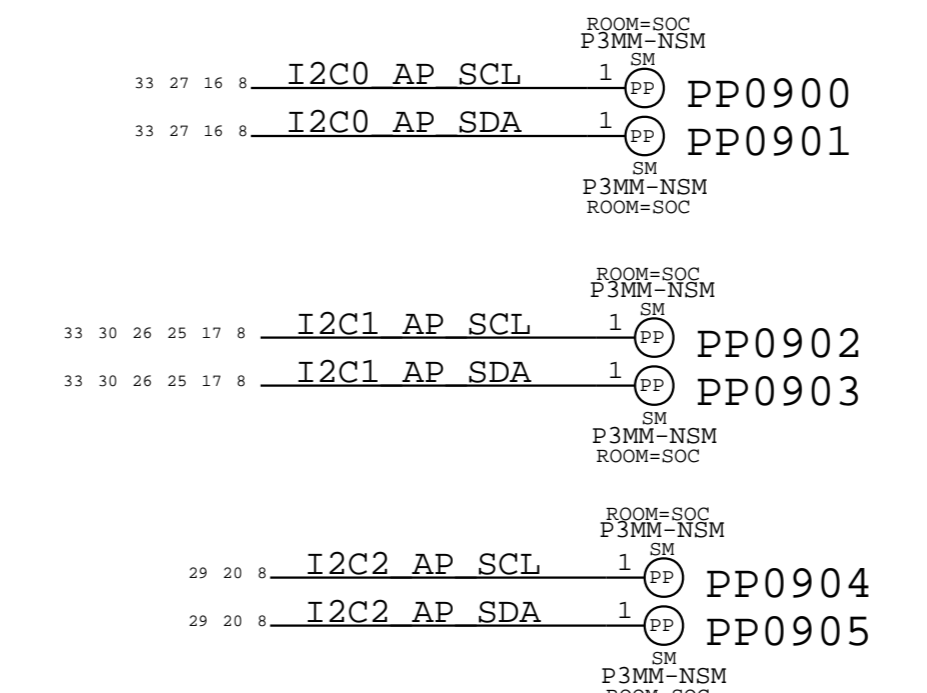
ANTI-ROLLBACK EEPROM
128kbit
APN: 335S0946



BUTTON PULL-UP RESISTORS

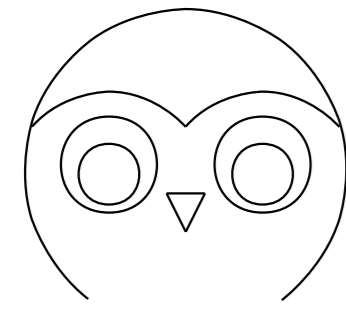


I2C PROBE POINTS

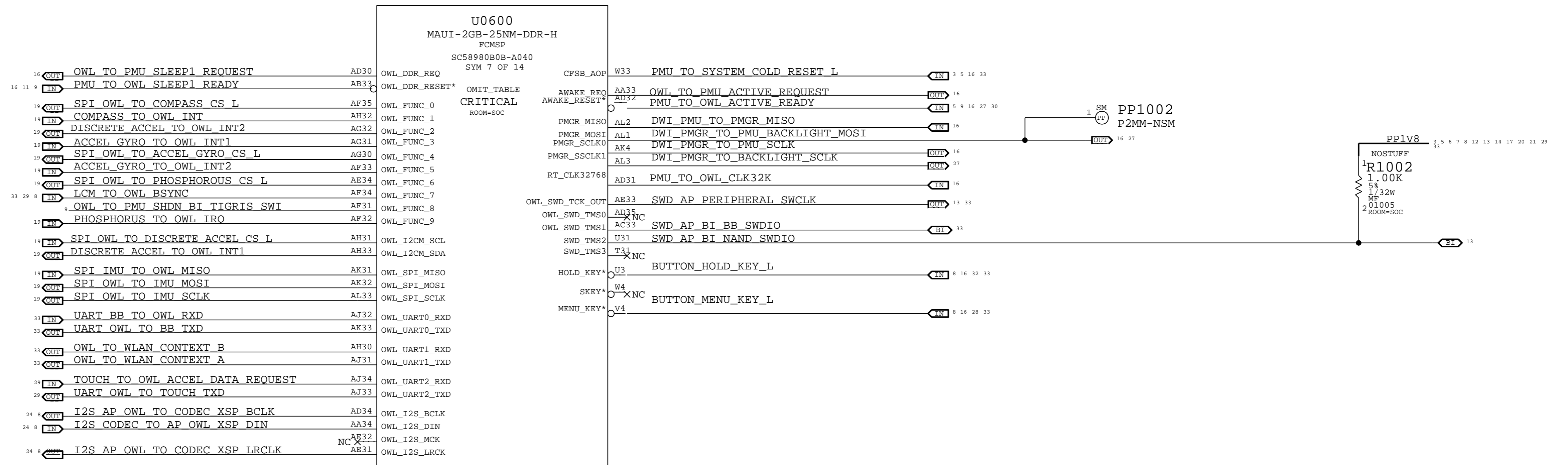
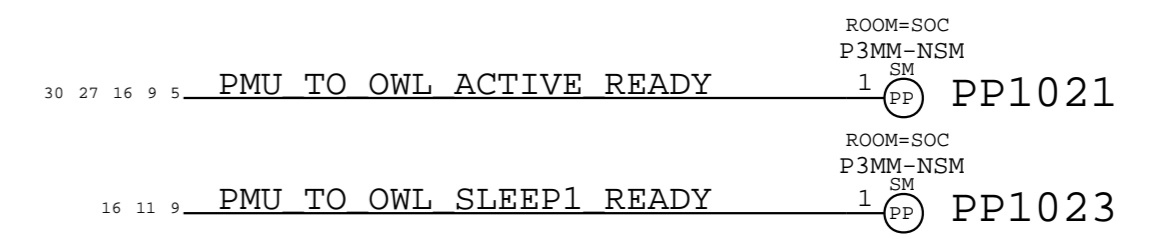


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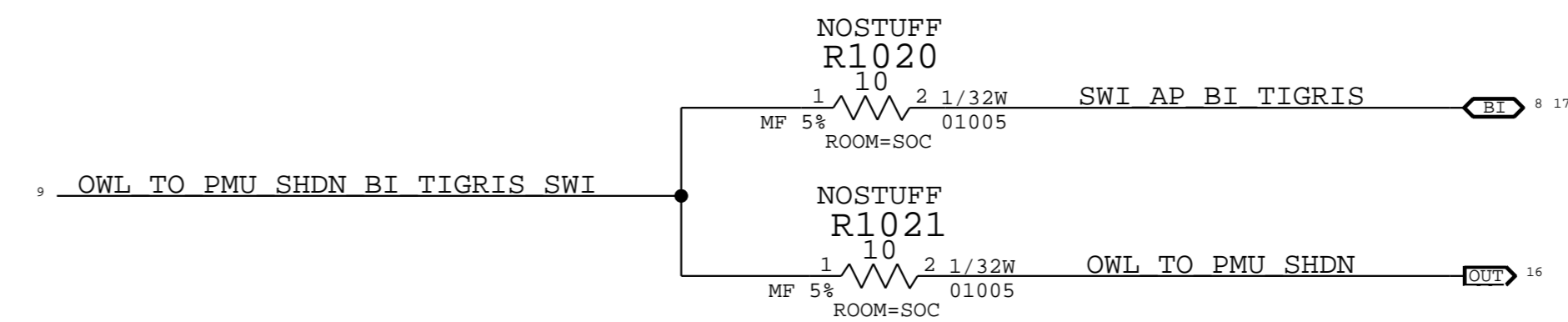
MAUI - OWL



POWER STATE CONTROL PROBE POINTS

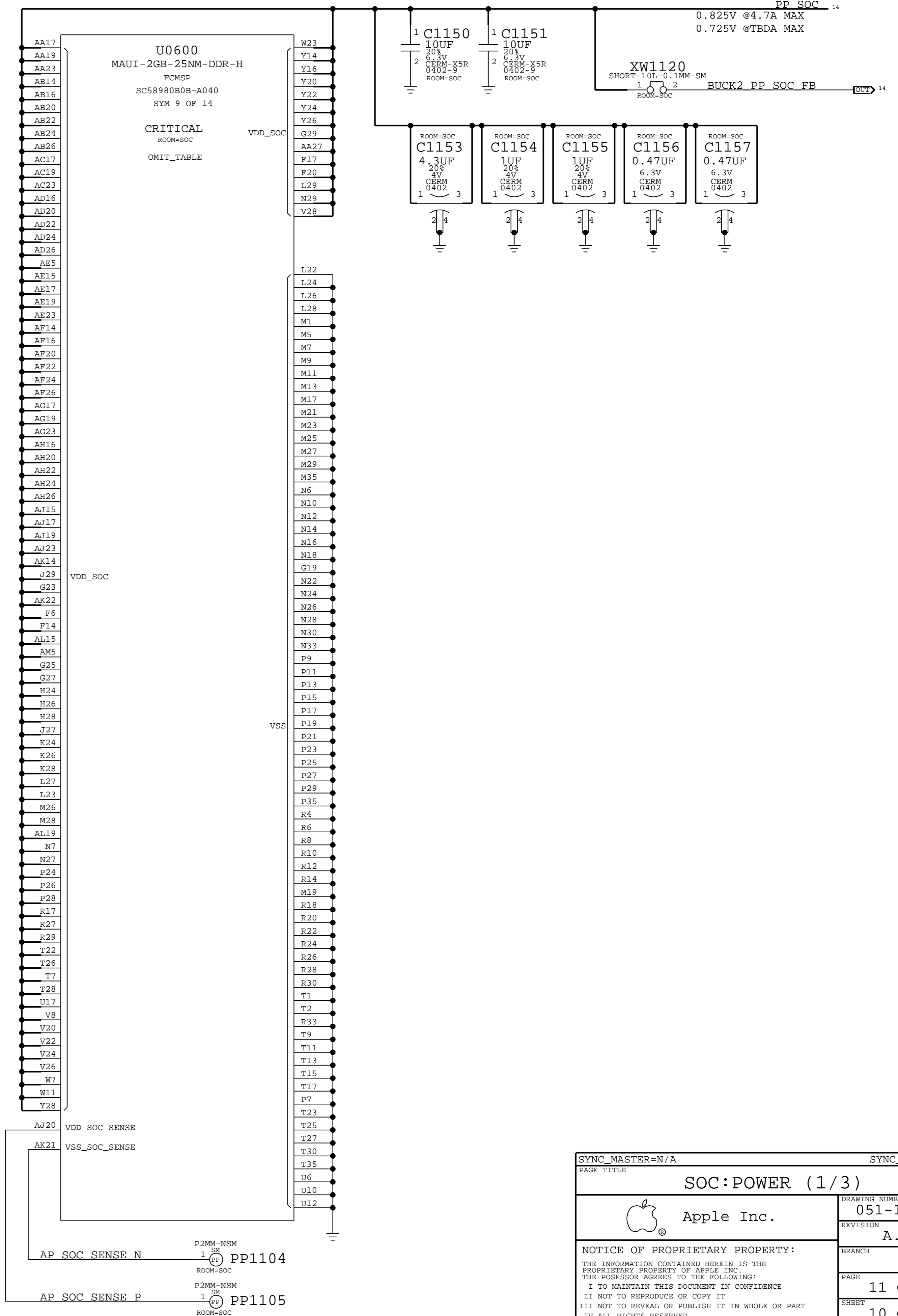
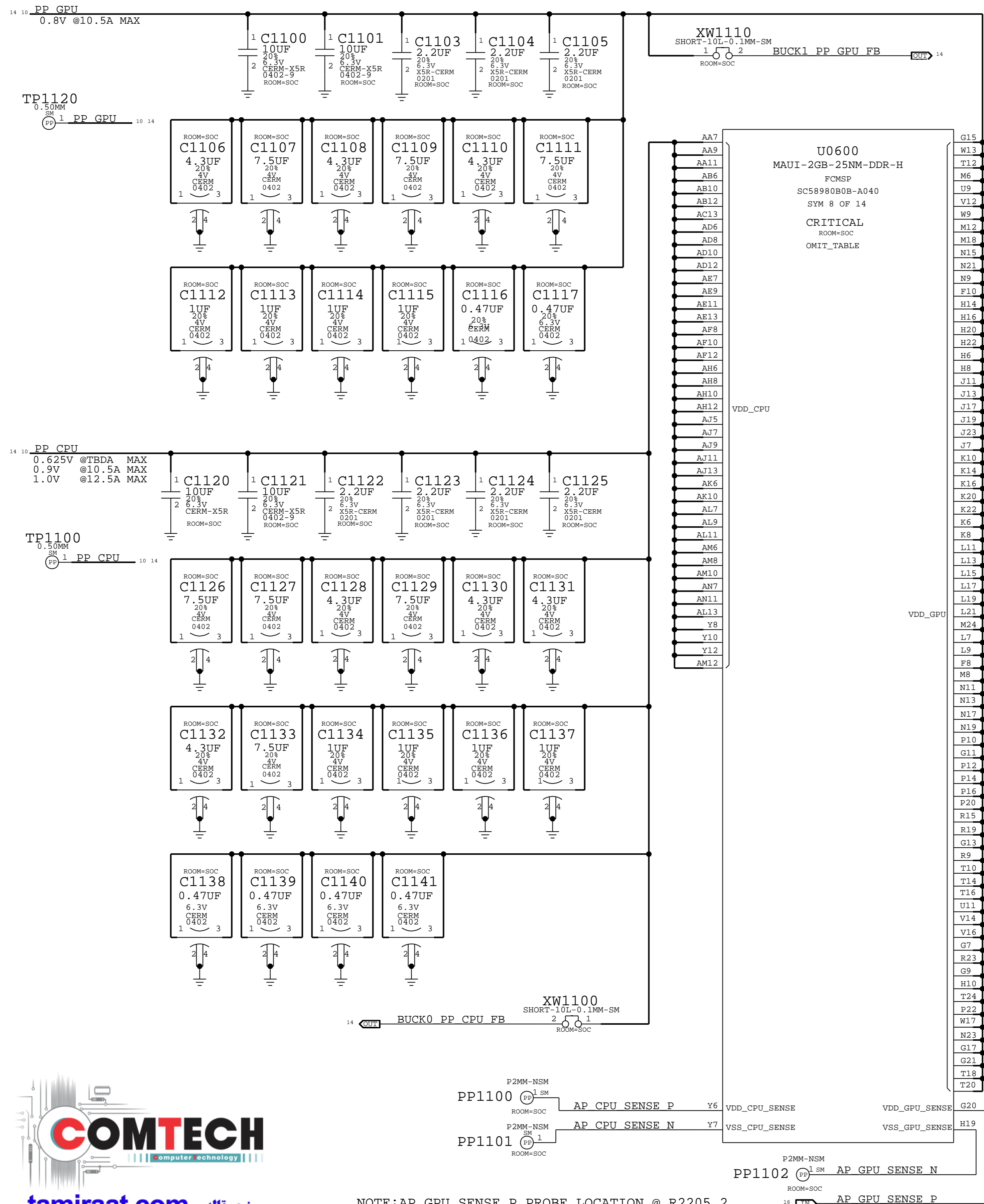


OWL SYSTEM SHUTDOWN OPTION



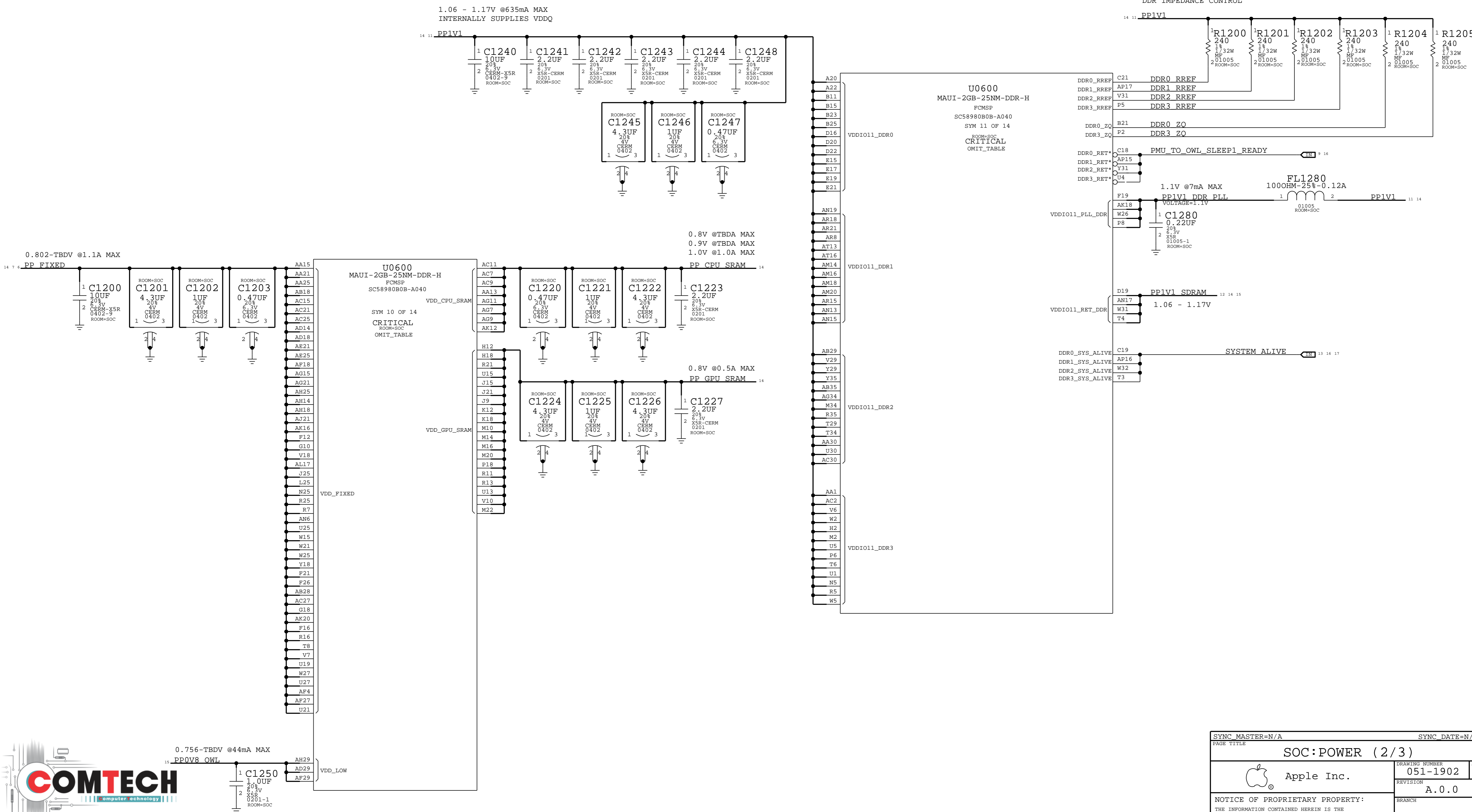
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		PAGE	10 OF 49
		SHEET	9 OF 59

MAUI - CPU, GPU & SOC RAILS



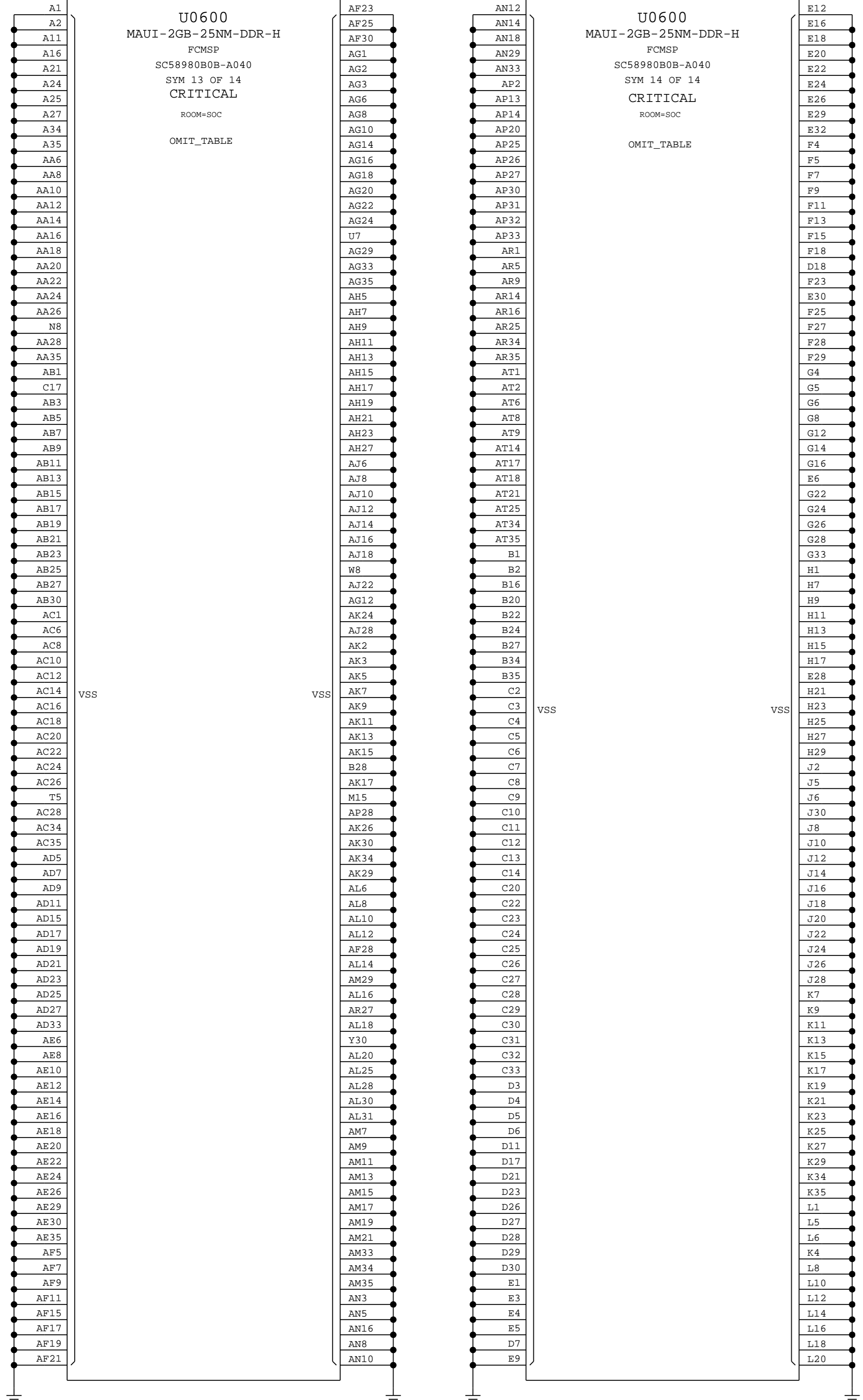
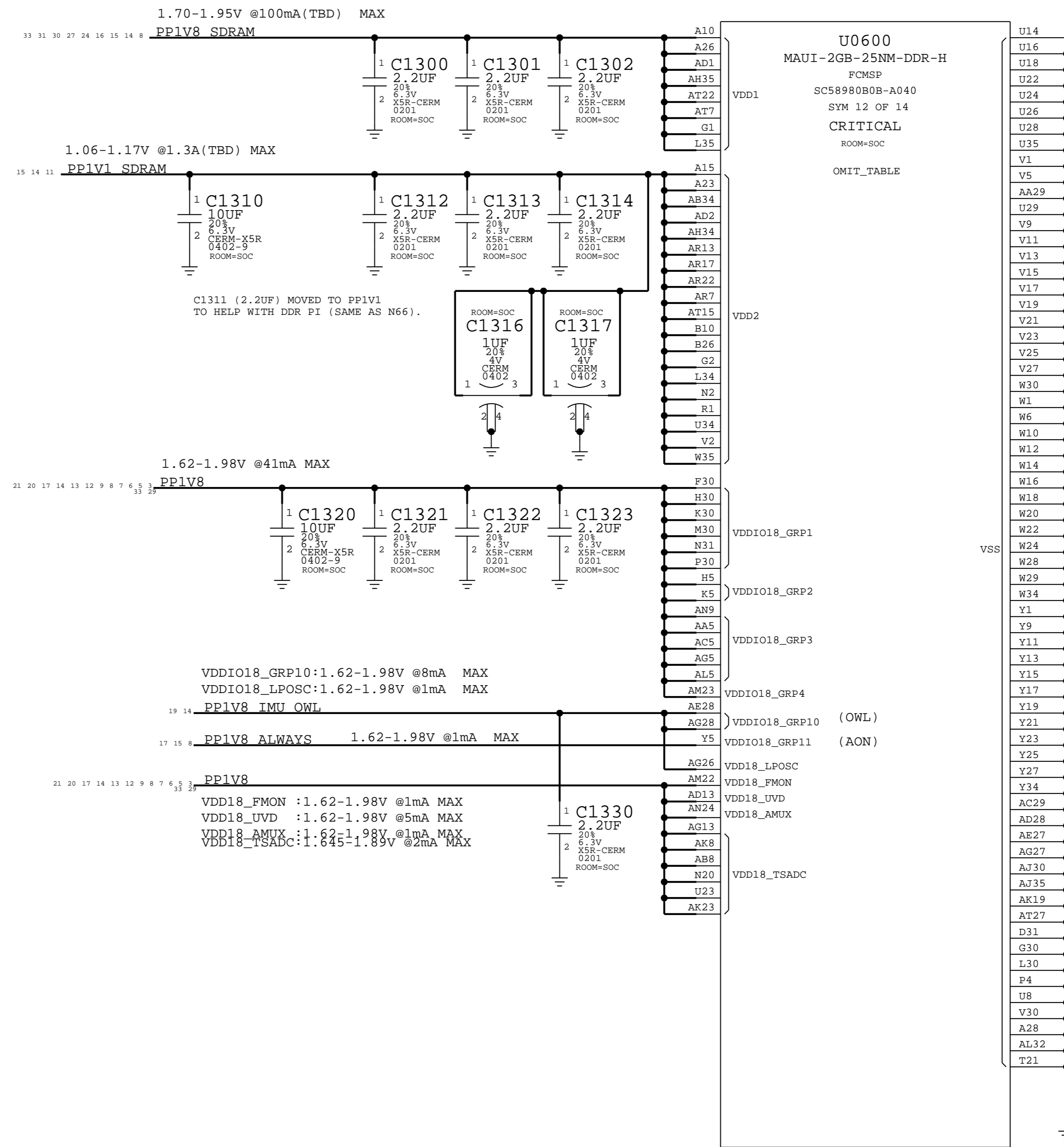
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MAUI - POWER SUPPLIES



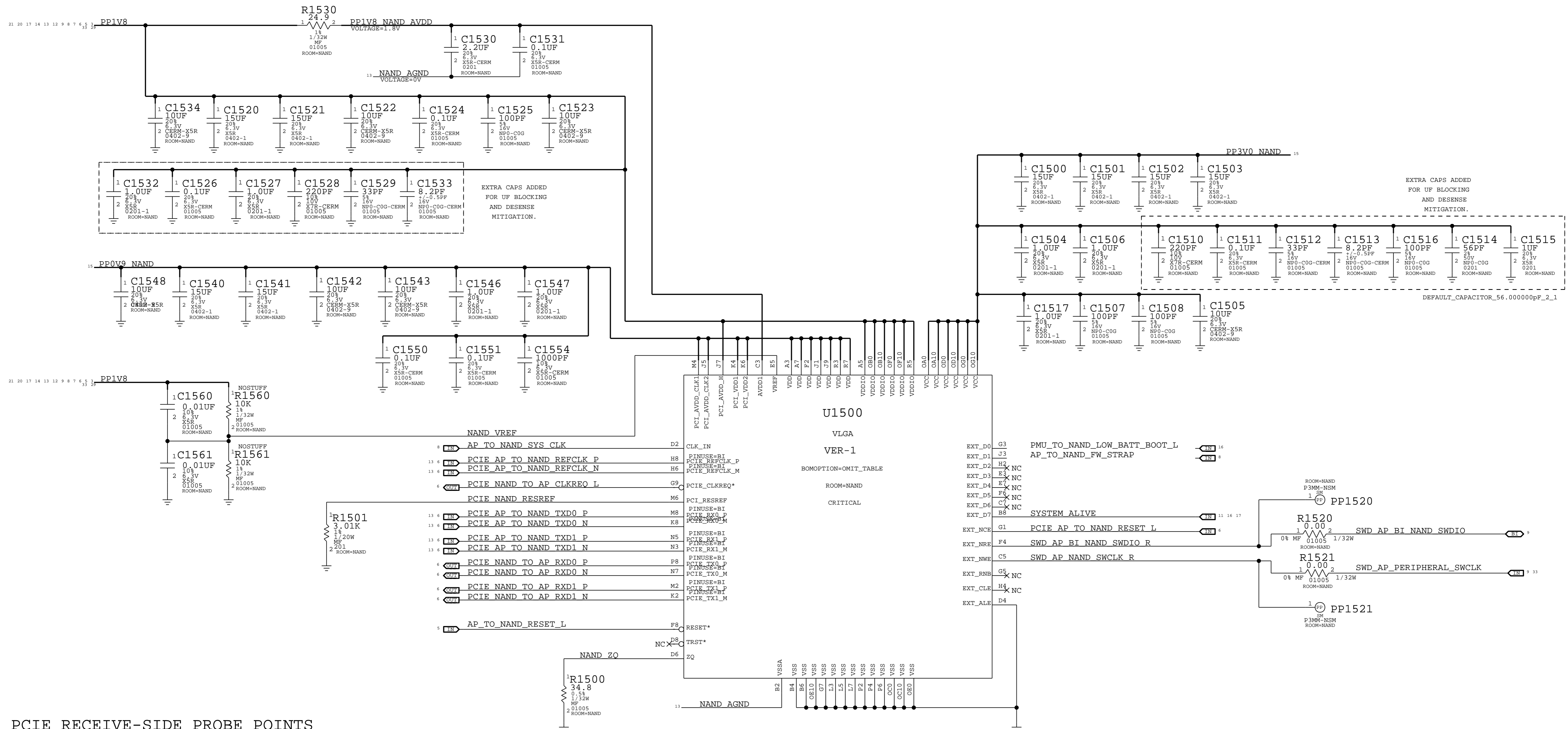
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MAUI - POWER SUPPLIES



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S3E NAND



PCIE RECEIVE-SIDE PROBE POINTS

Signal Name	Probe Point
PCIE AP TO NAND REFCLK P	PP1500
PCIE AP TO NAND REFCLK N	PP1501
PCIE AP TO NAND TXD0 P	PP1502
PCIE AP TO NAND TXD0 N	PP1503
PCIE AP TO NAND TXD1 P	PP1504
PCIE AP TO NAND TXD1 N	PP1505

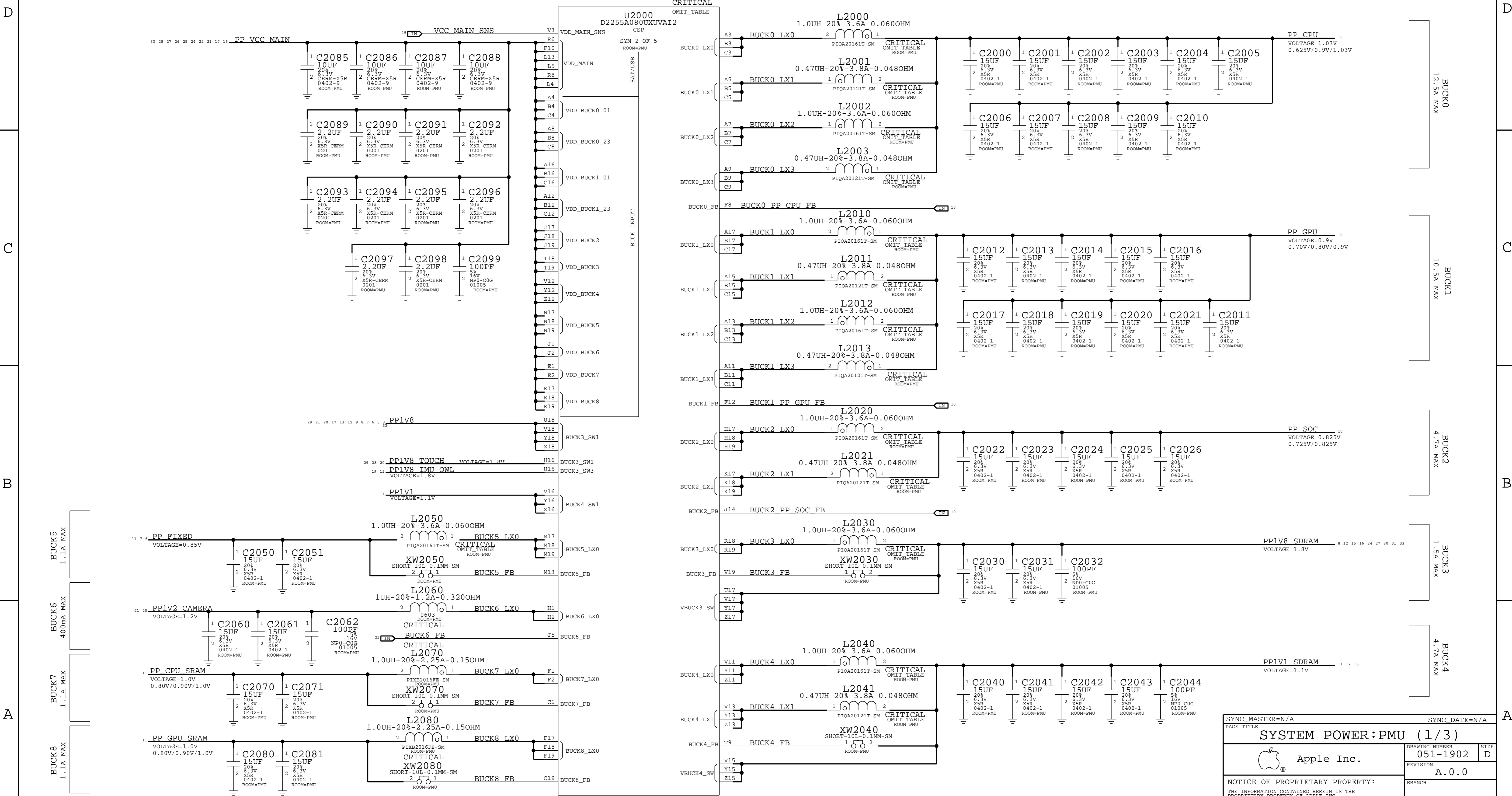
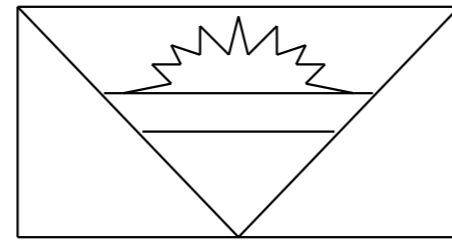
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Apple Inc. NAND

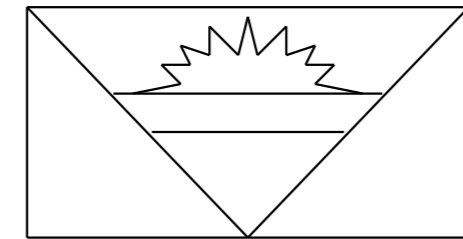
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ANTIGUA PMU - Buck Supplies



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Apple Inc.		DRAWING NUMBER	051-1902
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		SHEET	14 OF 59

ANTIGUA PMU - LDOs



ANTIGUA LDO SPECS

LDO#	ADJ. RANGE	ACCURACY	MAX. CURRENT
LDO1 (A)	2.5-3.3V	+/-1.4%	50mA
LDO2 (B)	1.2-2.0V	+/-2.5%	50mA
LDO3 (A)	2.5-3.3V	+/-1.4%	50mA
LDO4 (D)	0.7-1.2V	+/-2.5%	100mA
LDO5 (F)	2.5-3.3V	+/-2.5%	1000mA
LDO6 (C1)	1.2-3.6V	+/-2.5%	150mA
LDO7 (C)	2.5-3.3V	+/-25mV	250mA
LDO8 (C)	2.5-3.3V	+/-25mV	250mA
LDO9 (C)	2.5-3.3V	+/-25mV	250mA
LDO10 (G)	0.7-1.2V	+/-5.5%	1335mA
LDO11 (C)	2.5-3.3V	+/-25mV	250mA
LDO12 (E)	1.8V	+/-5%	10mA
LDO13 (C)	2.5-3.3V	+/-25mV	250mA
LDO14 (H)	0.8-1.5V	+/-2.5%	250mA
LDO15 (B)	1.2-2.0V	+/-2.5%	50mA

D

D

C

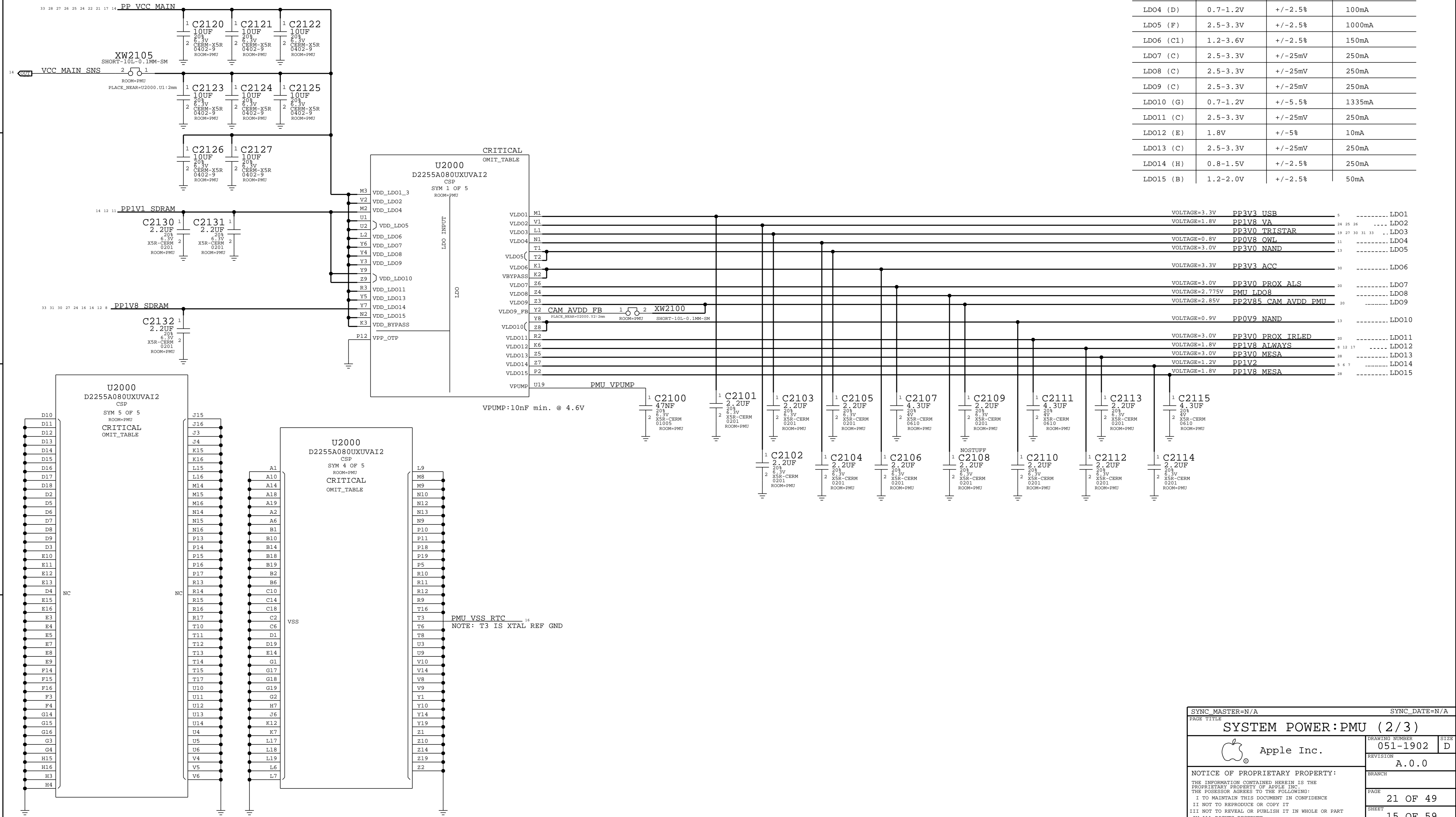
C

B

B

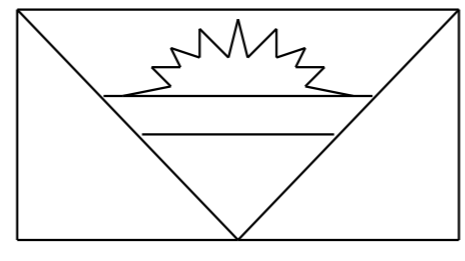
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		A.0.0	
		PAGE	21 OF 49
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ANTIGUA PMU - GPIOs, NTCs



CONTROL PIN NOTES:
 NOTE (1): INPUT PULL-DOWN 100-300k
 NOTE (2): INPUT PULL-DOWN 1M
 NOTE (3): INPUT PULL-UP OR DOWN 100k-300k
 NOTE (4): OUTPUT OPEN-DRAIN, REQUIRES PULL-UP

8 7 6 5 4 3 2 1

D

D

C

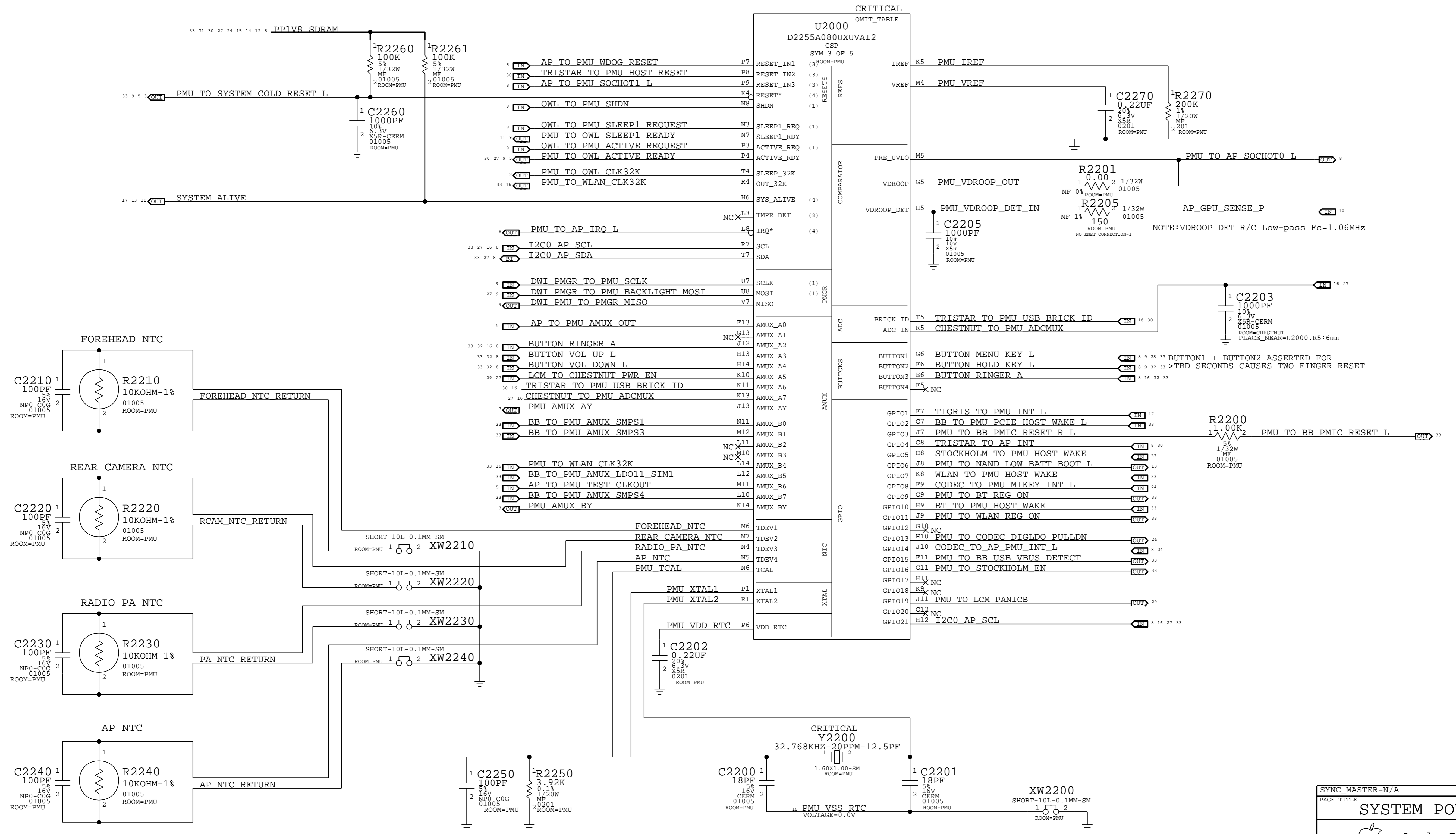
C

B

B

A

A



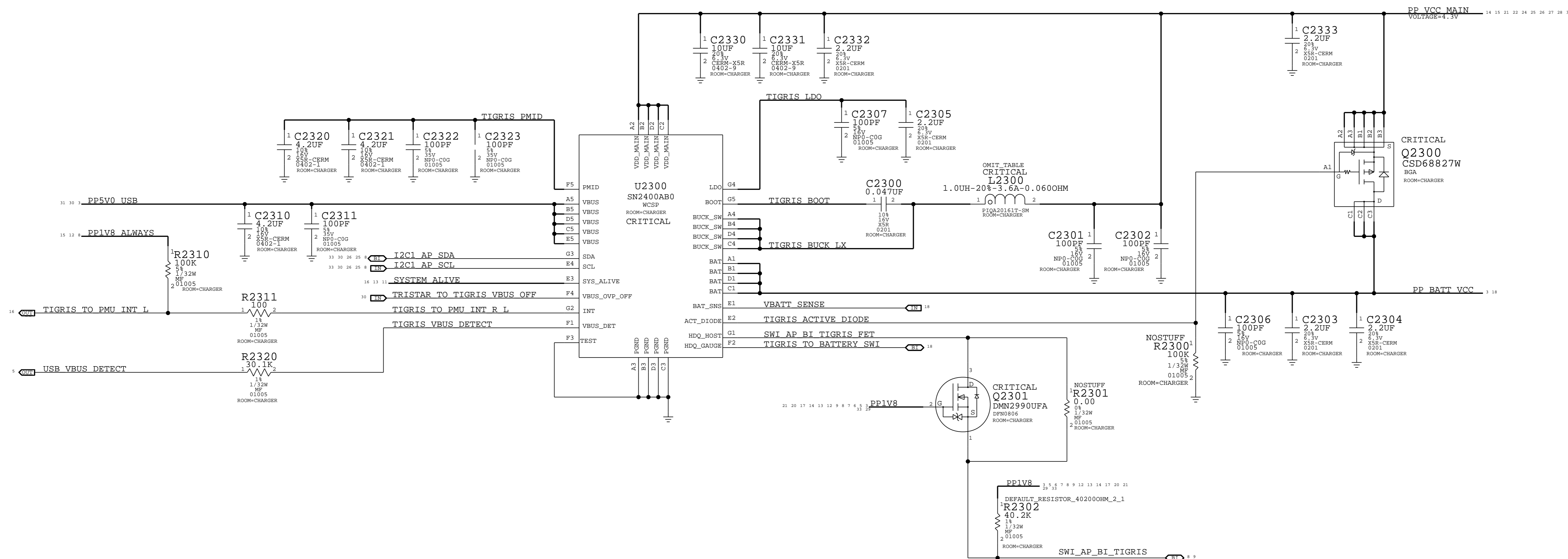
NOTE: 100PF CAPS ARE THE SAMPLING CAPS FOR PMU ADC

8 7 6 5 4 3 2 1

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Apple Inc.		DRAWING NUMBER	SIZE
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TIGRIS CHARGER

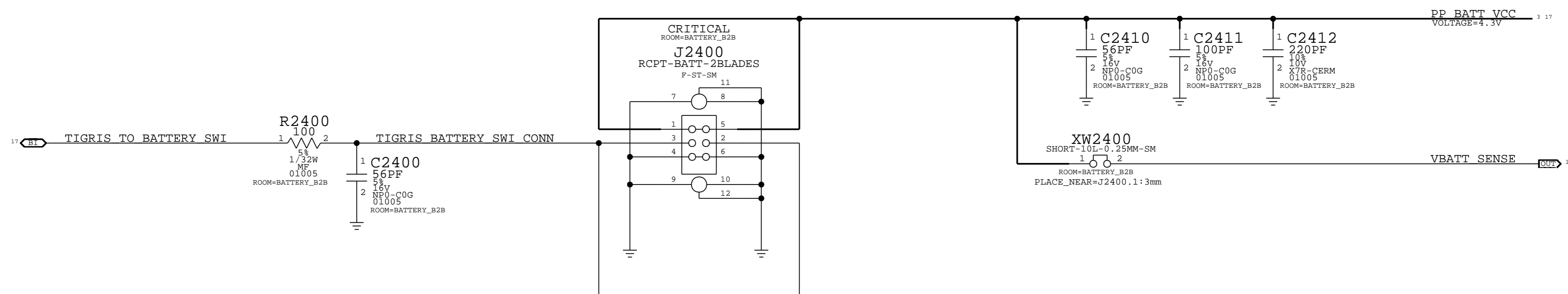
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BATTERY CONNECTOR

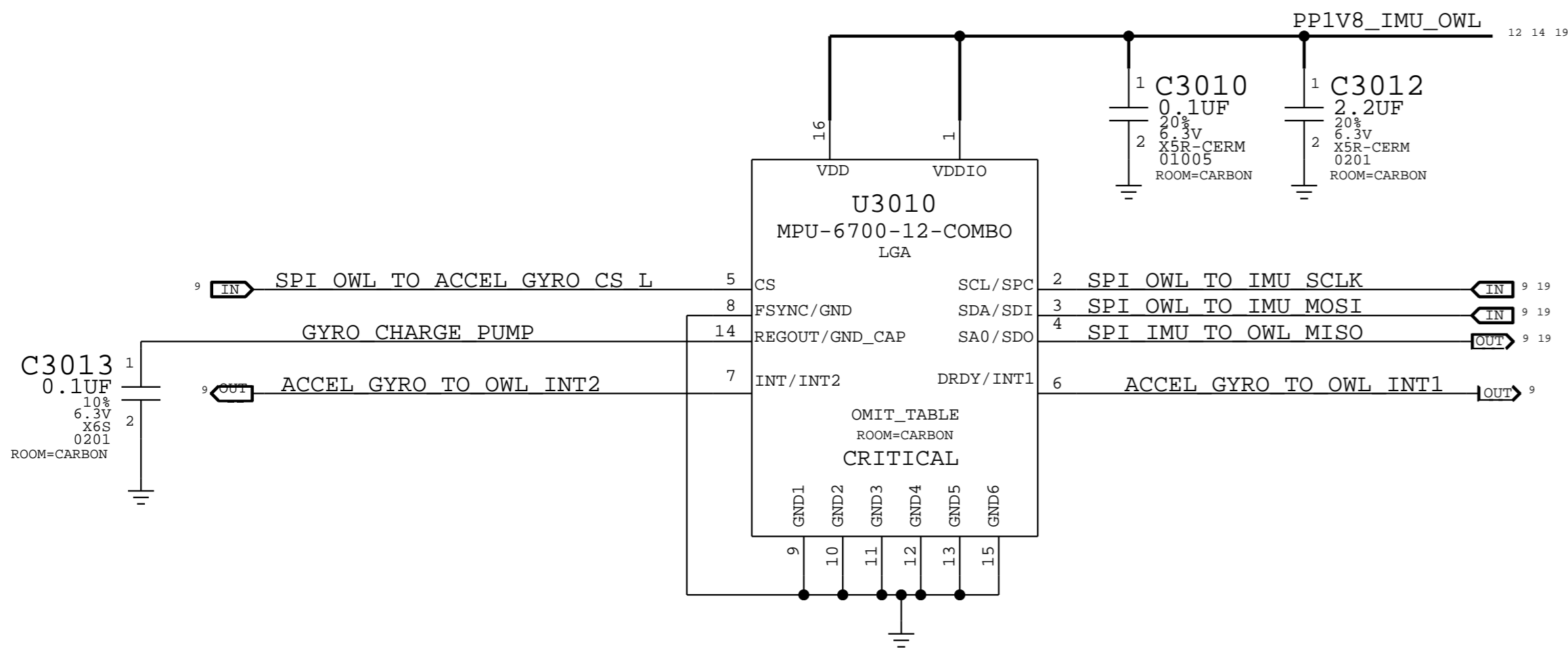
THIS ONE ON MLB ----> 516S00104 (RCPT)
516S00105 (PLUG)



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Apple Inc.		DRAWING NUMBER	SIZE
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		PAGE	24 OF 49
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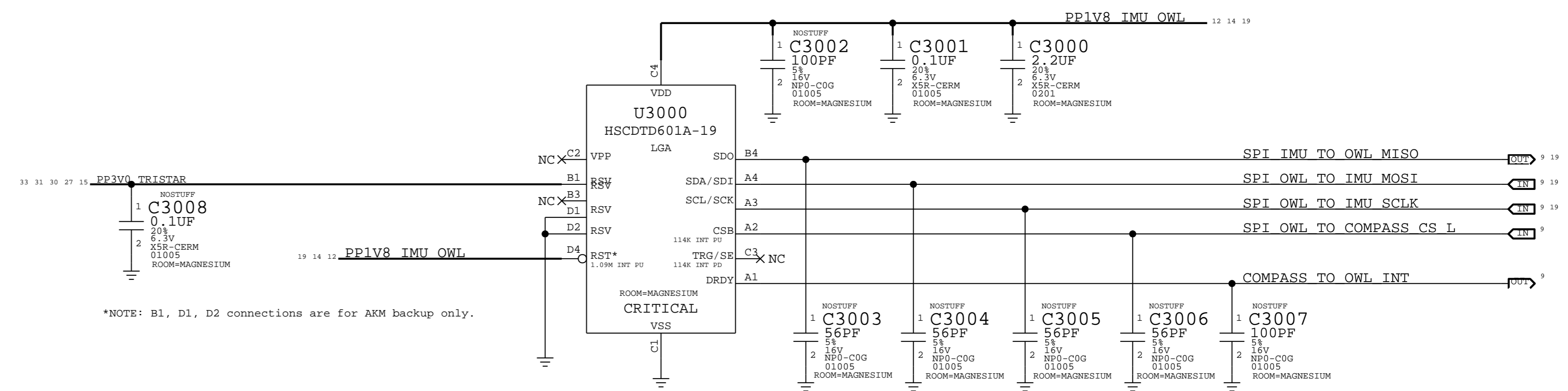
CARBON - ACCEL & GYRO

INVENSENSE (APN: 338S00017, 338S00087)



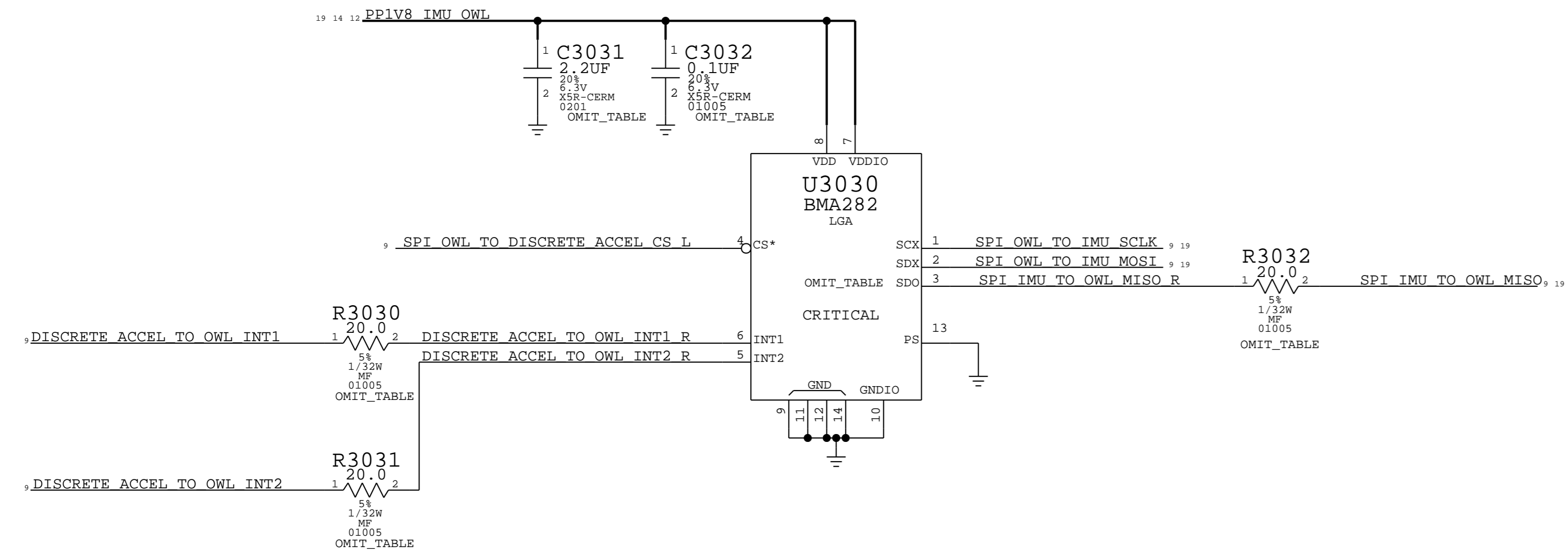
MAGNESIUM - COMPASS

ALPS (APN:338S00084)



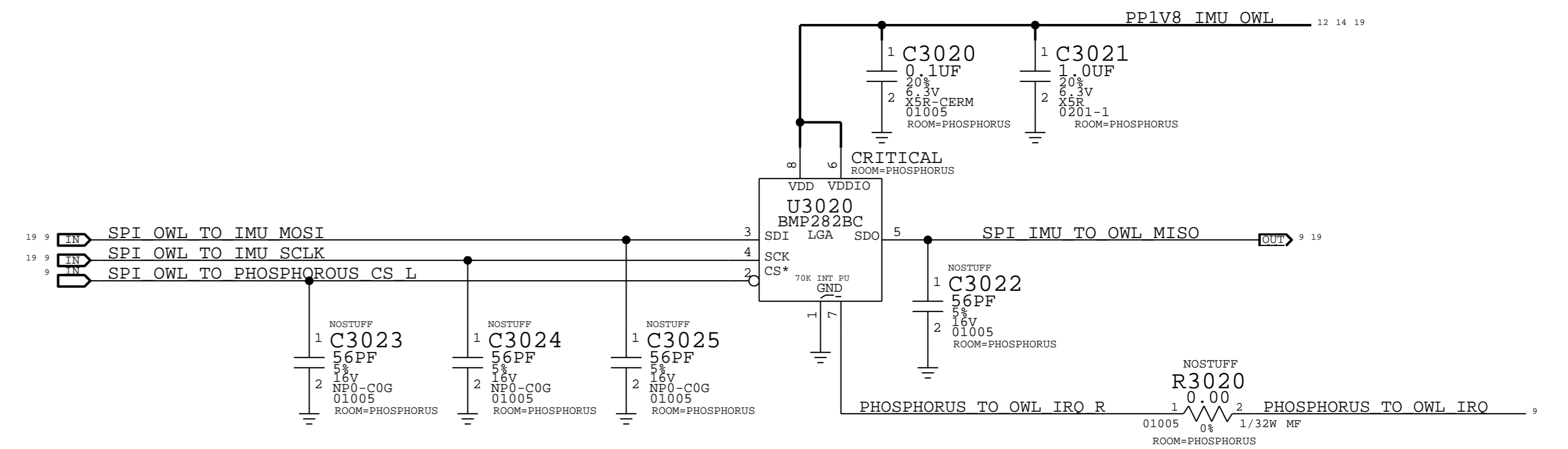
DISCRETE ACCEL

BOSCH (APN: 338S1163)



PHOSPHOROUS

BOSCH (APN:338S00044)



R3020 SHOULD BE STUFFED FOR ST PHOSPHOROUS ONLY. FOR BOSCH PHOSPHOROUS, PINS 1 AND 7 ARE SHORTED INTERNALLY, SO NO NEED FOR 0-OHM TO GROUND OPTION ON PIN 7.



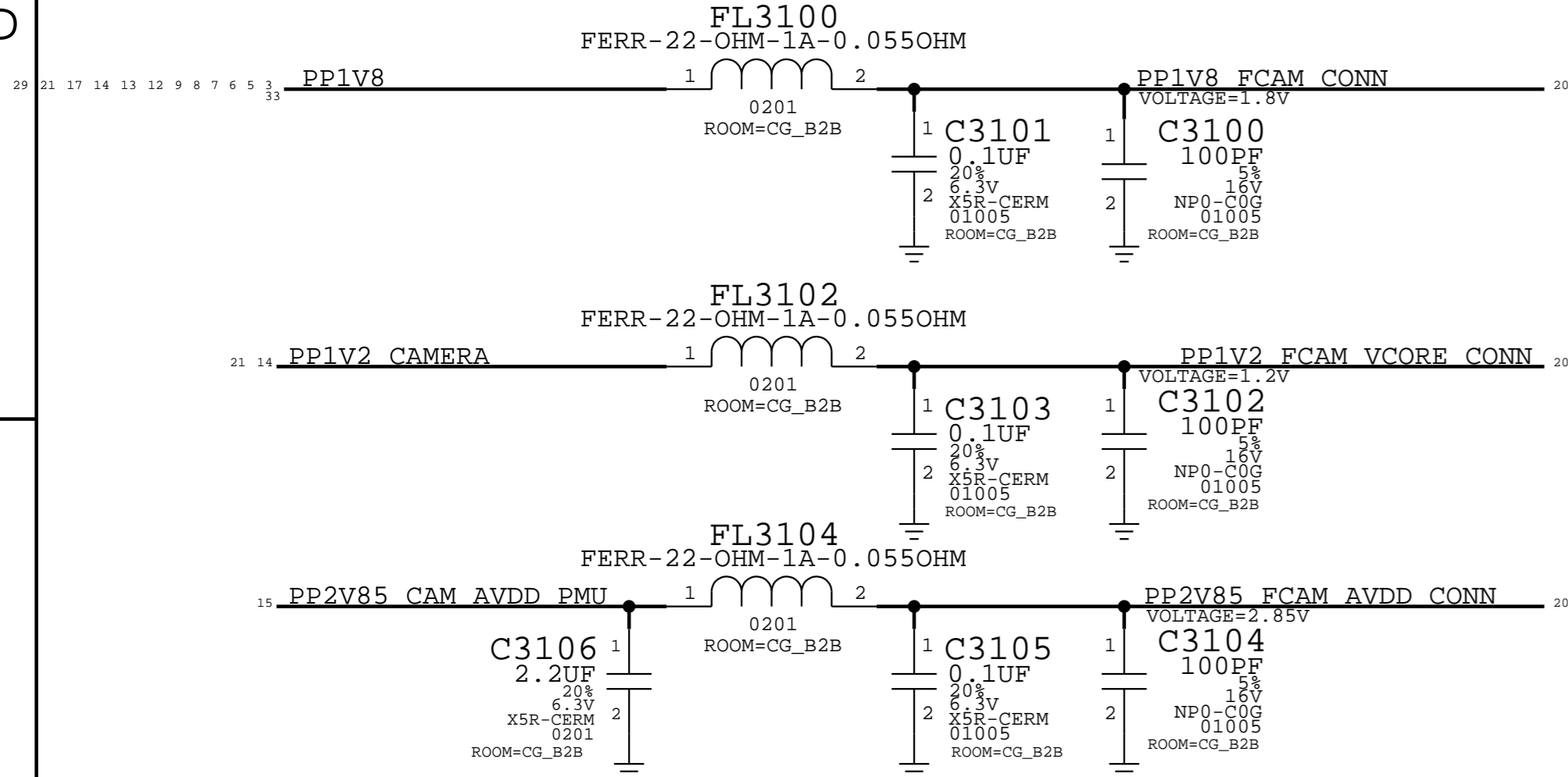
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Apple Inc.		REVISION		A.0.0
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FOREHEAD FLEX (FCAM)

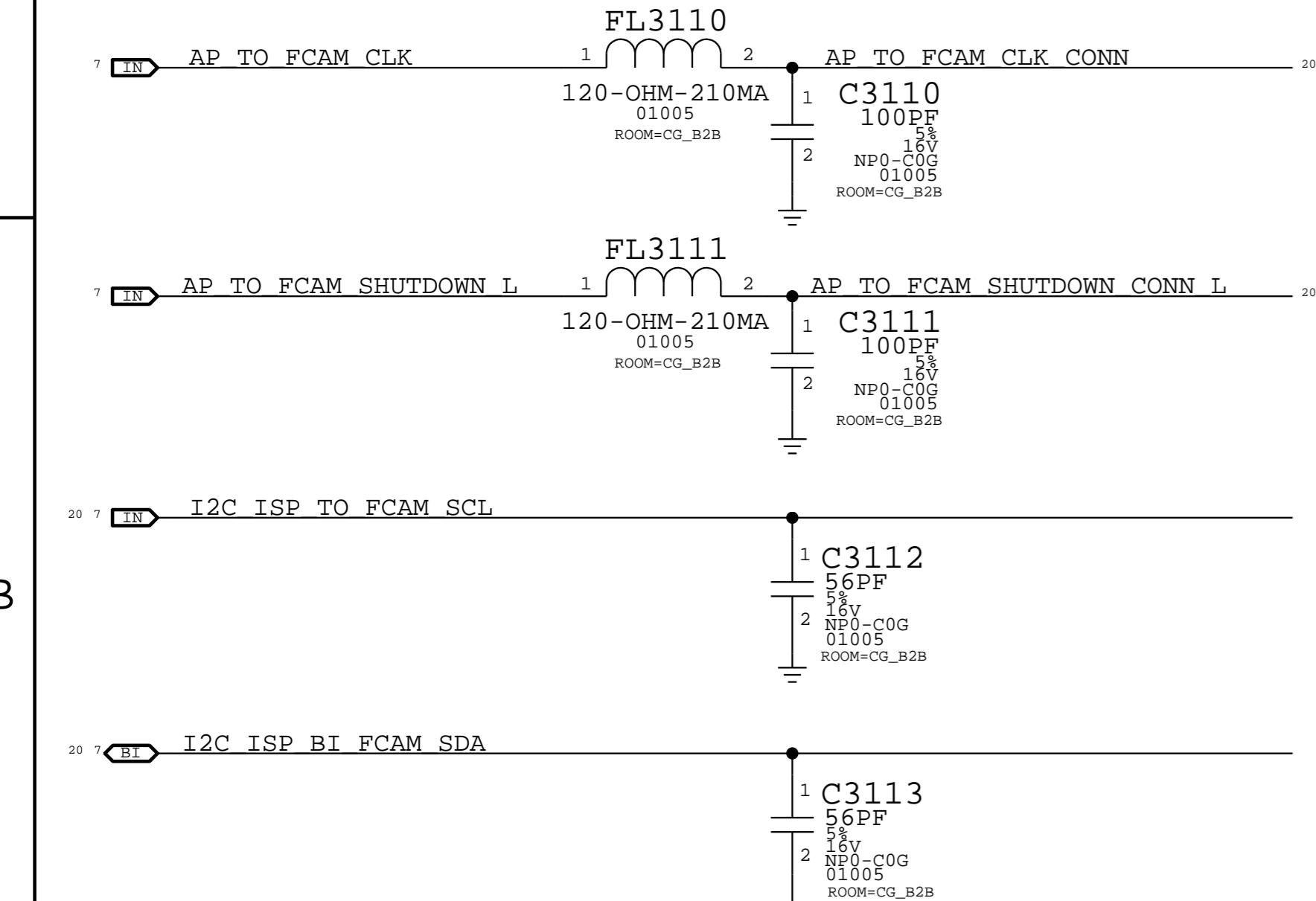
PROX & ALS POWER

FOREHEAD CONNECTOR

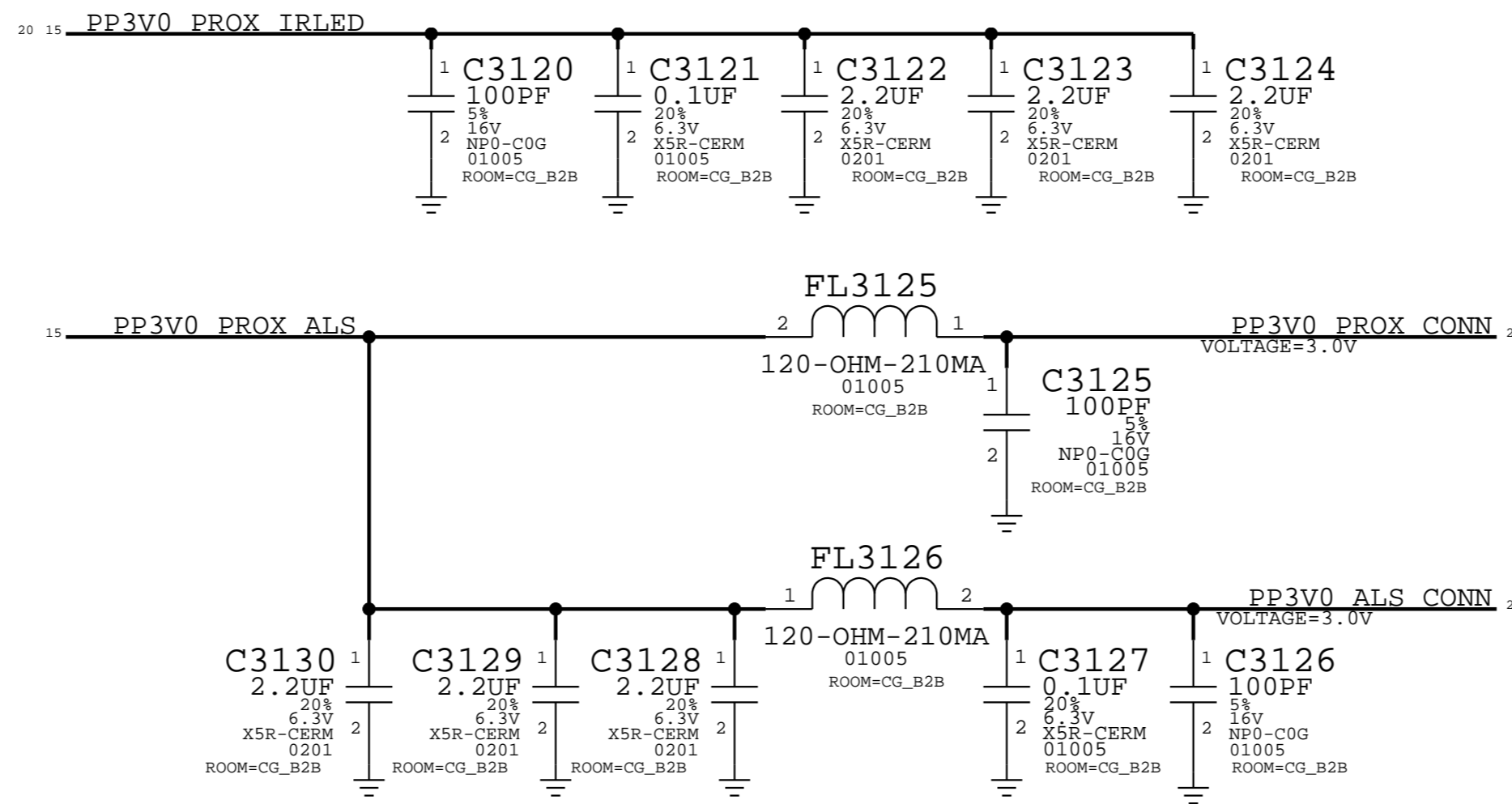
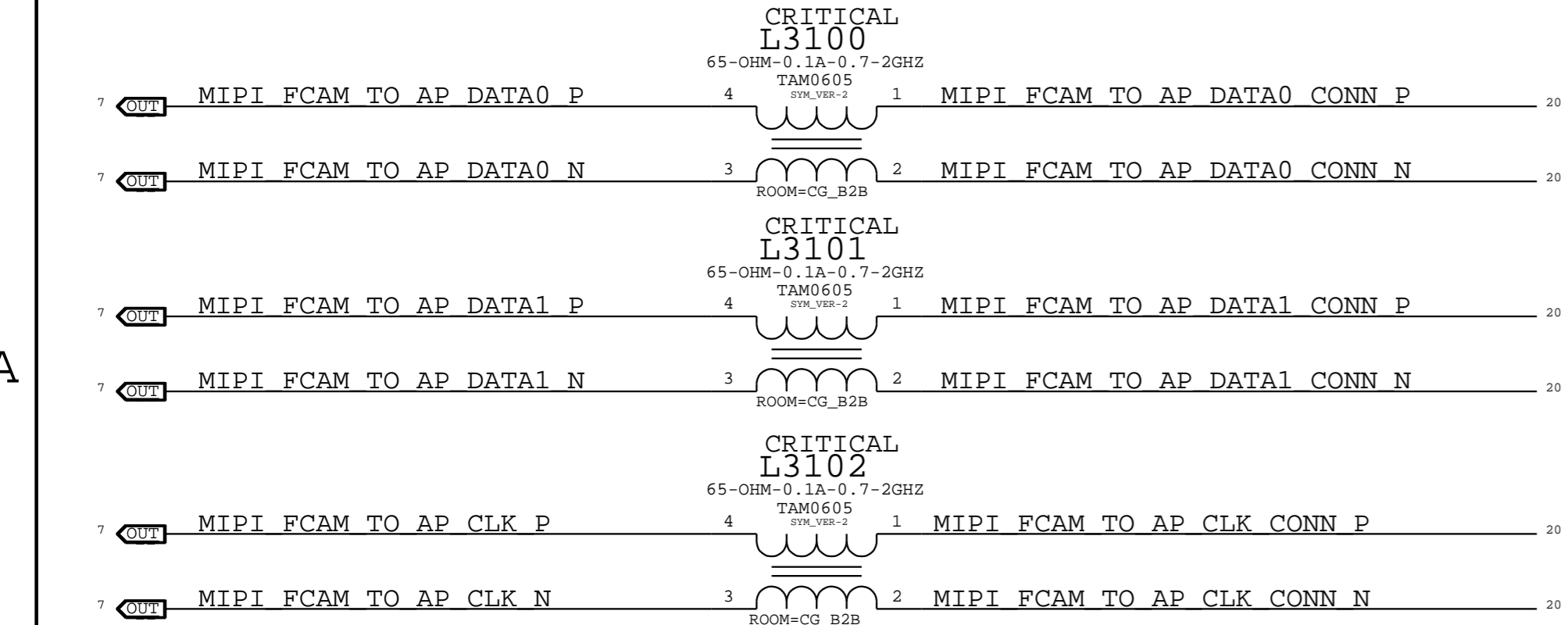
CAMERA POWER



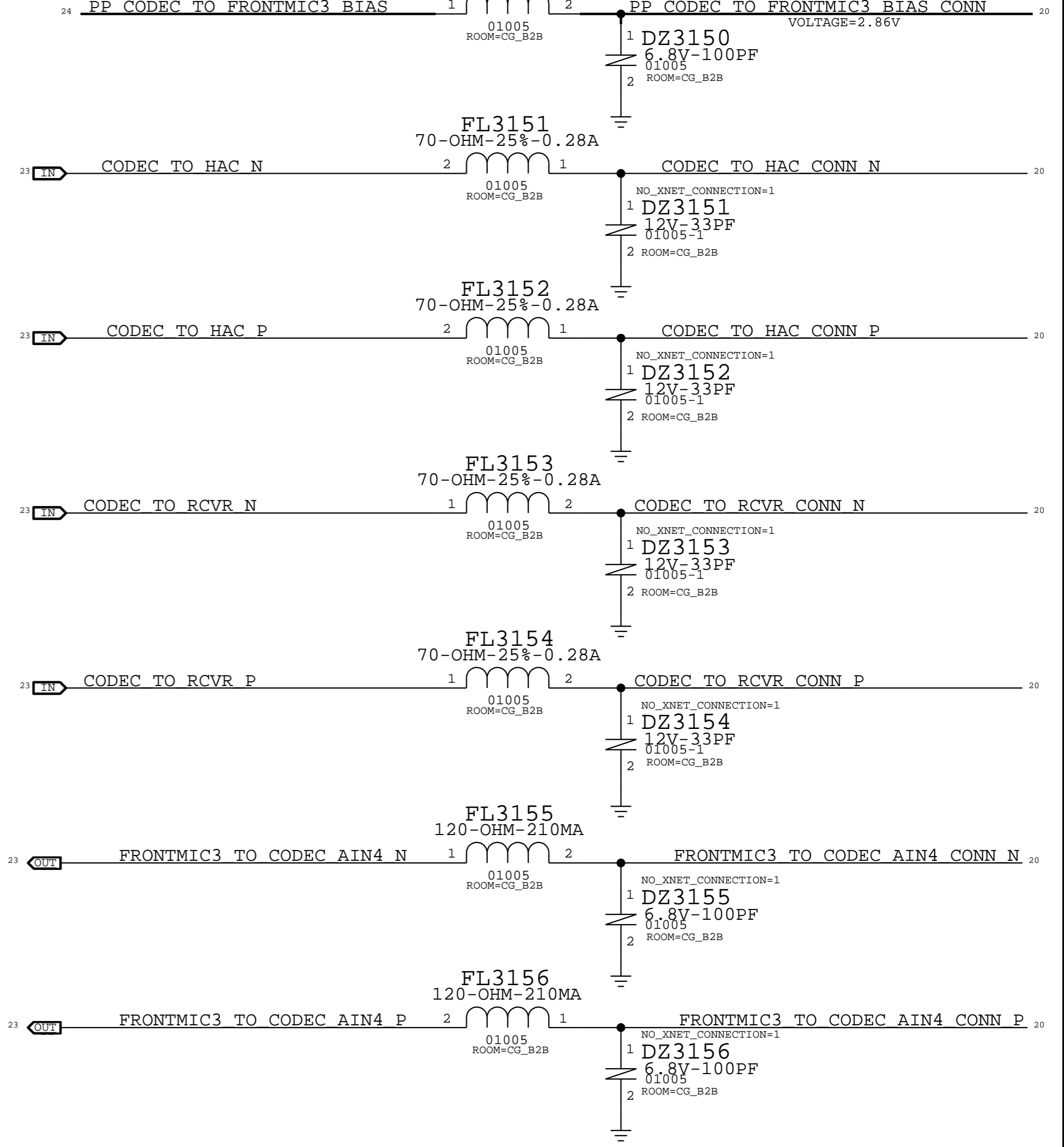
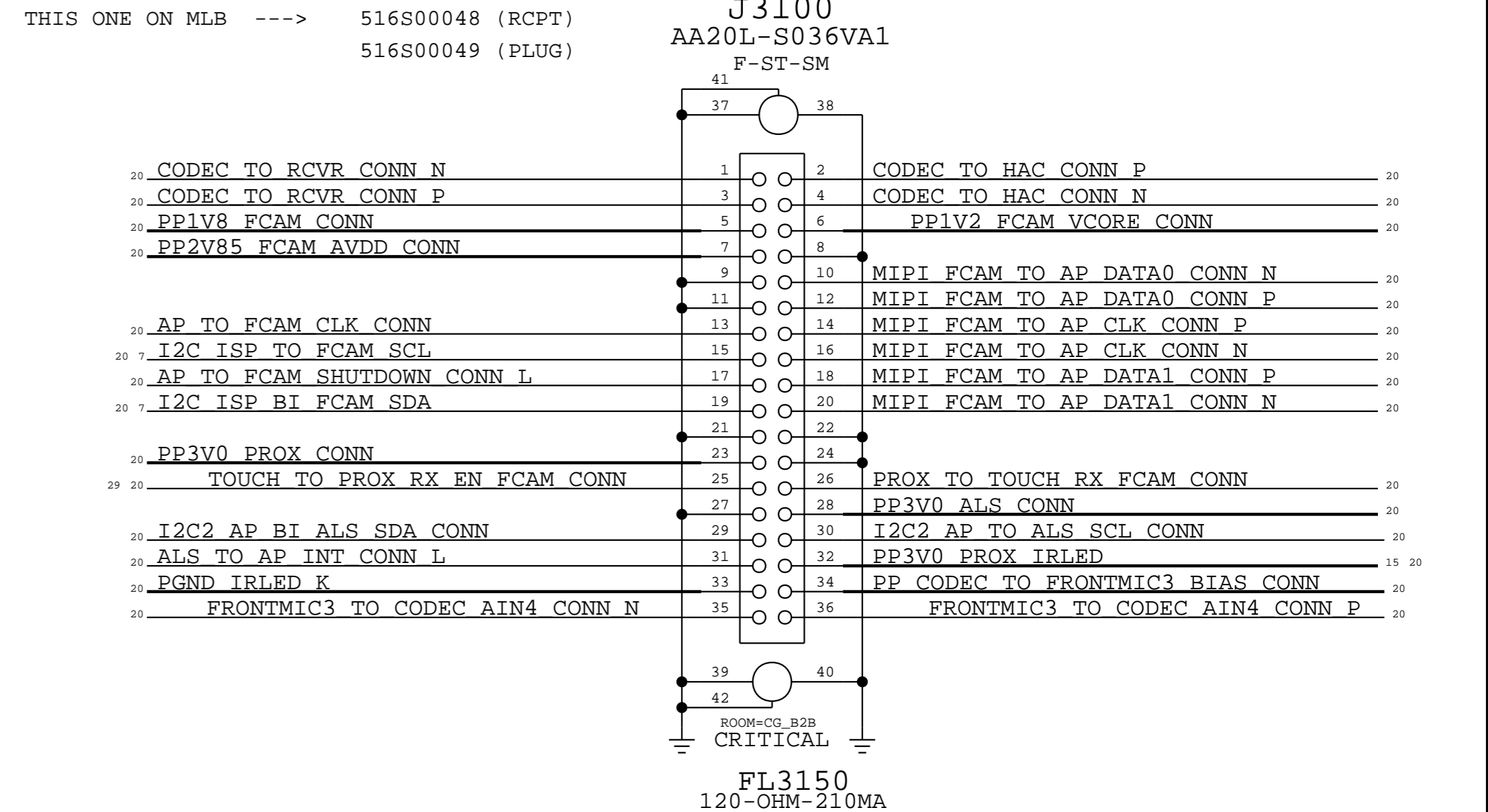
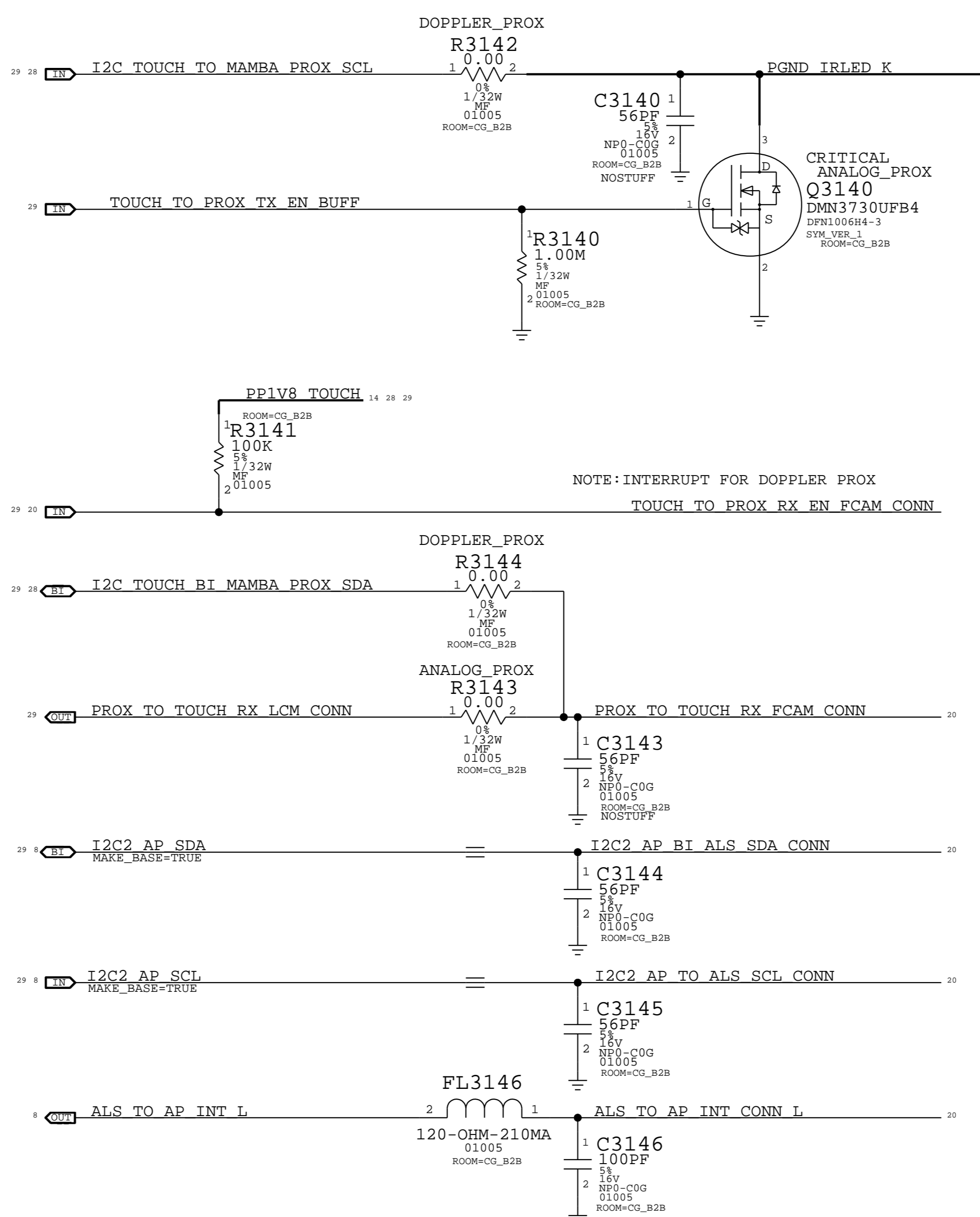
CAMERA I/O



CAMERA MIPI



PROX & ALS INTERFACE



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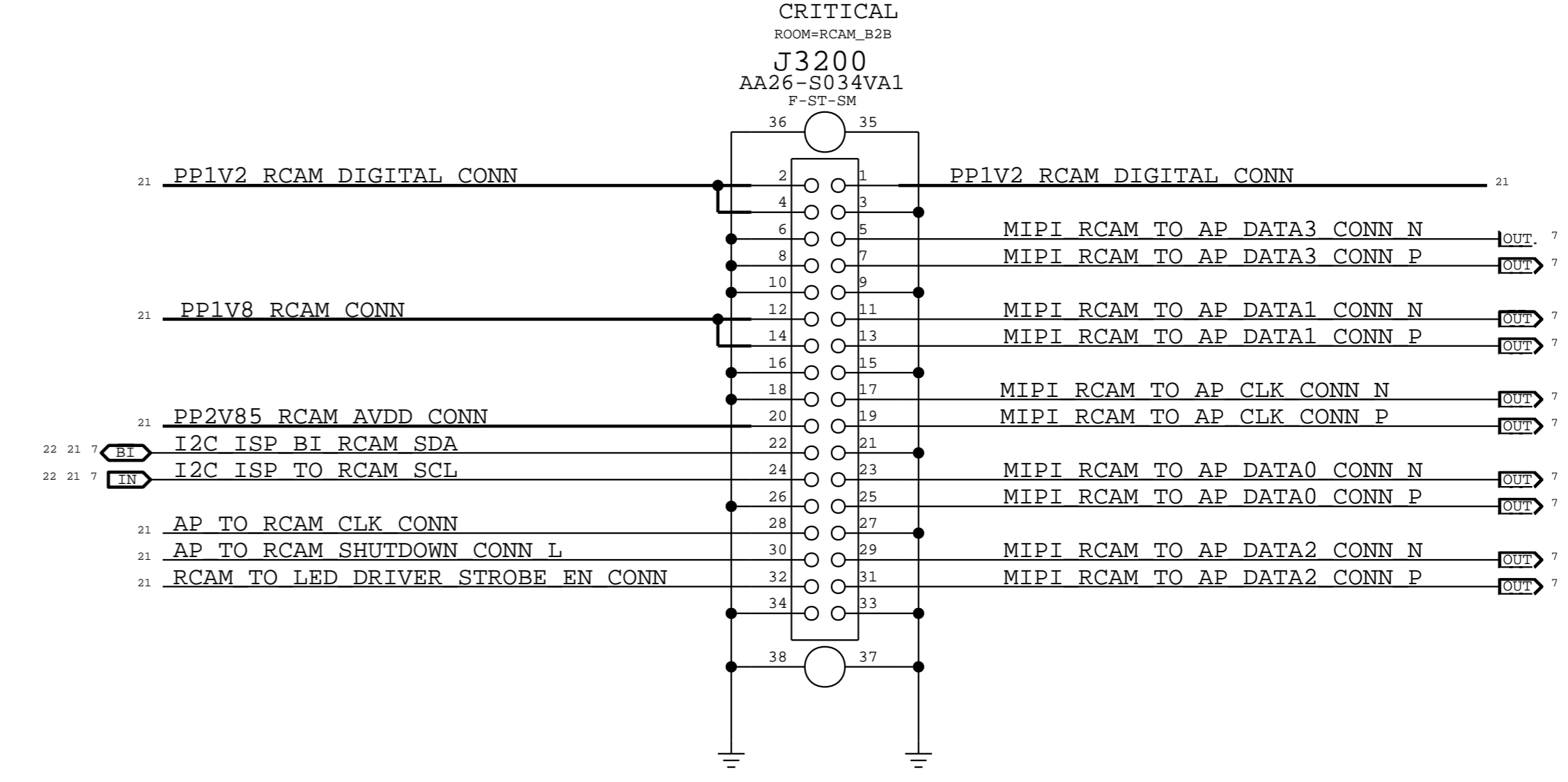
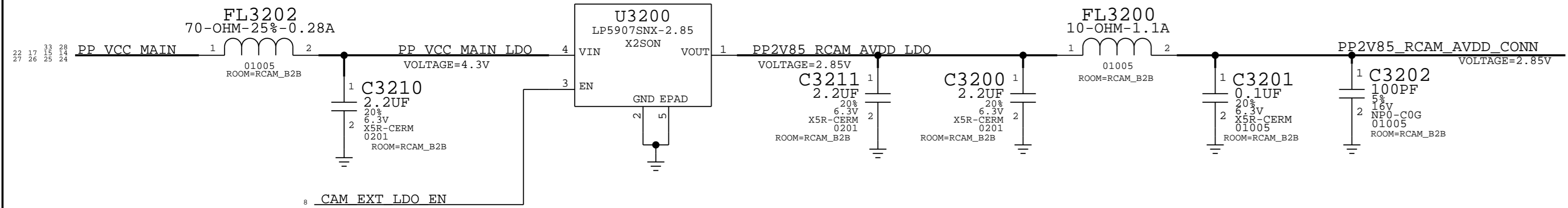
REAR CAMERA FLEX

RCAM CONNECTOR

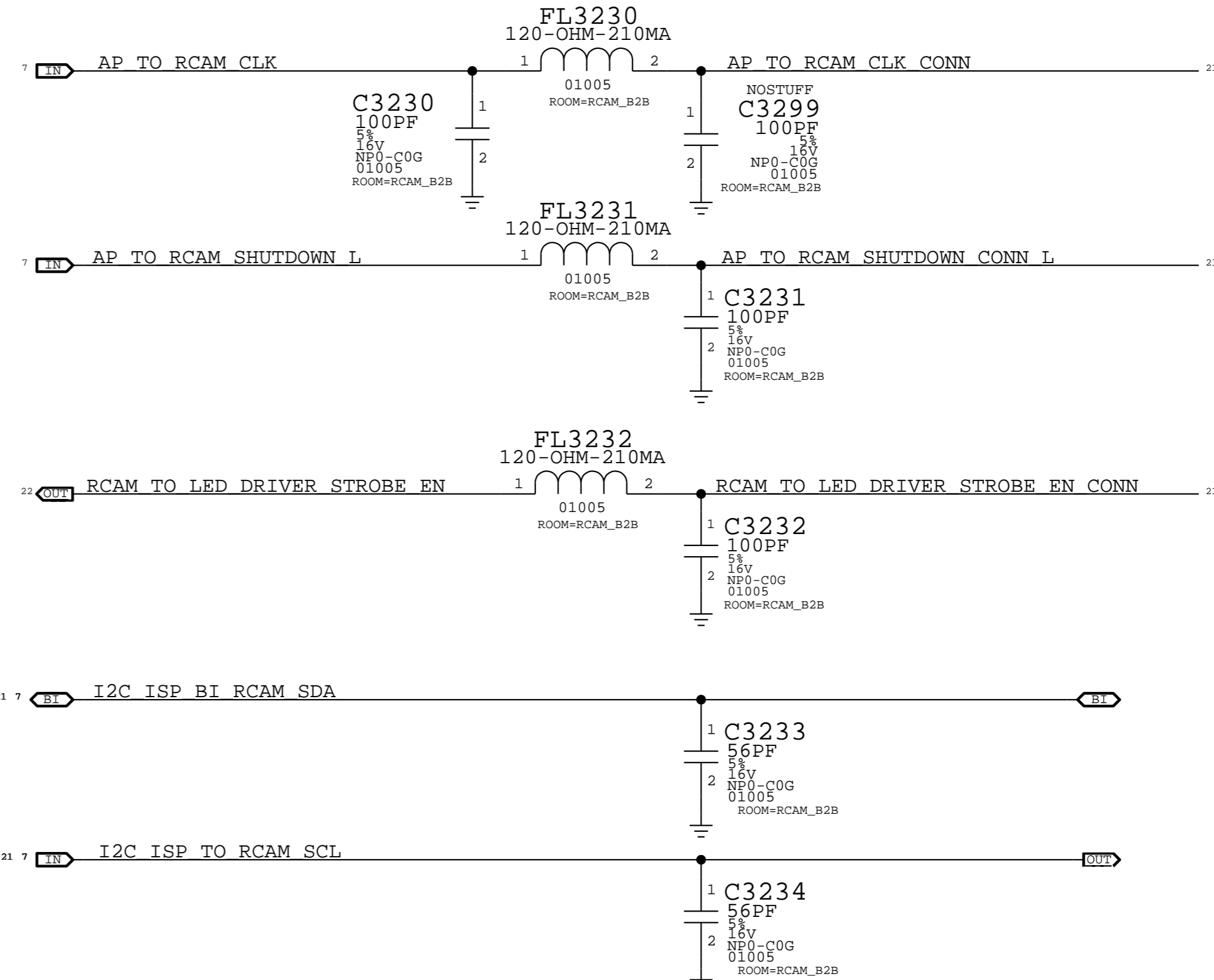
THIS ONE ON MLB ----> 516S00043 (RCPT)
516S00042 (PLUG)

CAMERA POWER

NOTE: OUTPUT IMPEDANCE MUST BE >0.01-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.



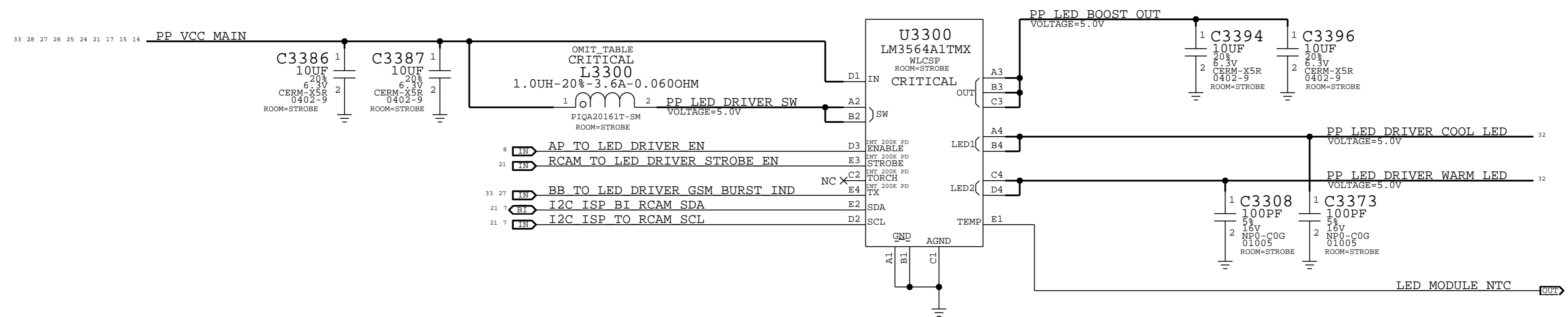
DIGITAL I/O



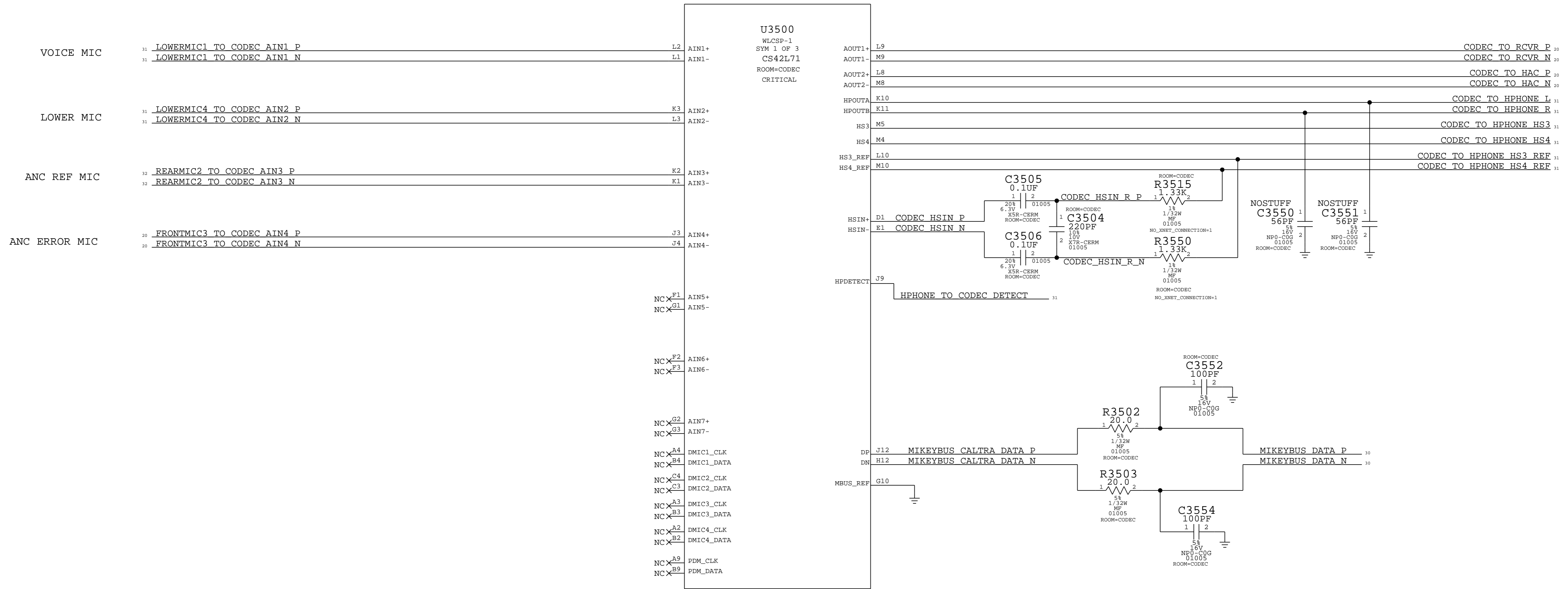
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DUAL LED STROBE DRIVER

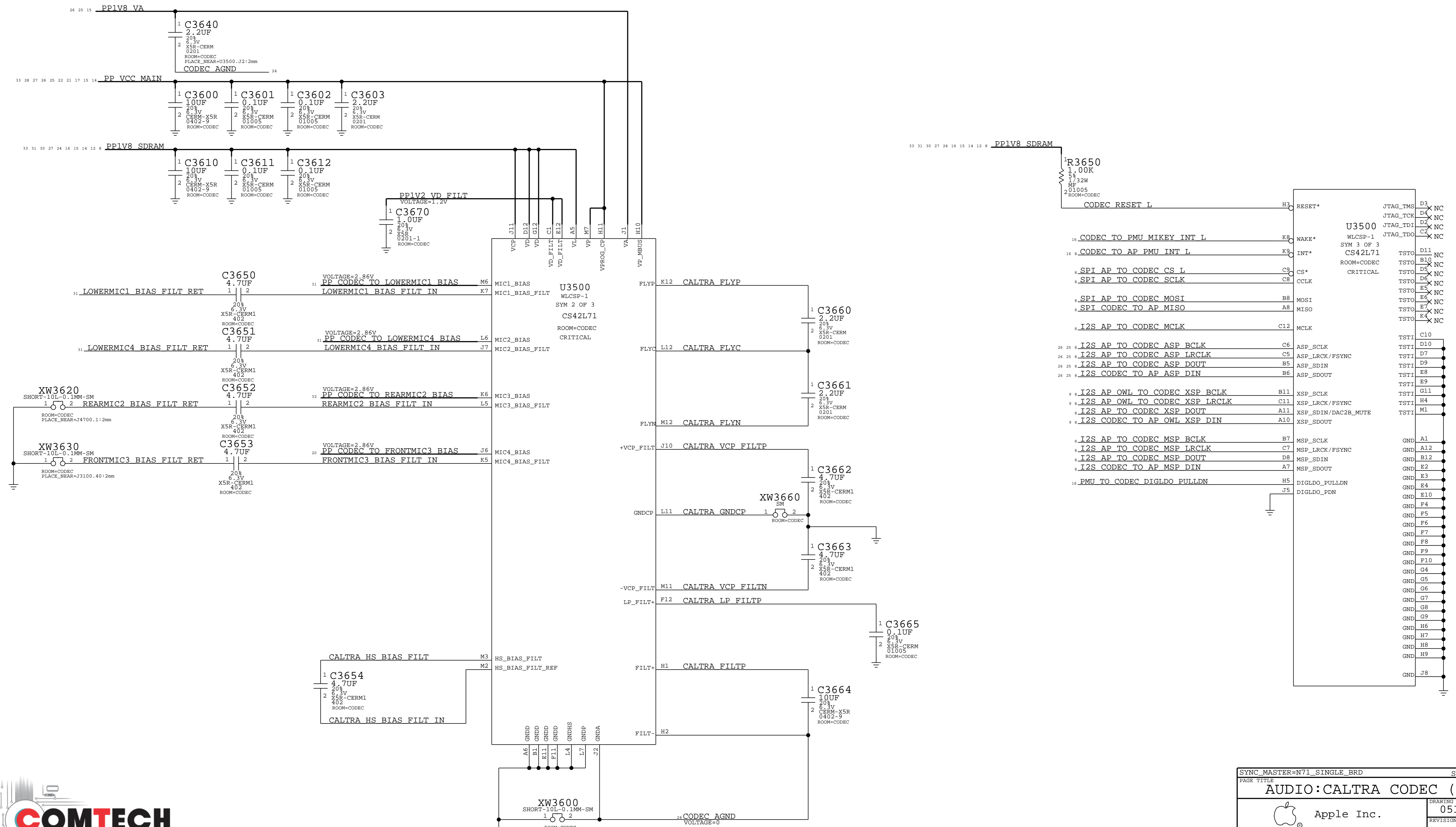
APN: 353S3899



CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)



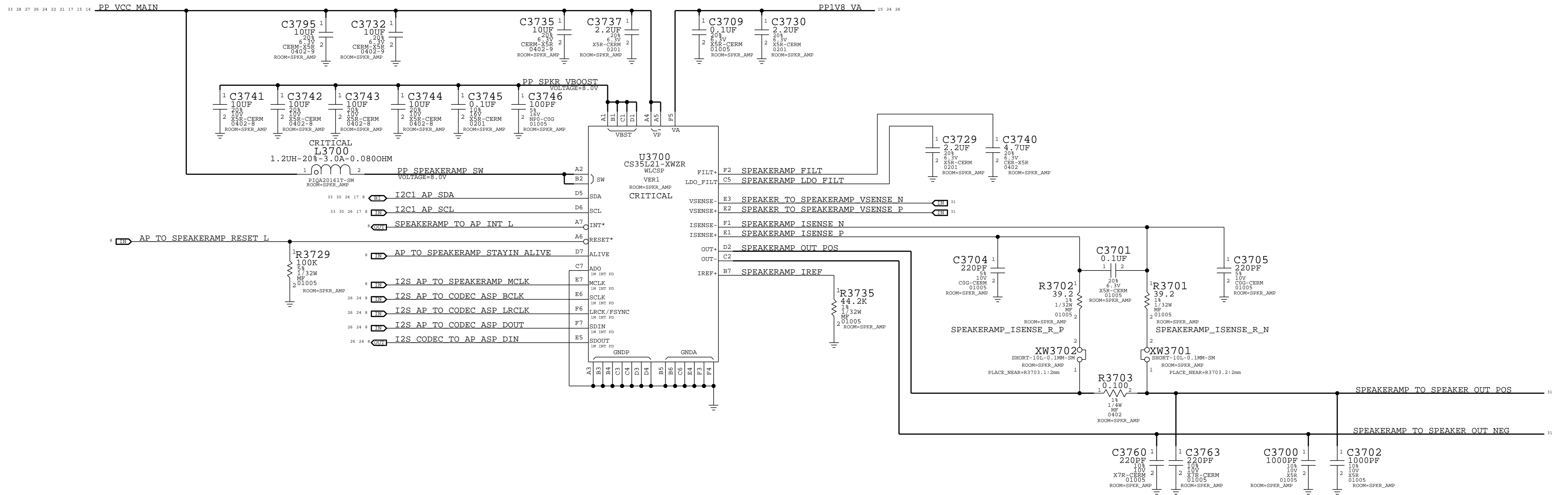
CALTRA AUDIO CODEC (POWER & I/O)



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Apple Inc.	DRAWING NUMBER	051-1902	SIZE
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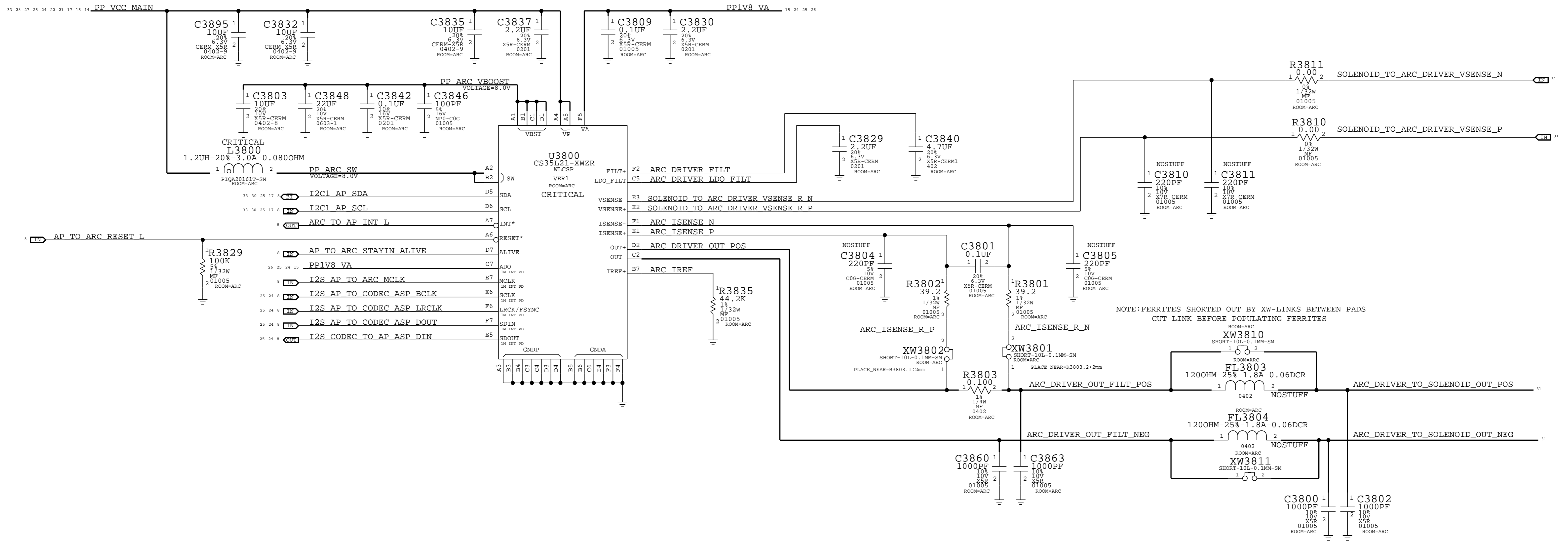
SPEAKER AMPLIFIER

APN: 338S1285



ARC DRIVER

APN: 338S1285



NOTE: FERRITES SHORTED OUT BY XW-LINKS BETWEEN PADS
CUT LINK BEFORE POPULATING FERRITES

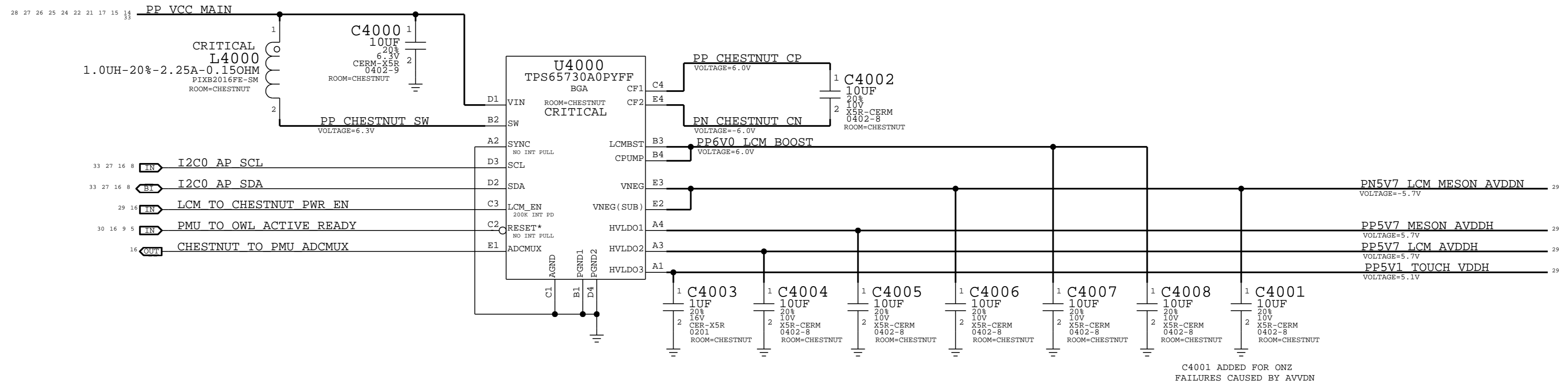


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DISPLAY & TOUCH - POWER SUPPLIES

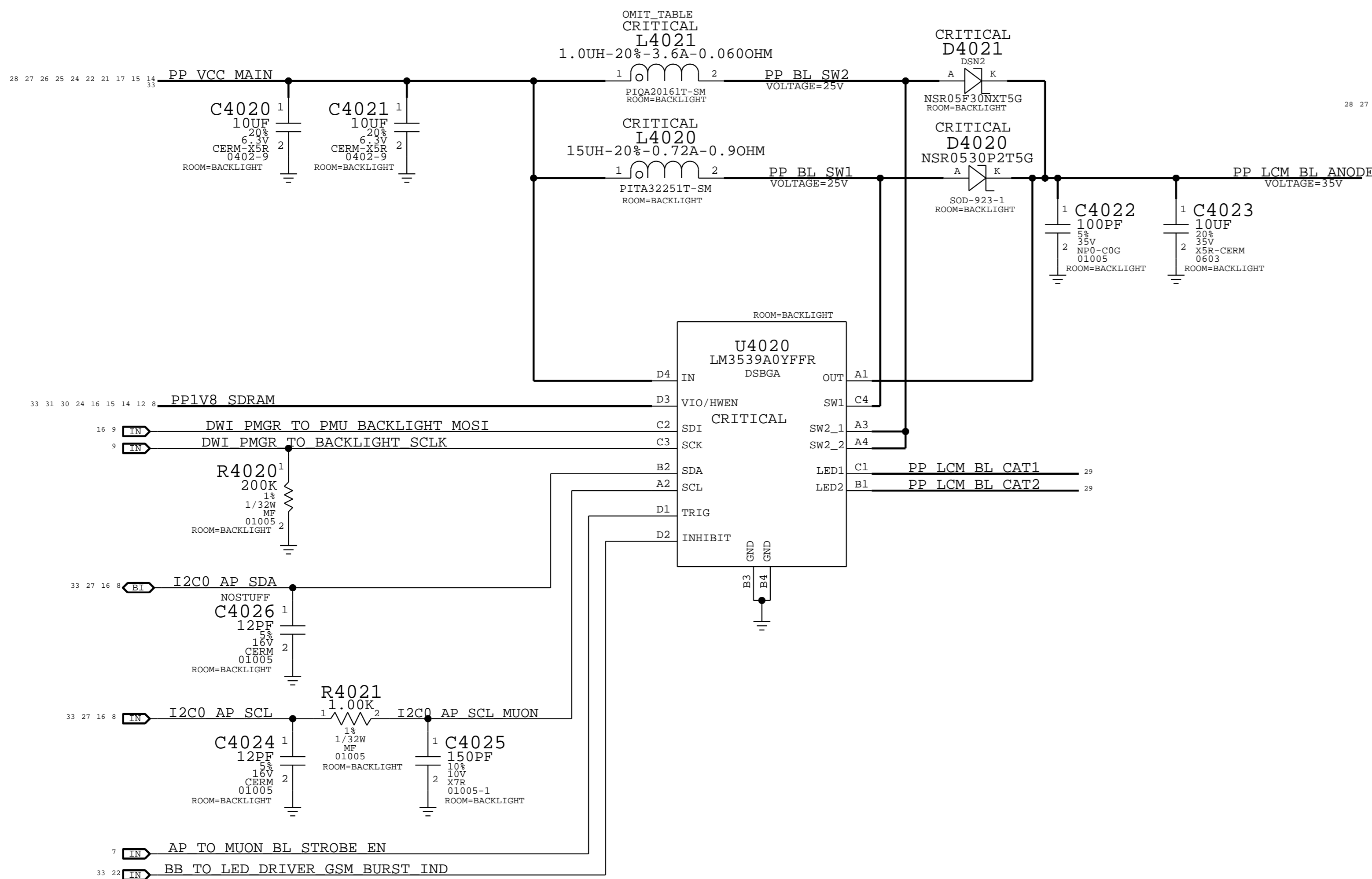
CHESTNUT DISPLAY PMU

APN: 338S1172



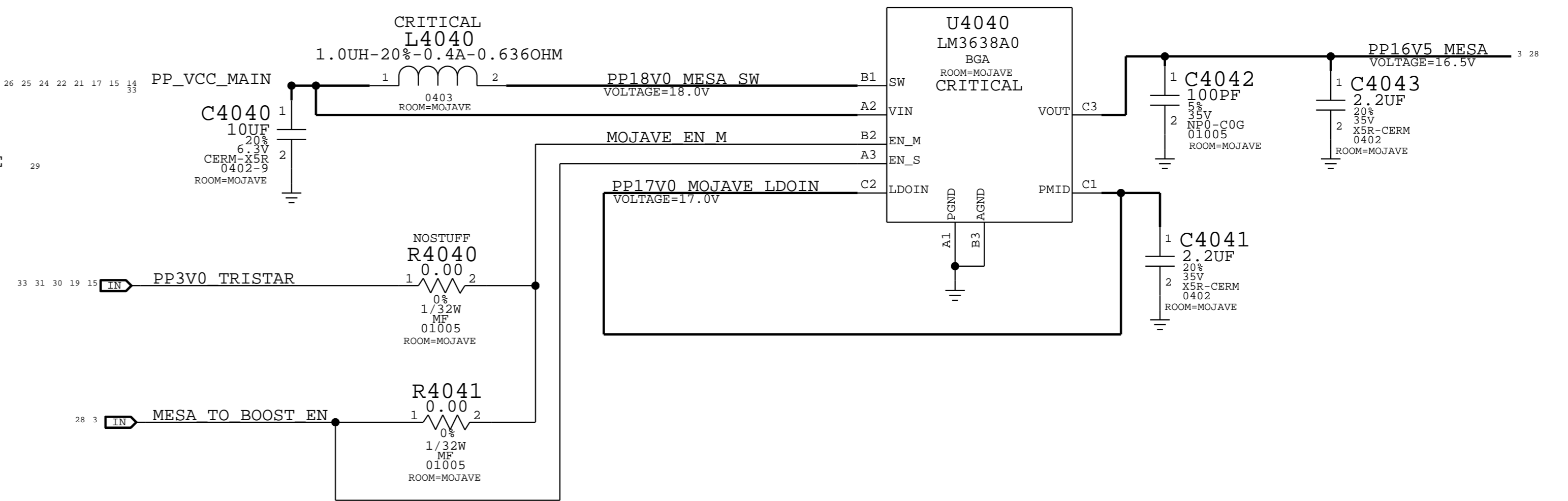
LED BACKLIGHT DRIVER

APN: 353S00407



MOJAVE MESA BOOST

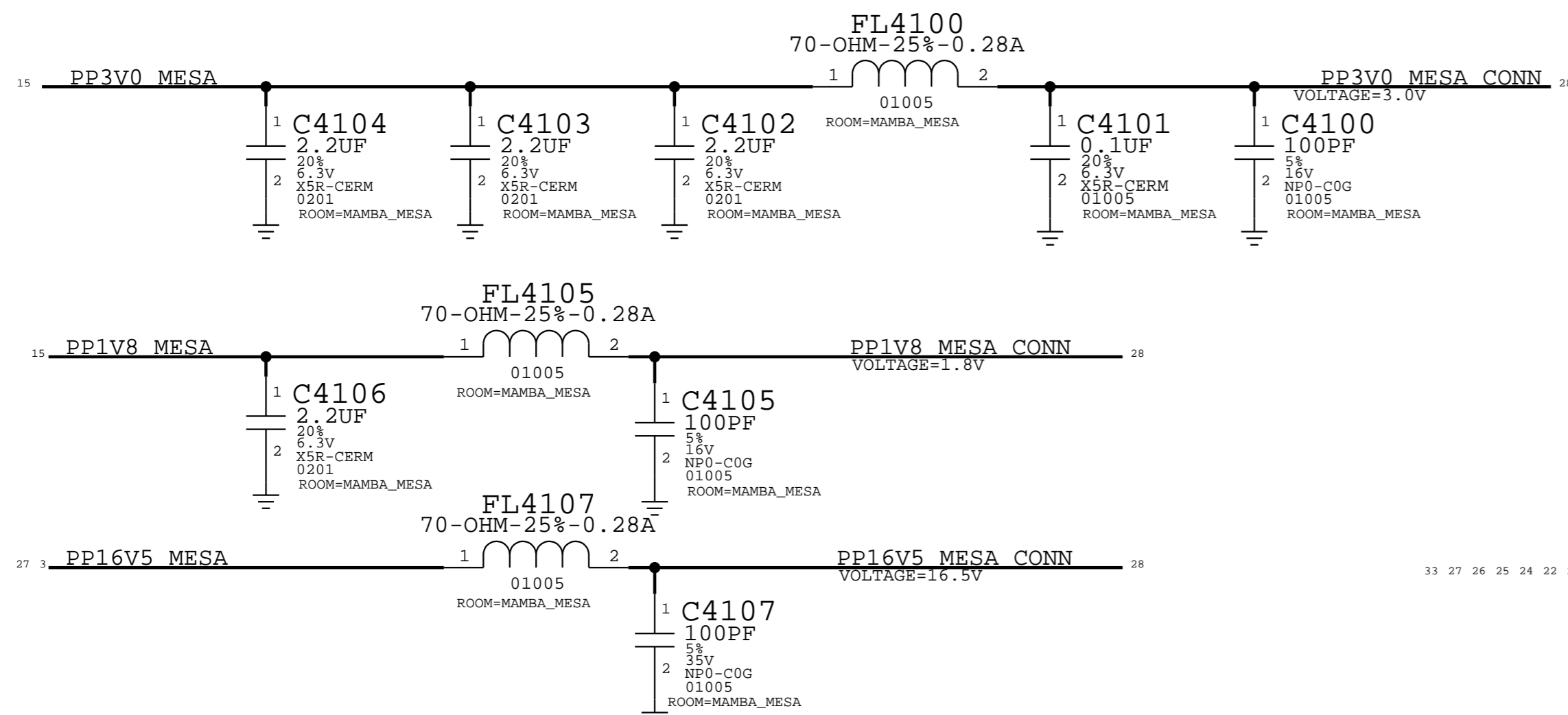
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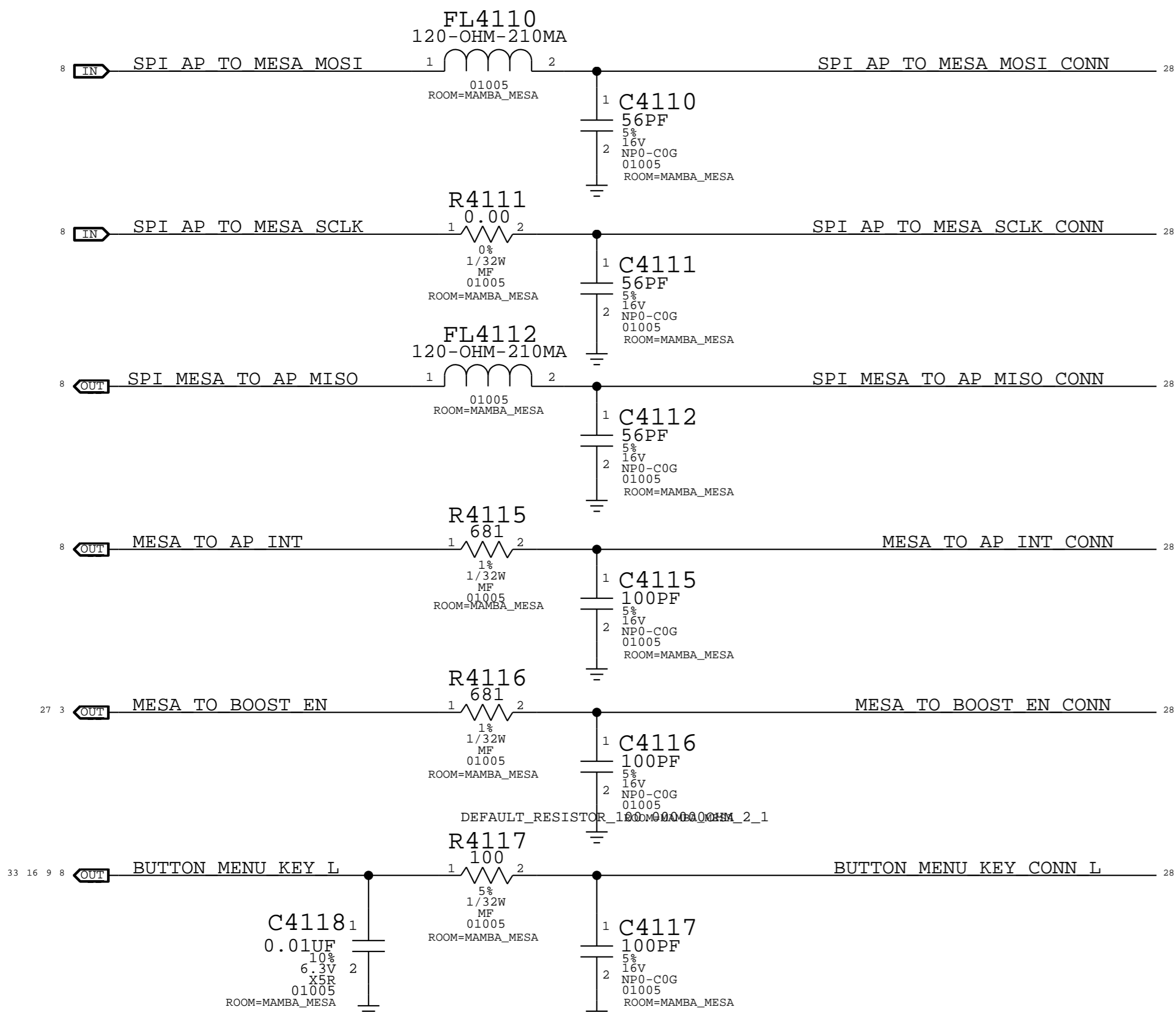
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MAMBA & MESA (M&M) FLEX

MESA POWER

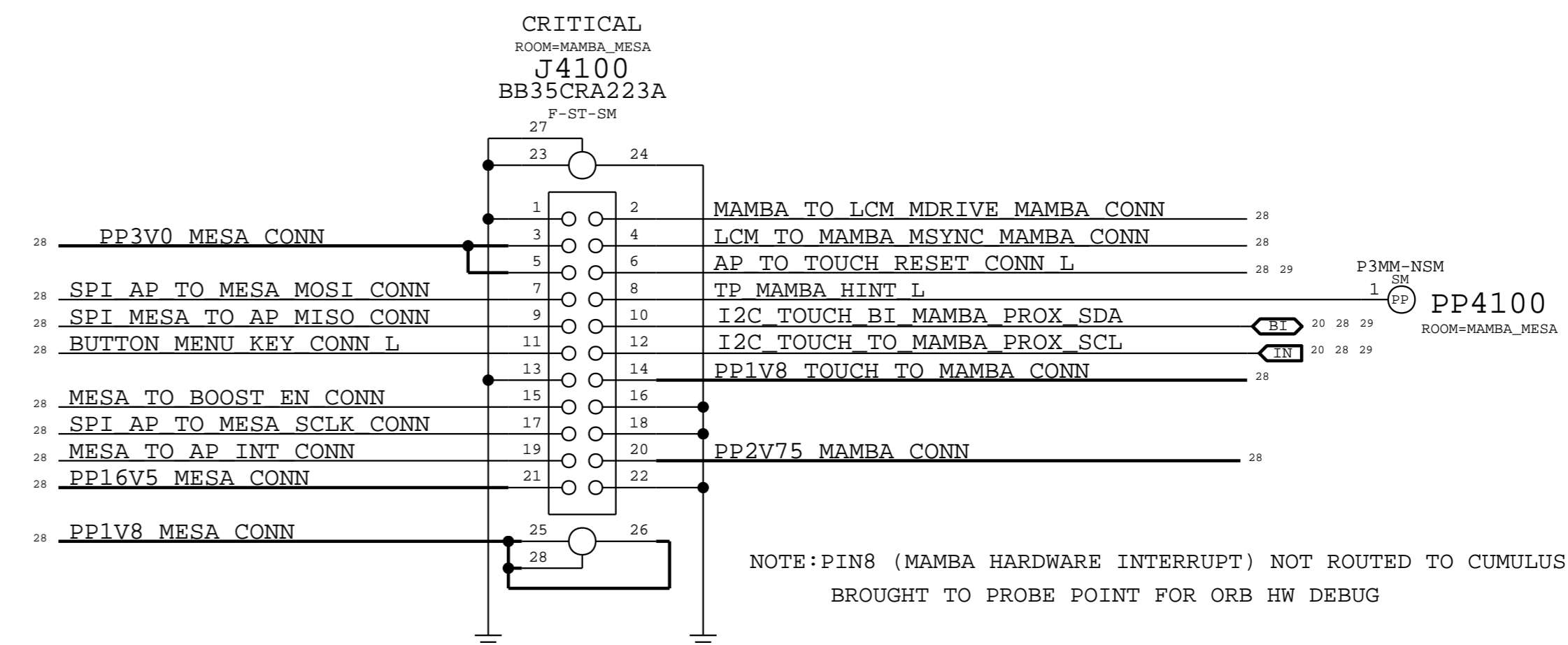


MESA DIGITAL I/O



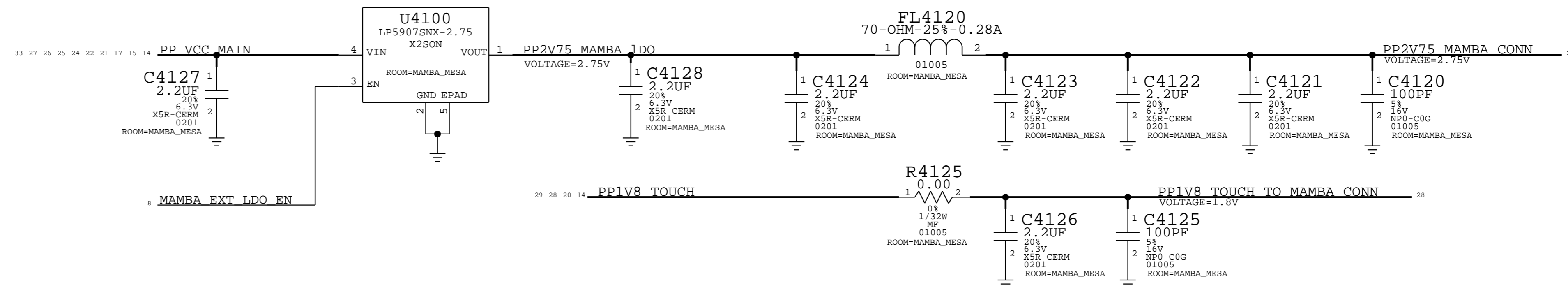
MAMBA & MESA CONNECTOR

THIS ONE ON MLB ---> 516S00056 (RCPT)
516S00057 (PLUG)

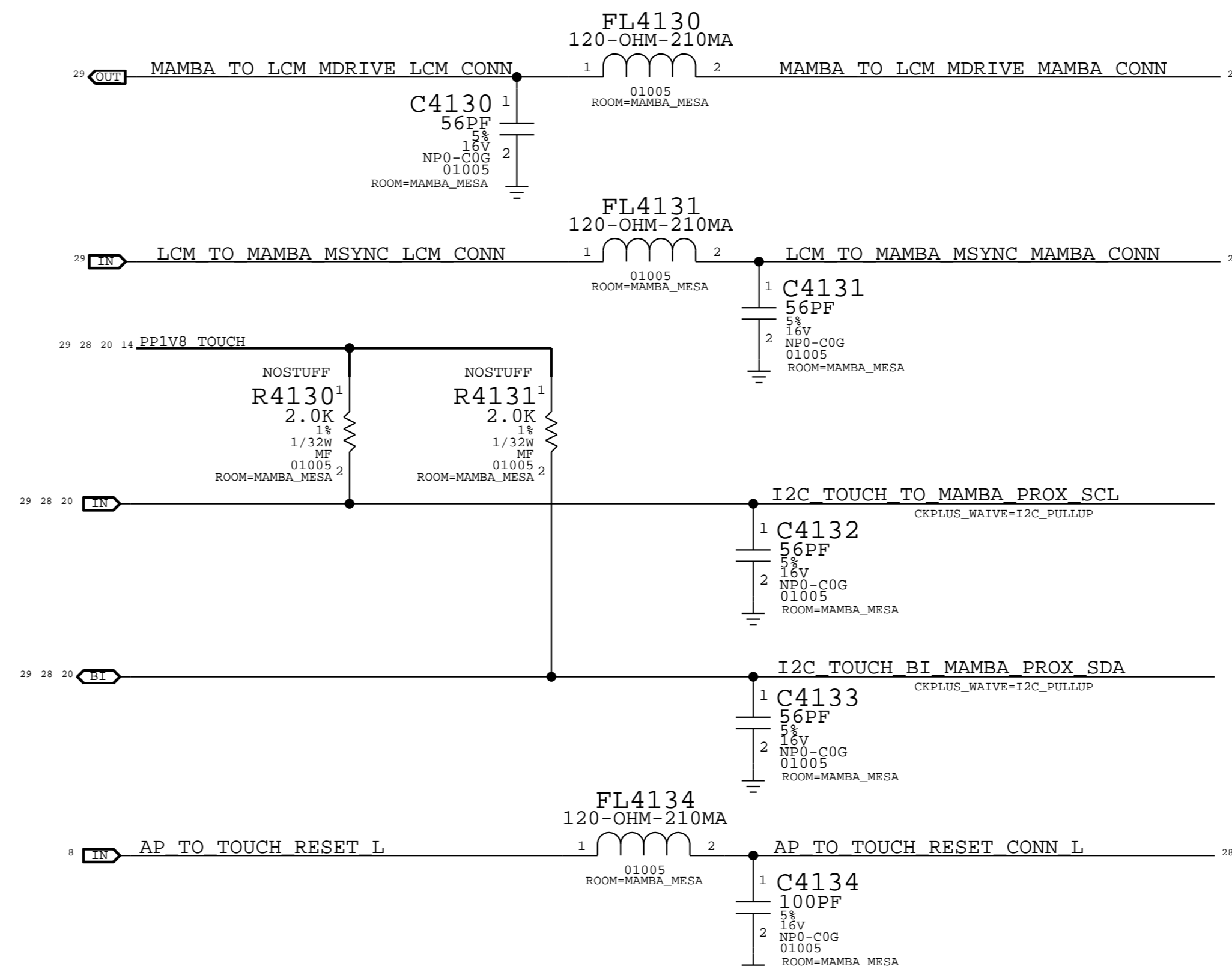


MAMBA POWER

NOTE: OUTPUT IMPEDANCE MUST BE >0.01-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.



MAMBA DIGITAL I/O



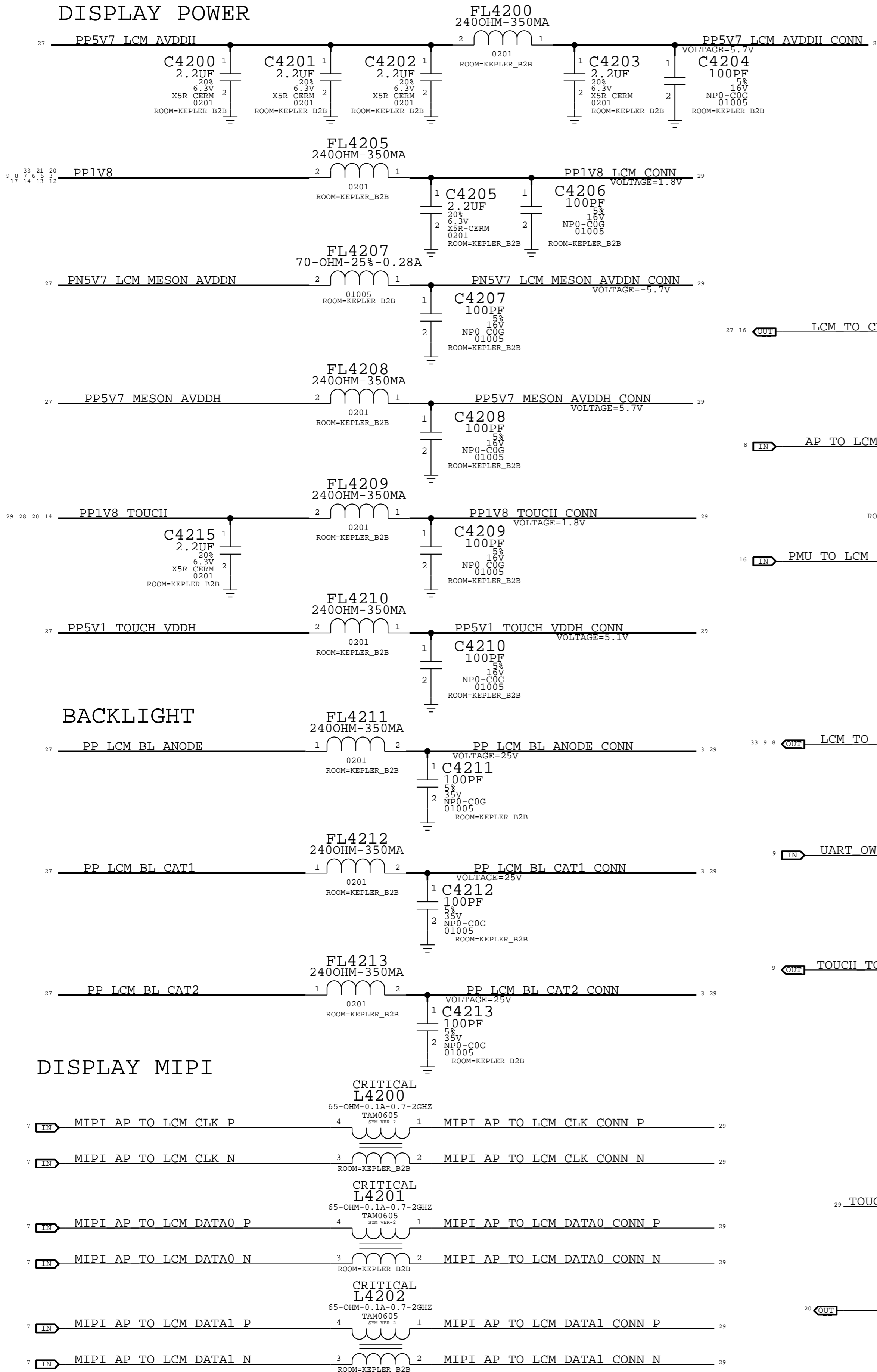
NOTE: TOUCH I2C PULL-UPS TO PP1V8_TOUCH INSIDE KEPLER
ADDING R4130, R4131 AS OPTION FOR TWEAKING VALUES.

SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE TOUCH:ORB & MESA B2B			
DRAWING NUMBER 051-1902		SIZE D	
REVISION A.0.0		BRANCH	
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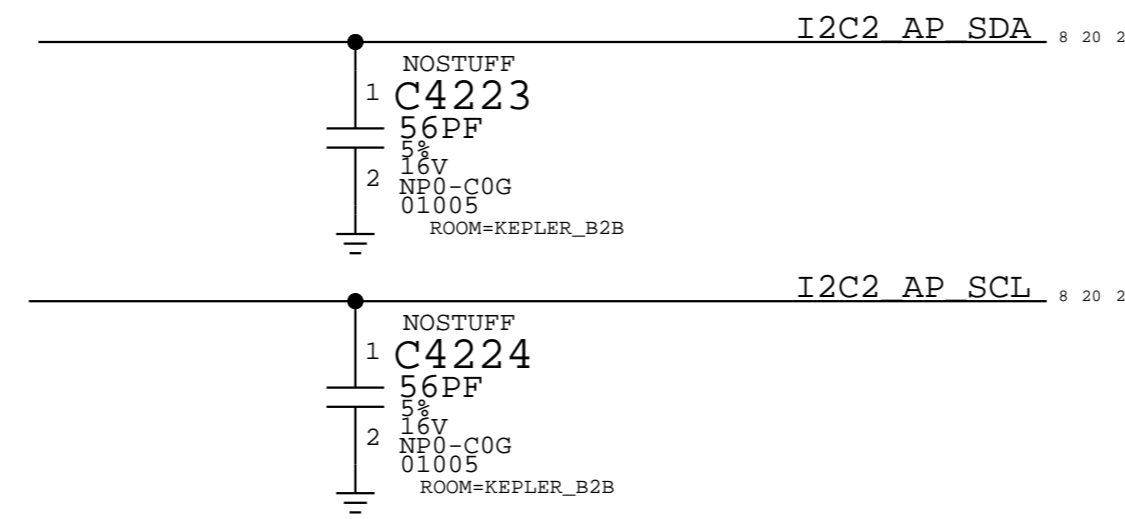
DISPLAY & TOUCH FLEX

DISPLAY CONNECTOR

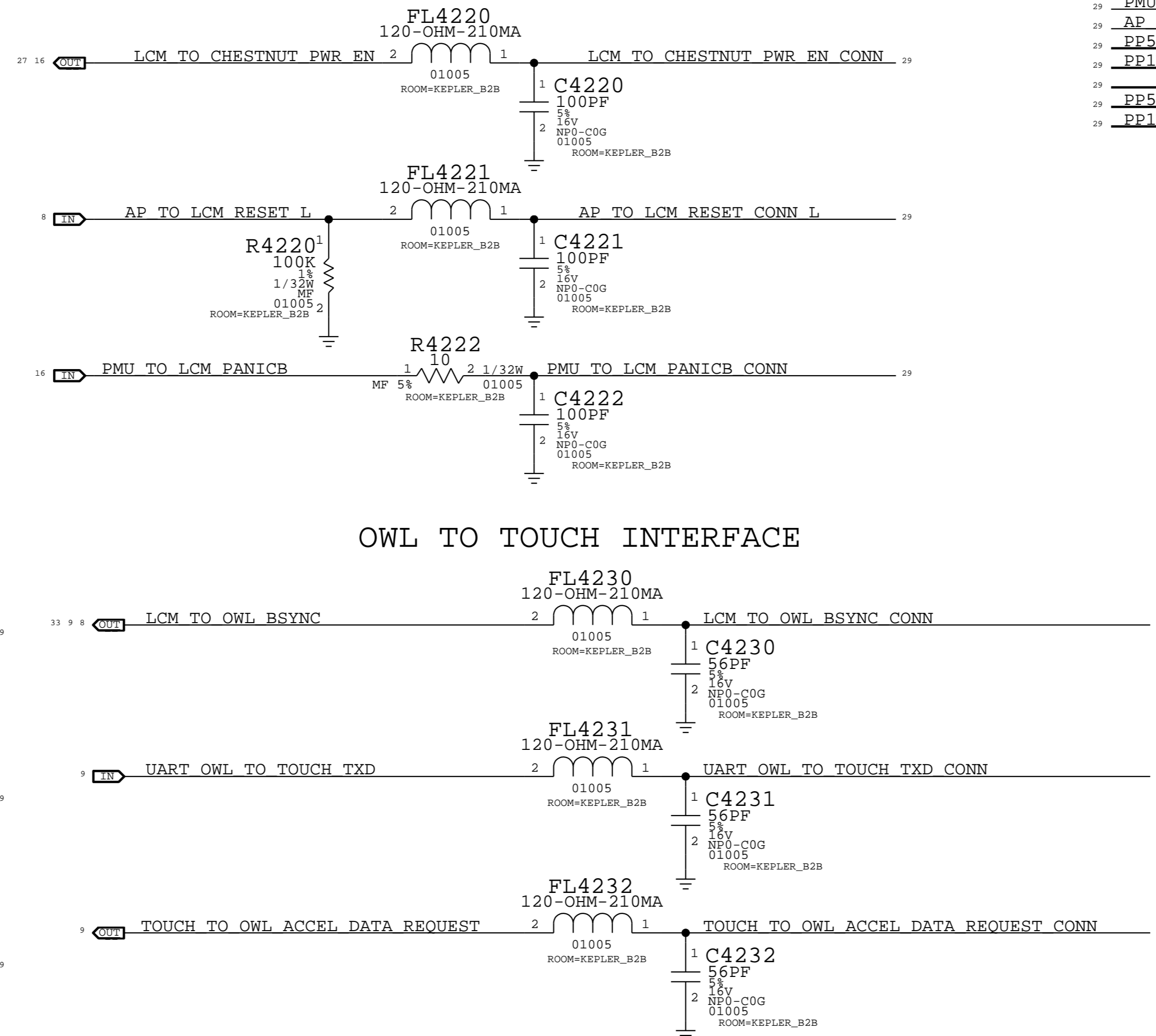
DISPLAY POWER



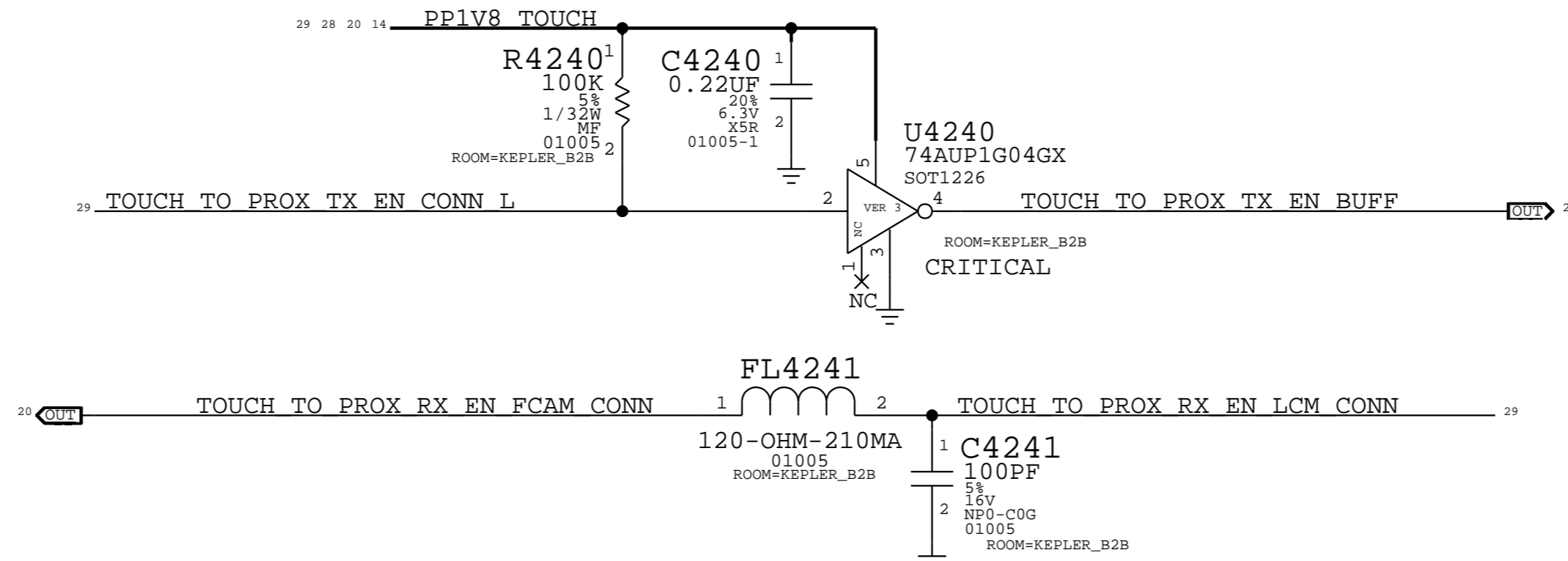
DISPLAY EEPROM I2C



DISPLAY CONTROL SIGNALS

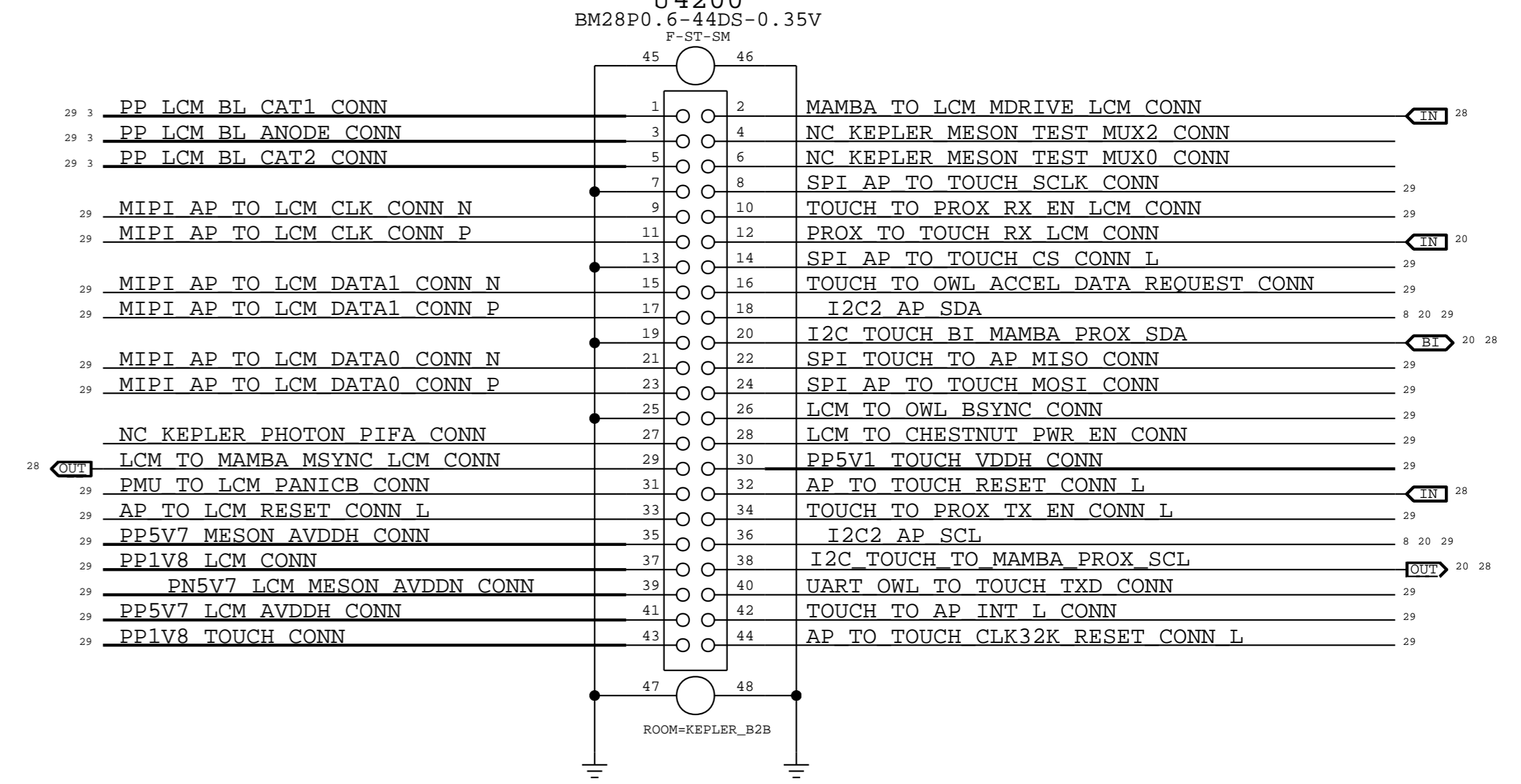


PROX TO TOUCH INTERFACE

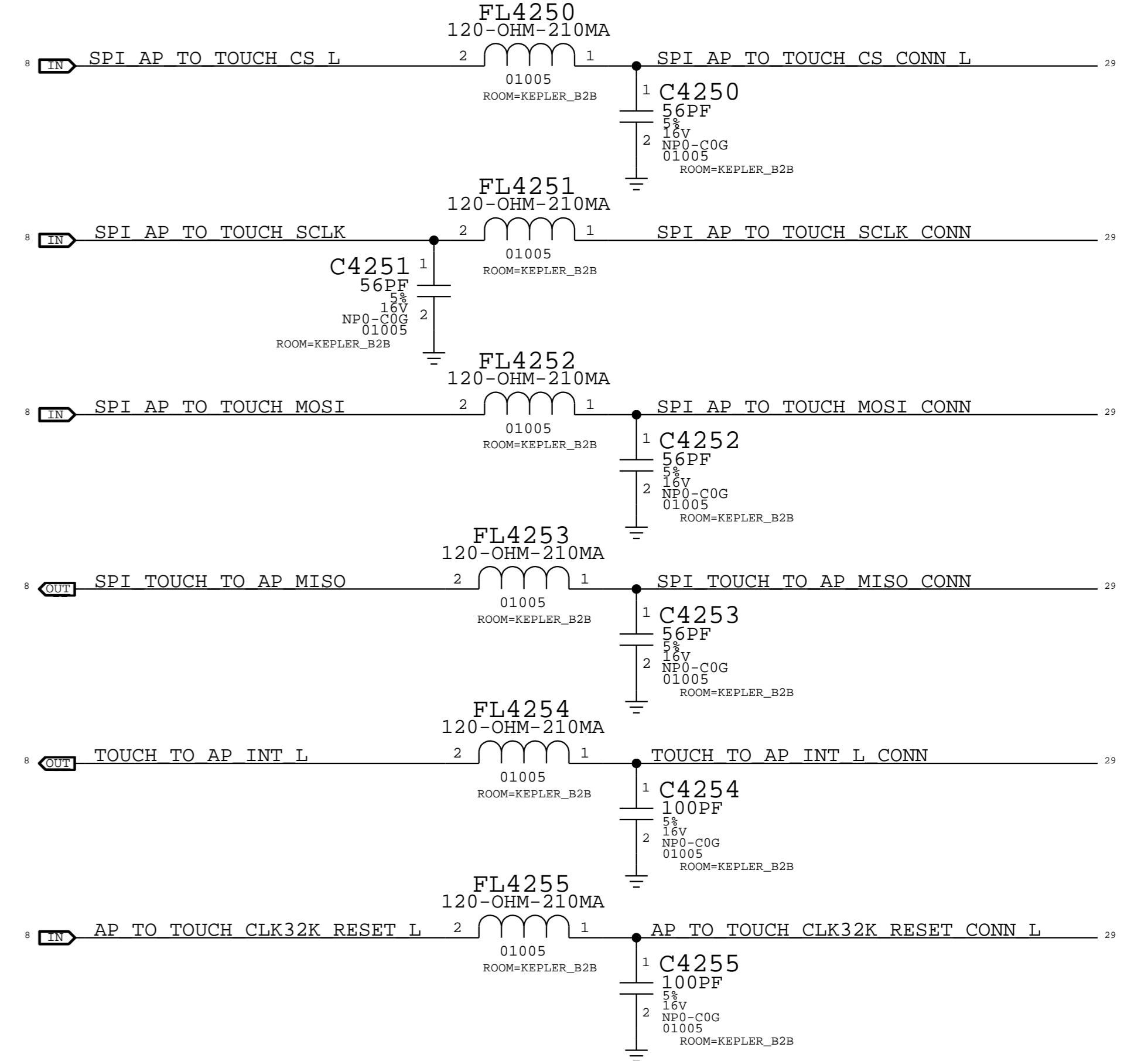


THIS ONE ON MLB ---> 516S00038 (RCPT)
516S00037 (PLUG)

CRITICAL



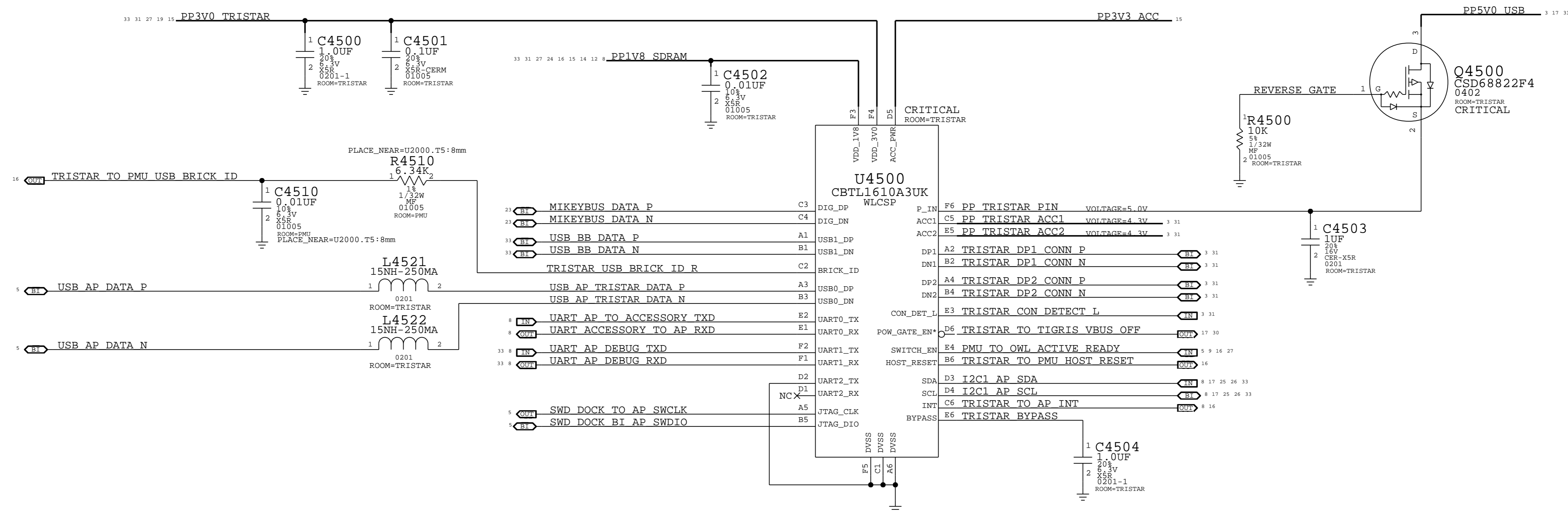
AP TO TOUCH INTERFACE



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Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
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TRISTAR 2 (A3)

APN: 343S0695



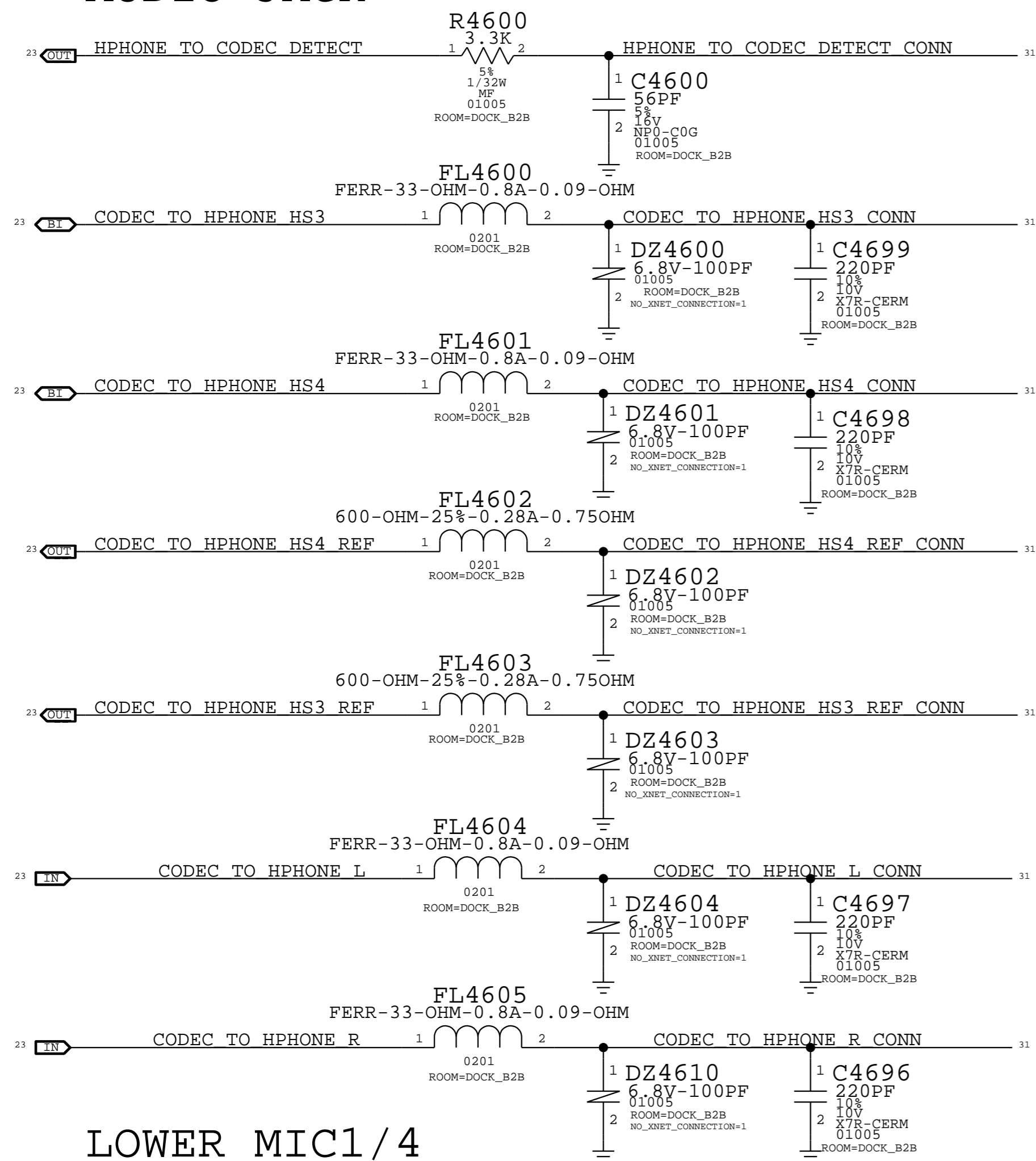
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P3MM-NSM
30 17 TRISTAR TO TIGRIS VBUS OFF 1 PP4500



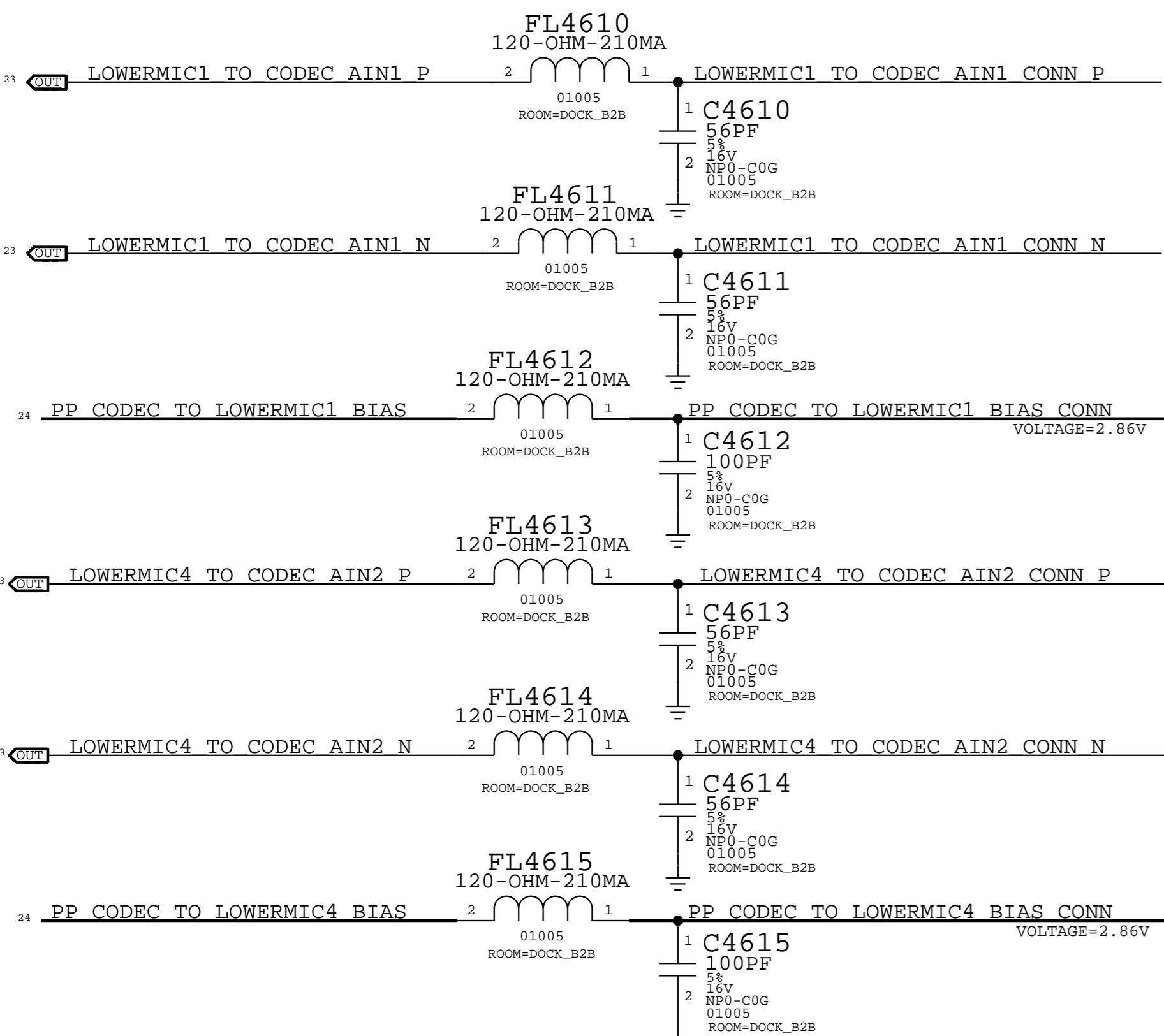
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	BRANCH		
	PAGE	45 OF 49	
	SHEET	30 OF 59	

DOCK FLEX CONNECTOR

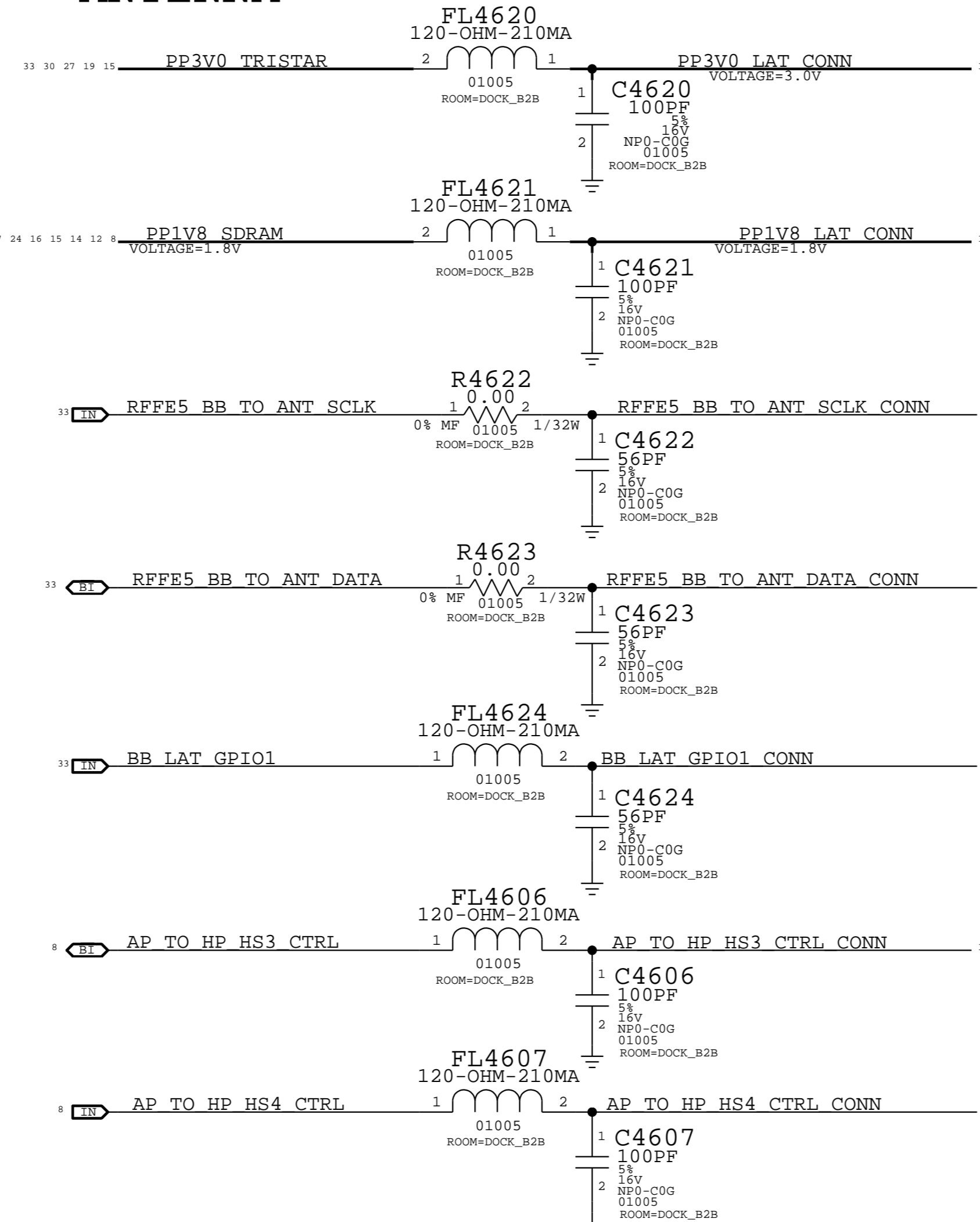
AUDIO JACK



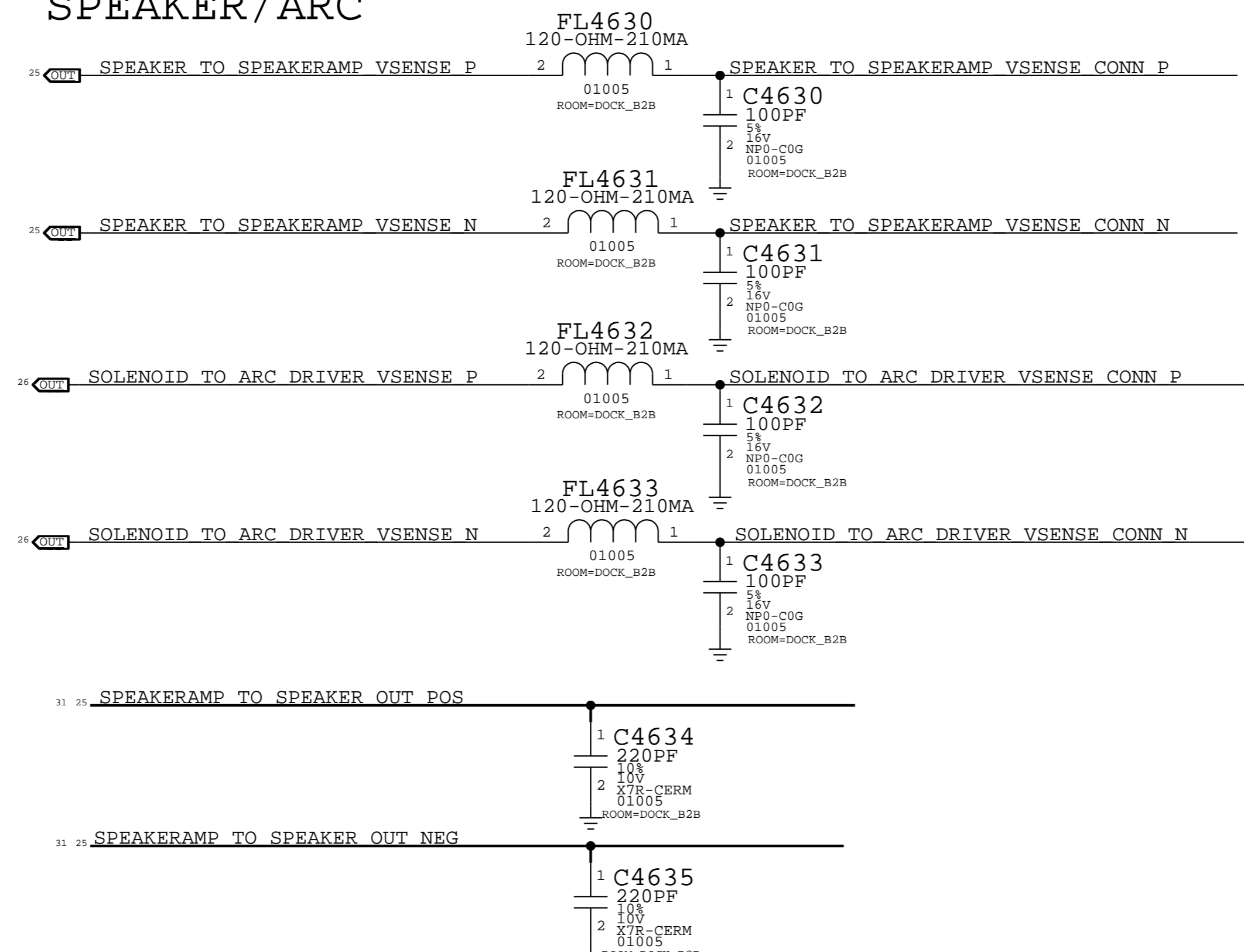
LOWER MIC1/4



ANTENNA

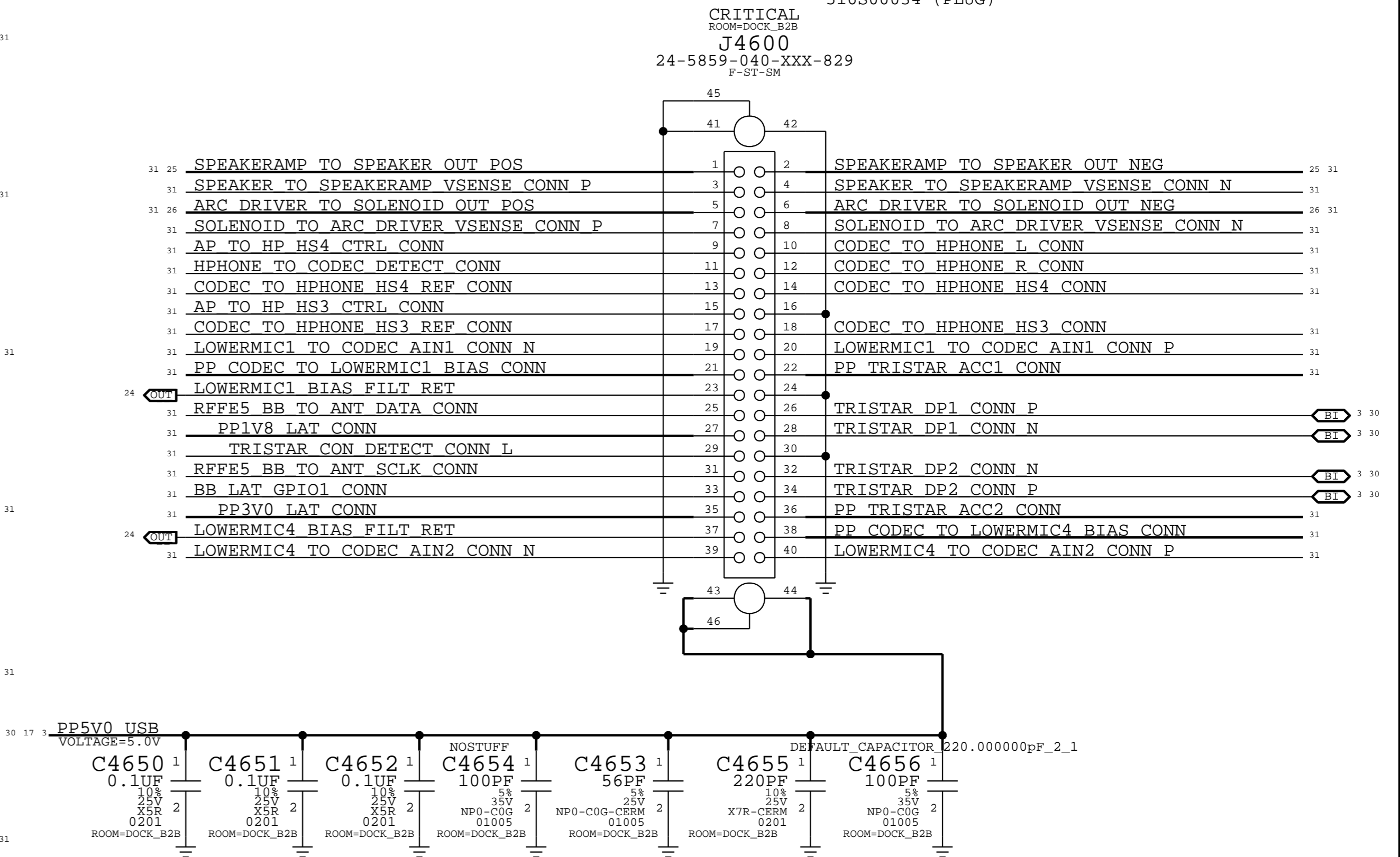


SPEAKER/ARC

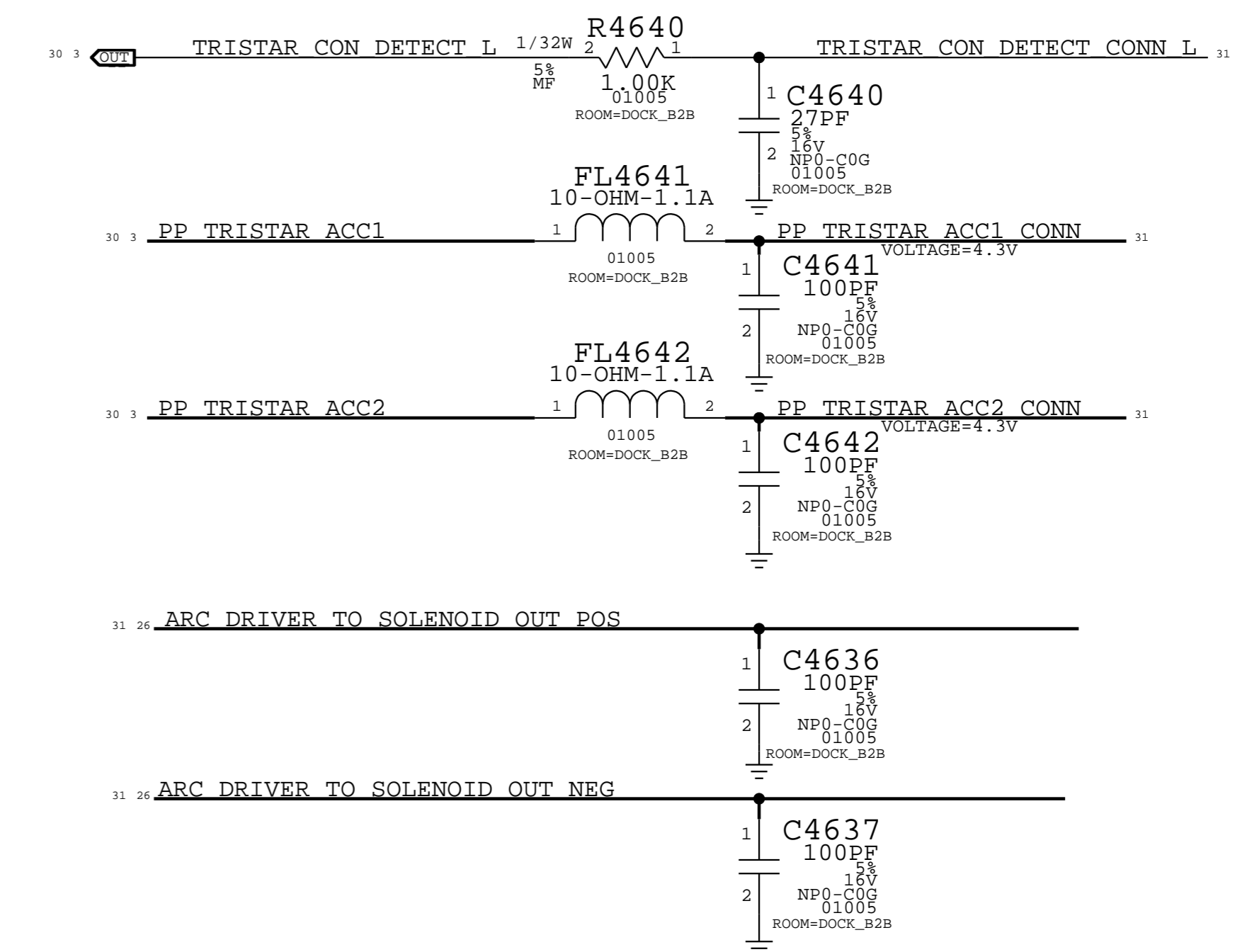


DOCK FLEX CONNECTOR

THIS ONE ON MLB ---> 516S00033 (RCPT)
516S00034 (PLUG)



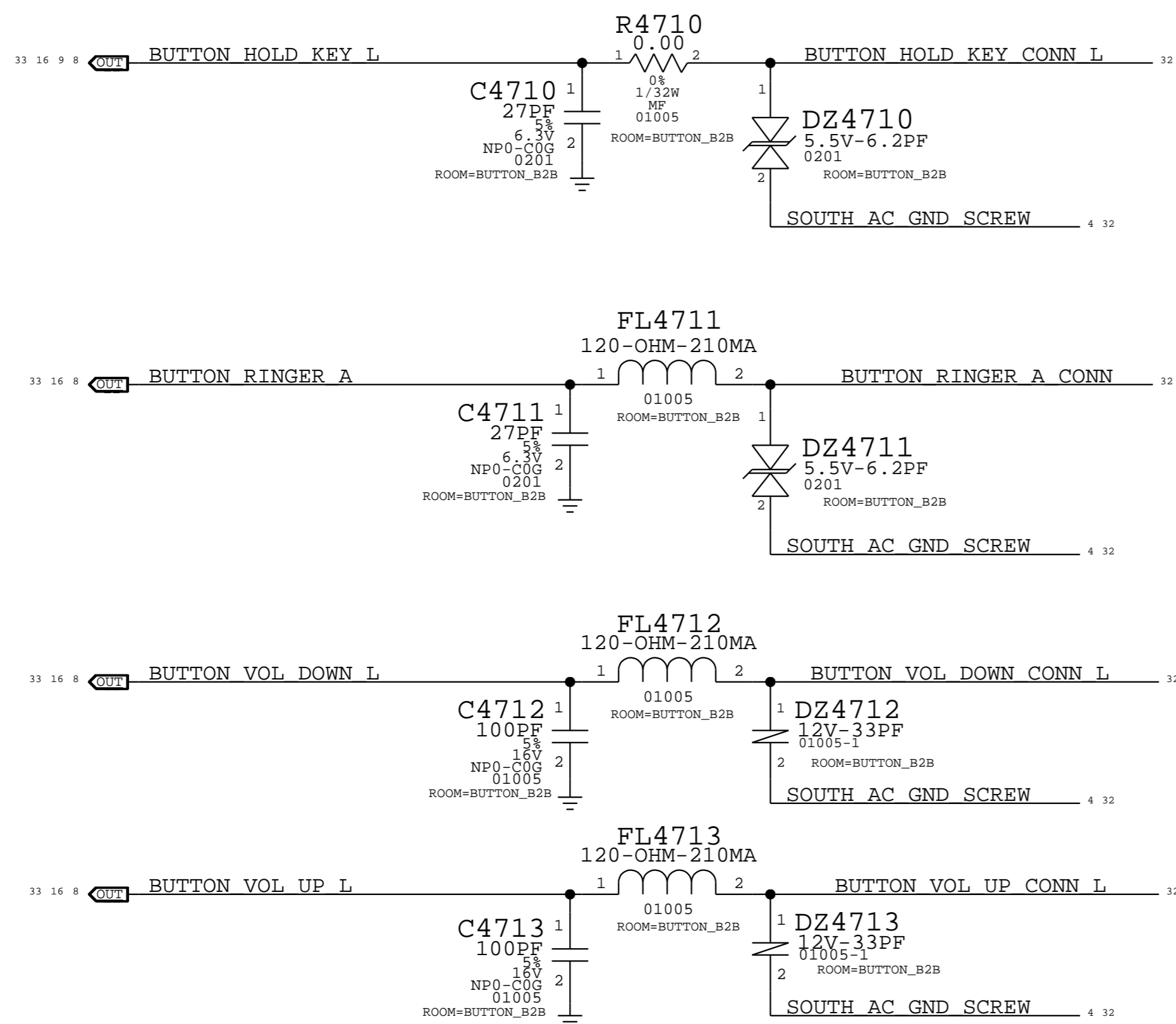
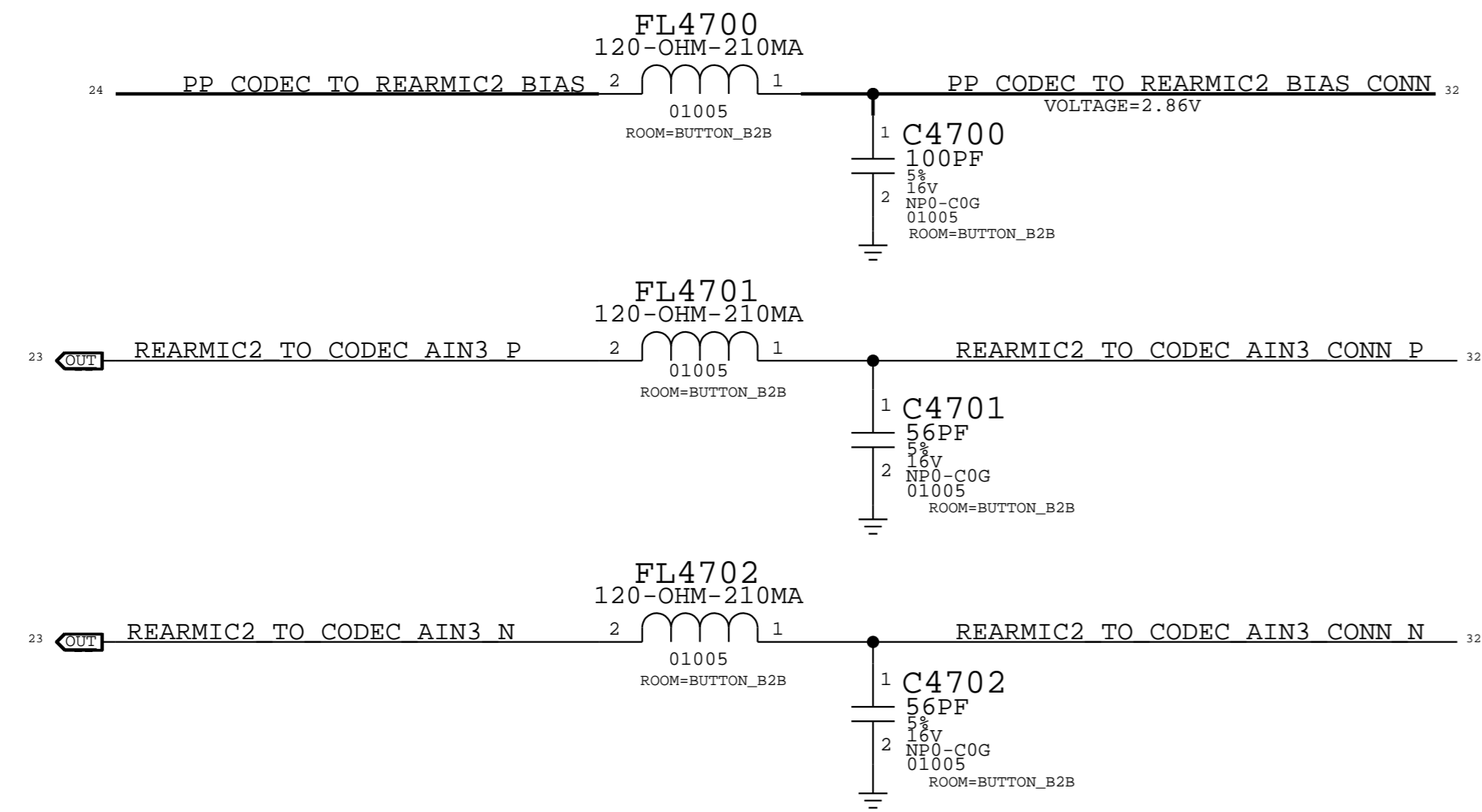
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		DRAWING NUMBER	051-1902
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		SHEET	31 OF 59

BUTTON FLEX

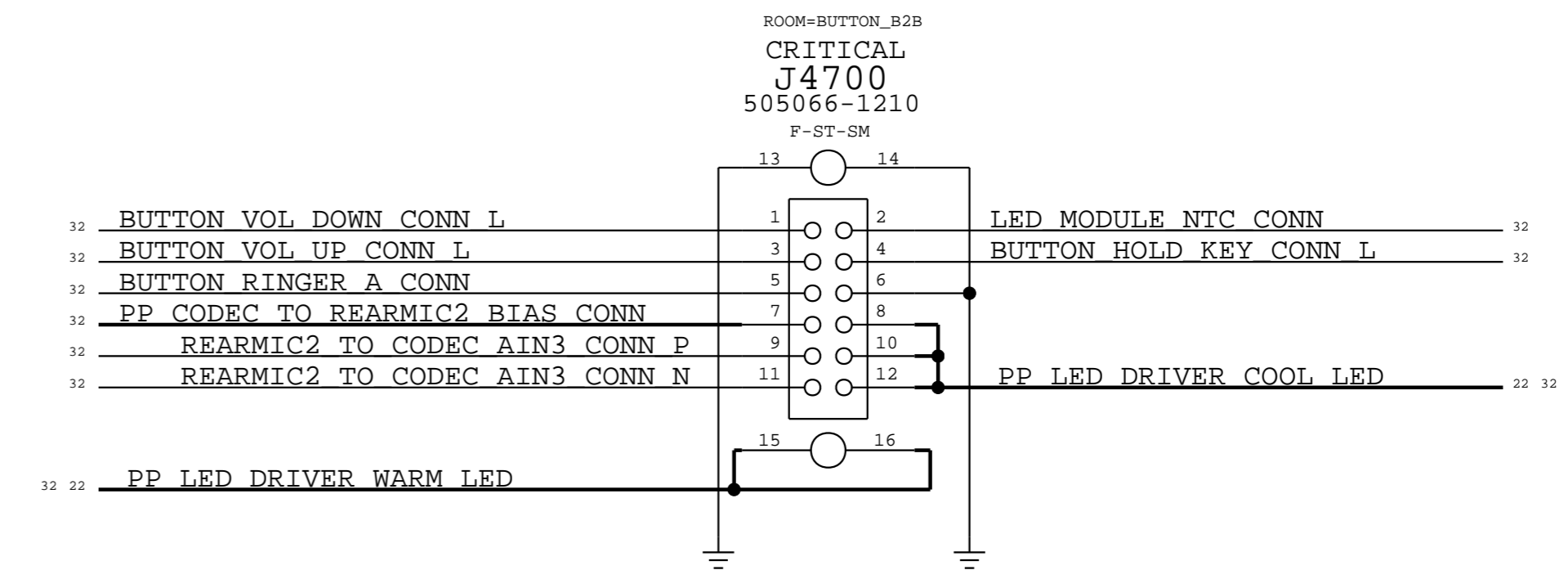
MIC2
ANC REF MIC



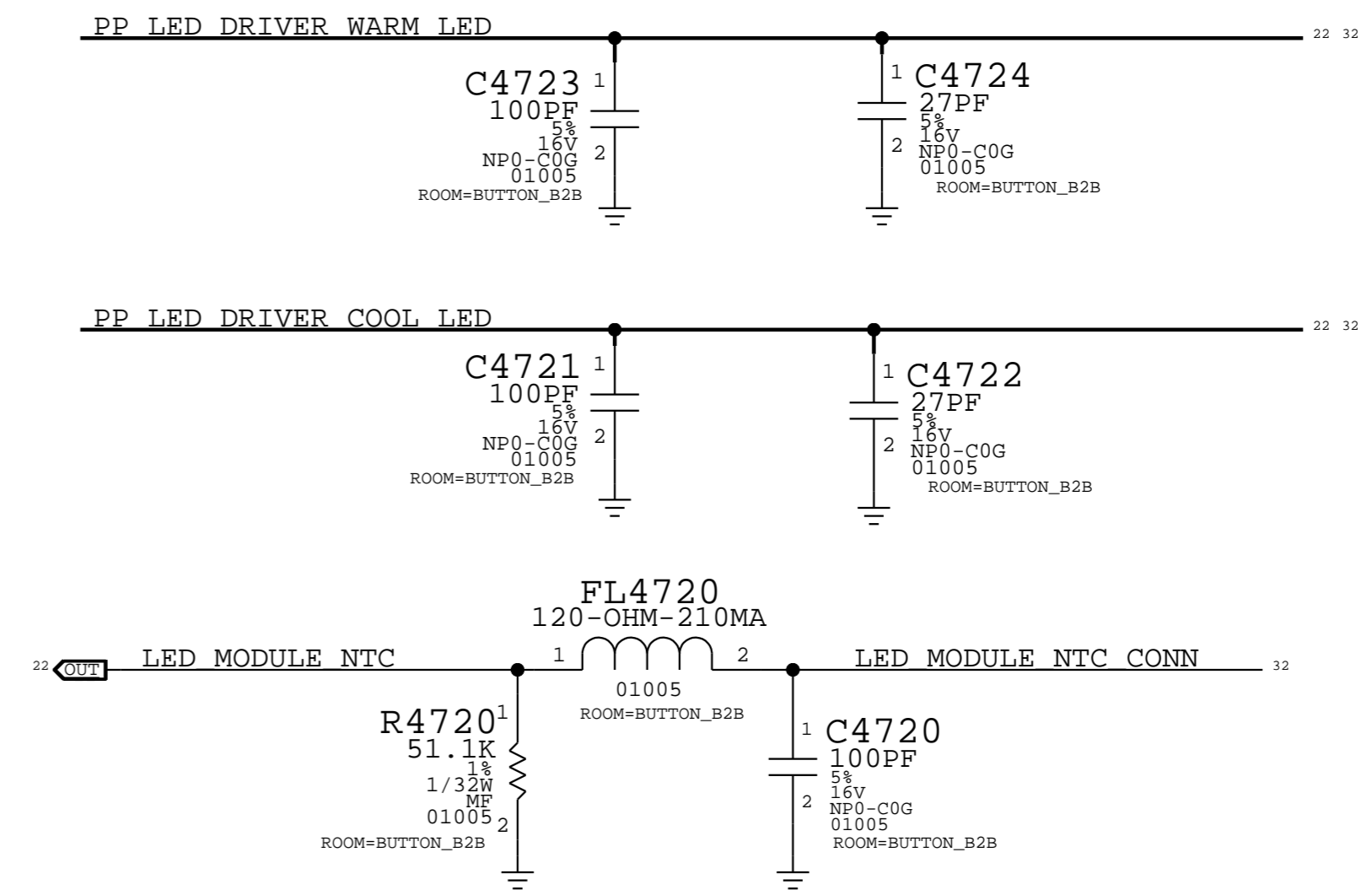
BUTTONS:
HOLD
RINGER
VOL UP/DOWN

BUTTON FLEX CONNECTOR

THIS ONE ON MLB ---> 516S00058 (RCPT)
516S00059 (PLUG)



STROBE:
WARM LED
COOL LED
MODULE NTC



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
I/O:BUTTON FLEX B2B			
		DRAWING NUMBER	051-1902
		REVISION	A.0.0
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		PAGE	47 OF 49
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BASEBAND, WLAN, BT & STOCKHOLM

1456

RADIO_MLB_MIMO	
SHARED POWER	
PP_VCC_MAIN	PP_VCC_MAIN
PP3V0_TRISTAR	PP3V0_TRISTAR SUBDESIGN_SUFFIX=RF
PP1V8_SDRAM	PP1V8_SDRAM
BASEBAND	
PCIE0_AP_TO_BB_TXD_P	PCIE0_AP_TO_BB_TXD_P
PCIE0_AP_TO_BB_TXD_N	PCIE0_AP_TO_BB_TXD_N
PCIE0_BB_TO_AP_RXD_P	PCIE0_BB_TO_AP_RXD_P
PCIE0_BB_TO_AP_RXD_N	PCIE0_BB_TO_AP_RXD_N
PCIE0_AP_TO_BB_REFCLK_P	PCIE0_AP_TO_BB_REFCLK_P
PCIE0_AP_TO_BB_REFCLK_N	PCIE0_AP_TO_BB_REFCLK_N
PCIE0_AP_TO_BB_RESET_L	PCIE0_AP_TO_BB_RESET_L
PCIE0_BB_BI_AP_CLKREQ_L	PCIE0_BB_BI_AP_CLKREQ_L
BB_TO_PMU_PCIE_HOST_WAKE_L	BB_TO_PMU_PCIE_HOST_WAKE_L
AP_TO_BB_PCIE_DEV_WAKE	AP_TO_BB_PCIE_DEV_WAKE
I2S_AP_TO_BB_LRCLK	I2S_AP_TO_BB_WS
I2S_AP_TO_BB_BCLK	I2S_AP_TO_BB_CLK
I2S_AP_TO_BB_DOUT	I2S_AP_TO_BB_TX
I2S_BB_TO_AP_DIN	I2S_BB_TO_AP_TX
AP_TO_BB_RADIO_ON_L	AP_TO_BBPMU_RADIO_ON_L
PMU_TO_BB_PMIC_RESET_L	PMU_TO_BB_PMIC_RESET_L
AP_TO_BB_RESET_L	AP_TO_BB_RST_L
BB_TO_AP_RESET_DETECT_L	BB_TO_AP_RESET_DET_L
BB_TO_LED_DRIVER_GSM_BURST_IND	BB_TO_LED_DRIVER_GSM_BURST_IND
AP_TO_BB_MESA_UP_L	AP_TO_BB_MESA_ON_L
BB_TO_AP_GPS_TIME_MARK	BB_TO_AP_GPS_TIME_MARK
AP_TO_BB_COREDUMP_TRIG	AP_TO_BB_COREDUMP_TRIG
BB_IPC_GPIO	AP_TO_BB_IPC_GPIO
LCM_TO_OWL_BSYNC	TOUCH_TO_BBPMU_FORCE_PWM
UART_OWL_TO_BB_TXD	UART_OWL_TO_BB_TX
UART_BB_TO_OWL_RXD	UART_BB_TO_OWL_TX
USB_BB_DATA_P	USB_BB_P
USB_BB_DATA_N	USB_BB_N
PMU_TO_BB_USB_VBUS_DETECT	USB_BB_VBUS_DETECT
SWD_AP_PERIPHERAL_SWCLK	SWD_CLK_BB_JTAG_TCK
SWD_AP_BI_BB_SWDIO	SWD_IO_BB_JTAG_TMS
RFFE5_BB_TO_ANT_SCLK	75_RFFE5_SCLK_BB
RFFE5_BB_TO_ANT_DATA	75_RFFE5_SDATA_BB
BB_LAT_GPIO1	RFFE_BUFFER_LAT_GPIO1
BB_TO_PMU_AMUX_LD011_SIM1	BB_TO_PMU_AMUX_LD011_SIM1
BB_TO_PMU_AMUX_SMPS1	BB_TO_PMU_AMUX_SMPS1
BB_TO_PMU_AMUX_SMPS3	BB_TO_PMU_AMUX_SMPS3
BB_TO_PMU_AMUX_SMPS4	BB_TO_PMU_AMUX_SMPS4
ANT	
50 AP UAT FEED	50_UPPER_ANT_FEED
50 AP WIFI 5G CONN ANT	50_WIFI_5G_CONN_ANT
AP TO STOCKHOLM ANT	STOCKHOLM_ANT
AP DEBUG	
PP1V8	PP1V8
DFU STATUS	DFU_STATUS
FORCE_DFU	FORCE_DFU
NC_PMU_AMUX_AY	PMU_AMUX_AY
NC_PMU_AMUX_BY	PMU_AMUX_BY
PMU_TO_SYSTEM_COLD_RESET_L	PMU_TO_SYSTEM_COLD_RESET_L
I2C0 AP_SCL	I2C0_AP_SCL
I2C0 AP_SDA	I2C0_AP_SDA
I2C1 AP_SCL	I2C1_AP_SCL
I2C1 AP_SDA	I2C1_AP_SDA
BUTTON_HOLD_KEY_L	BUTTON_HOLD_KEY_L
BUTTON_MENU_KEY_L	BUTTON_MENU_KEY_L
BUTTON_RINGER_A	BUTTON_RINGER_A
BUTTON_VOL_DOWN_L	BUTTON_VOL_DOWN_L
BUTTON_VOL_UP_L	BUTTON_VOL_UP_L
NC_PMU_GPIO20	PMU_GPIO20
NC_PMU_GPIO21	PMU_GPIO21
UART AP DEBUG TXD	AP_RESERVED0
UART AP DEBUG RXD	AP_RESERVED1
NC AP RESERVED2	AP_RESERVED2

WLAN

PCIE0_BB_TO_AP_TXD_P	PCIE0_BB_TO_AP_TXD_P
PCIE0_BB_TO_AP_TXD_N	PCIE0_BB_TO_AP_TXD_N
PCIE0_WLAN_TO_AP_TXD_P	PCIE0_WLAN_TO_AP_TXD_P
PCIE0_WLAN_TO_AP_TXD_N	PCIE0_WLAN_TO_AP_TXD_N
PCIE0_WLAN_TO_AP_RXD_P	PCIE0_WLAN_TO_AP_RXD_P
PCIE0_WLAN_TO_AP_RXD_N	PCIE0_WLAN_TO_AP_RXD_N
PCIE0_WLAN_REFCLK_P	PCIE0_WLAN_REFCLK_P
PCIE0_WLAN_REFCLK_N	PCIE0_WLAN_REFCLK_N
PCIE0_WLAN_RESET_L	PCIE0_WLAN_RESET_L
PCIE0_WLAN_DEV_WAKE	PCIE0_WLAN_DEV_WAKE
PCIE0_WLAN_CLKREQ_L	PCIE0_WLAN_CLKREQ_L
UART4_AP_TO_WLAN_TX	UART4_AP_TO_WLAN_TX
UART4_WLAN_TO_AP_RTS_L	UART4_WLAN_TO_AP_RTS_L
UART4_WLAN_TO_AP_RTS_N	UART4_WLAN_TO_AP_RTS_N
UART4_WLAN_TO_AP_CTS_L	UART4_WLAN_TO_AP_CTS_L
PMU_TO_WLAN_CLK32K	PMU_TO_WLAN_CLK32K
PMU_TO_WLAN_REG_ON	PMU_TO_WLAN_REG_ON
WLAN_TO_PMU_HOST_WAKE	WLAN_TO_PMU_HOST_WAKE
OWL_TO_WLAN_CONTEXT_A	OWL_TO_WLAN_CONTEXT_A
OWL_TO_WLAN_CONTEXT_B	OWL_TO_WLAN_CONTEXT_B

BLUETOOTH

I2S_AP_TO_BT_LRCLK	I2S_AP_TO_BT_LRCLK
I2S_AP_TO_BT_BCLK	I2S_AP_TO_BT_BCLK
I2S_AP_TO_BT_DOUT	I2S_AP_TO_BT_DOUT
I2S_BT_TO_AP_DIN	I2S_BT_TO_AP_DIN
UART1_AP_TO_BT_TXD	UART1_AP_TO_BT_TXD
UART1_AP_TO_BT_RTS_L	UART1_AP_TO_BT_RTS_L
UART1_BT_TO_AP_RXD	UART1_BT_TO_AP_RXD
UART1_BT_TO_AP_CTS_L	UART1_BT_TO_AP_CTS_L
PMU_TO_BT_REG_ON	PMU_TO_BT_REG_ON
BT_TO_PMU_HOST_WAKE	BT_TO_PMU_HOST_WAKE
AP_TO_BT_WAKE	AP_TO_BT_WAKE

STOCKHOLM

UART3_AP_TO_STOCKHOLM_TXD	UART3_AP_TO_STOCKHOLM_TXD
UART3_AP_TO_STOCKHOLM_RTS_L	UART3_AP_TO_STOCKHOLM_RTS_L
UART3_STOCKHOLM_TO_AP_TXD	UART3_STOCKHOLM_TO_AP_TXD
UART3_STOCKHOLM_TO_AP_RTS_L	UART3_STOCKHOLM_TO_AP_RTS_L
PMU_TO_STOCKHOLM_EN	PMU_TO_STOCKHOLM_EN
STOCKHOLM_TO_PMU_HOST_WAKE	STOCKHOLM_TO_PMU_HOST_WAKE
AP_TO_STOCKHOLM_DEV_WAKE	AP_TO_STOCKHOLM_DEV_WAKE
AP_TO_STOCKHOLM_FW_DLWD_REQ	AP_TO_STOCKHOLM_FW_DLWD_REQ

28 27 26 25 24 22 21 17 15 14
31 30 27 19 15
31 30 27 24 16 15 14 12 8

6 IN PCIE AP TO BB TXD P
6 IN PCIE AP TO BB TXD N
6 OUT PCIE BB TO AP RXD P
6 OUT PCIE BB TO AP RXD N
6 IN PCIE AP TO BB REFCLK P
6 IN PCIE AP TO BB REFCLK N
6 IN PCIE AP TO BB RESET L
6 BT PCIE BB BI AP_CLKREQ L
16 OUT BB TO PMU_PCIE_HOST_WAKE L
8 IN AP TO BB_PCIE_DEV_WAKE
8 IN I2S AP TO BB_LRCLK
8 IN I2S AP TO BB_BCLK
8 IN I2S AP TO BB_DOUT
8 OUT I2S_BB_TO_AP_DIN
8 IN AP TO BB_RADIO_ON_L
16 IN PMU_TO_BB_PMIC_RESET_L
8 IN AP TO_BB_RESET_L
8 OUT BB_TO_AP_RESET_DETECT_L
27 22 OUT BB_TO_LED_DRIVER_GSM_BURST_IND
8 IN AP_TO_BB_MESA_UP_L
8 IN BB_TO_AP_GPS_TIME_MARK
8 IN AP_TO_BB_COREDUMP_TRIG
8 IN BB_IPC_GPIO
29 9 IN LCM_TO_OWL_BSYNC
9 IN UART_OWL_TO_BB_TXD
9 OUT UART_BB_TO_OWL_RXD
30 BT USB_BB_DATA_P
30 BT USB_BB_DATA_N
16 IN PMU_TO_BB_USB_VBUS_DETECT
13 9 IN SWD_AP_PERIPHERAL_SWCLK
9 BT SWD_AP_BI_BB_SWDIO
31 OUT RFFE5_BB_TO_ANT_SCLK
31 BT RFFE5_BB_TO_ANT_DATA
31 OUT BB_LAT_GPIO1
16 OUT BB_TO_PMU_AMUX_LD011_SIM1
16 OUT BB_TO_PMU_AMUX_SMPS1
16 OUT BB_TO_PMU_AMUX_SMPS3
16 OUT BB_TO_PMU_AMUX_SMPS4
4 OUT 50 AP UAT FEED
4 OUT 50 AP WIFI 5G CONN ANT
4 OUT AP TO STOCKHOLM ANT
21 20 17 14 13 12 9 8 7 6 5 3 2 1
8 3 IN DFU STATUS
8 3 OUT FORCE_DFU
8 3 OUT NC_PMU_AMUX_AY
8 3 OUT NC_PMU_AMUX_BY
16 9 5 3 OUT PMU_TO_SYSTEM_COLD_RESET_L
27 16 8 BT I2C0 AP_SCL
27 16 8 BT I2C0 AP_SDA
30 26 25 17 8 BT I2C1 AP_SCL
30 26 25 17 8 BT I2C1 AP_SDA
32 16 9 8 OUT BUTTON_HOLD_KEY_L
28 16 9 8 OUT BUTTON_MENU_KEY_L
32 16 8 OUT BUTTON_RINGER_A
32 16 8 OUT BUTTON_VOL_DOWN_L
32 16 8 OUT BUTTON_VOL_UP_L
32 16 8 OUT NC_PMU_GPIO20
32 16 8 OUT NC_PMU_GPIO21
30 8 IN UART AP DEBUG TXD
30 8 OUT UART AP DEBUG RXD
30 8 OUT NC AP RESERVED2

MIS-NAMED NET. GPS_TIME_MARK ACTUALLY GOES FROM AP TO BB.

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	0004536627	PRODUCTION RELEASED		2015-07-21

N71 RADIO_MLB_MIMO - PVT

JULY 07, 2015

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3	3	FE: ANT CONNECTORS AND UAT TUNER
4	30	DEBUG CONN & TEST POINTS
5	31	CELLULAR BASEBAND: POWER1
6	32	CELLULAR BASEBAND: POWER2
7	33	CELLULAR BASEBAND: CONTROL AND INTERFACES
8	34	CELLULAR BASEBAND: GPIOs
9	35	CELLULAR PMU: CONTROL AND CLOCKS
10	36	CELLULAR PMU: SWITCHERS AND LDOS
11	37	CELLULAR PMU: ET MODULATOR
12	38	CELLULAR TRANSCEIVER: POWER
13	39	CELLULAR TRANSCEIVER: PRX PORTS
14	40	CELLULAR TRANSCEIVER: DRX/GPS PORTS
15	41	CELLULAR TRANSCEIVER: TX PORTS
16	42	CELLULAR FRONT END: LB PAD
17	43	CELLULAR FRONT END: MB PAD
18	44	CELLULAR FRONT END: HB PAD
19	45	CELLULAR FRONT END: 2G PA
20	46	CELLULAR FRONT END: LB ASM
21	47	CELLULAR FRONT END: MB-HB ASM
22	48	CELLULAR FRONT END: DIVERSITY
23	49	SIM
24	50	WIFI/BT: WIFI/BT MODULE
25	51	STOCKHOLM

ROW/RF2 HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	ROW
152S1990	1	3.0NH, INDUCTOR	L4105_RF	RF2
131S0377	1	1.2PF, CAPACITOR	C4108_RF	RF2
152S2007	1	8.2NH, INDUCTOR	L4401_RF	ROW
131S0426	1	22PF, CAPACITOR	C4405_RF	ROW
152S2001	1	2.4NH, INDUCTOR	C4405_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4406_RF	RF2
152S2044	1	2.2NH, INDUCTOR	C4406_RF	ROW
152S2021	1	1.5NH, INDUCTOR	C4406_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4407_RF	ROW
152S2056	1	5.6NH, INDUCTOR	L4403_RF	ROW
131S0429	1	8.2PF, CAPACITOR	C4407_RF	ROW
152S2036	1	2.5NH, INDUCTOR	C4407_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4408_RF	RF2
152S00143	1	15NH, INDUCTOR	L4404_RF	ROW
131S0823	1	33PF, CAPACITOR	C4408_RF	ROW
152S2051	1	1.3NH, INDUCTOR	C4408_RF	RF2
152S2042	1	1.8NH, INDUCTOR	C4409_RF	RF2
117S0108	1	51 OHM, RESISTOR	L4410_RF	ROW
131S0363	1	0.6PF, CAPACITOR	L4410_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3910_RF	ROW
152S00026	1	3.5NH, INDUCTOR	L3910_RF	RF2
152S2039	1	3.8NH, INDUCTOR	L3911_RF	ROW
117S0201	1	0 OHM, RESISTOR	L3911_RF	RF2
131S0279	1	1.3PF, CAPACITOR	L3919_RF	ROW
152S2045	1	3.0NH, INDUCTOR	L3919_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3912_RF	RF2
131S0599	1	1.5PF, CAPACITOR	C3922_RF	RF2
131S0630	1	27PF, CAPACITOR	C3911_RF	RF2

LB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	ROW
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	RF2
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	RFC
353S00541	1	IC, PWR AMP, LB_PAD, PT	ULBPA_RF	DARWIN

MB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	ROW
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	RF2
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	RFC
353S00477	1	IC, PWR AMP, MB_PAD, PT	UMBPA_RF	DARWIN

RFC HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	RFC
152S2007	1	8.2NH, INDUCTOR	L4401_RF	RFC
131S0426	1	22PF, CAPACITOR	C4405_RF	RFC
152S2044	1	2.2NH, INDUCTOR	C4406_RF	RFC
131S0631	1	0.3PF, CAPACITOR	L4407_RF	RFC
152S2056	1	5.6NH, INDUCTOR	L4403_RF	RFC
131S0429	1	8.2PF, CAPACITOR	C4407_RF	RFC
152S00143	1	15NH, INDUCTOR	L4404_RF	RFC
131S0823	1	33PF, CAPACITOR	C4408_RF	RFC
117S0108	1	51 OHM, RESISTOR	L4410_RF	RFC
152S00052	1	3.4NH, INDUCTOR	L3910_RF	RFC
152S2039	1	3.8NH, INDUCTOR	L3911_RF	RFC
131S0279	1	1.3PF, CAPACITOR	L3919_RF	RFC

DARWIN HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	DARWIN
152S2007	1	8.2NH, INDUCTOR	L4401_RF	DARWIN
131S0426	1	22PF, CAPACITOR	C4405_RF	DARWIN
152S2044	1	2.2NH, INDUCTOR	C4406_RF	DARWIN
131S0631	1	0.3PF, CAPACITOR	L4407_RF	DARWIN
152S2056	1	5.6NH, INDUCTOR	L4403_RF	DARWIN
131S0429	1	8.2PF, CAPACITOR	C4407_RF	DARWIN
152S00143	1	15NH, INDUCTOR	L4404_RF	DARWIN
131S0823	1	33PF, CAPACITOR	C4408_RF	DARWIN
117S0108	1	51 OHM, RESISTOR	L4410_RF	DARWIN
152S00052	1	3.4NH, INDUCTOR	L3910_RF	DARWIN
152S2039	1	3.8NH, INDUCTOR	L3911_RF	DARWIN
131S0279	1	1.3PF, CAPACITOR	L3919_RF	DARWIN

LAT DIPLEXER1

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	ROW
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RF2
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RFC
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	DARWIN

HB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00376	1	IC, PWR AMP, HB_PAD, TQS	UHBPA_RF	ROW
353S4494	1	IC, PWR AMP, HB_PAD, AVAGO	UHBPA_RF	RF2
353S00376	1	IC, PWR AMP, HB_PAD, TQS	UHBPA_RF	RFC
353S00478	1	IC, PWR AMP, HB_PAD, PT	UHBPA_RF	DARWIN

19.2MHZ XTAL ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ
197S0598	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ

VINYL

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
337S00176	1	IC, VINYL	U5101_RF	ROW
337S00176	1	IC, VINYL	U5101_RF	RF2

VINYL RESISTOR


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
117S0161	1	0 OHM, RESISTOR	R3402_RF	RFC
117S0161	1	0 OHM, RESISTOR	R3402_RF	DARWIN

HW_REV1_ID RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
118S0646	1	51.1 KOHM, RESISTOR	R3503_RF	DARWIN

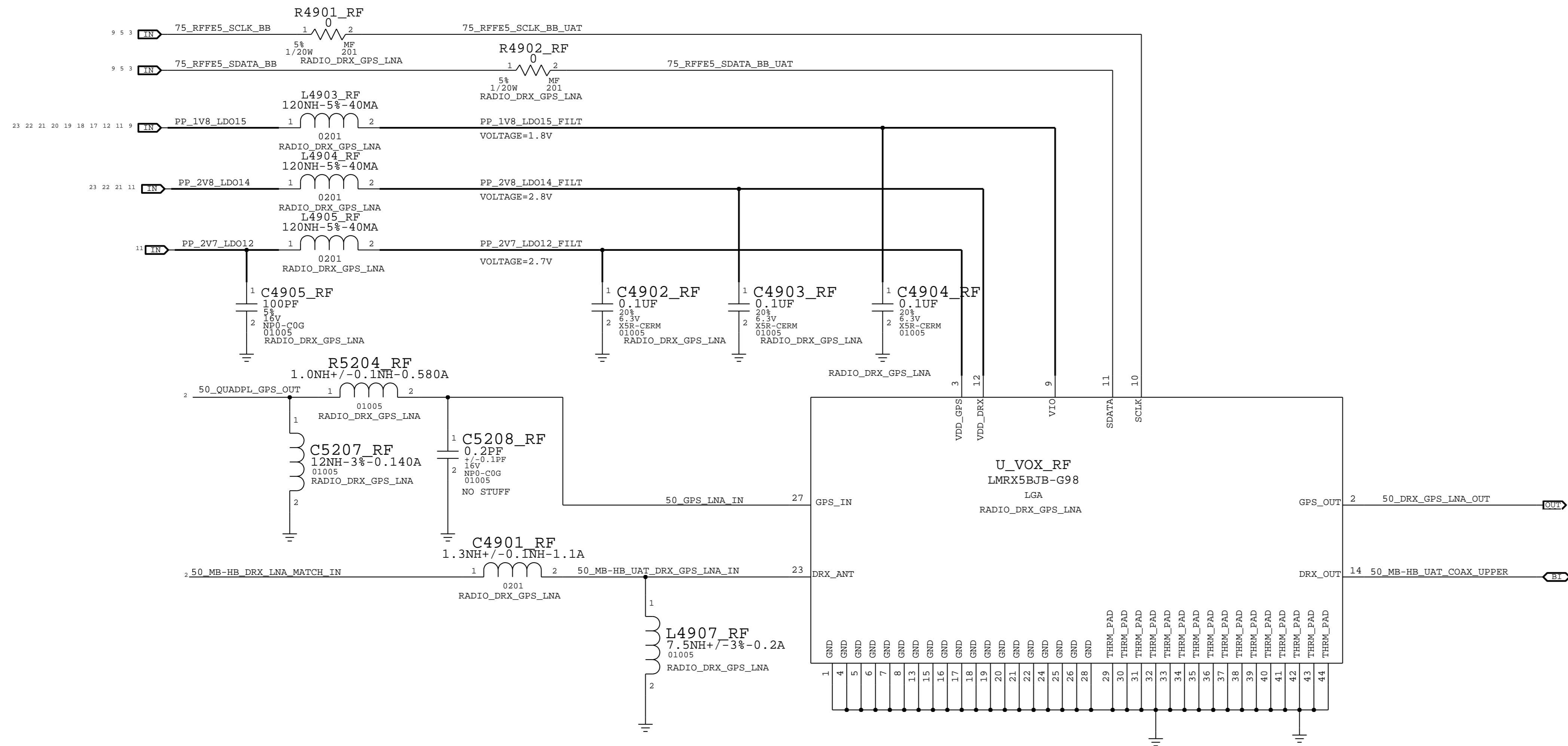
SIM ESD DIODE ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S00042	377S0163	ALTERNATE	VR301_RF	ON SEMI ESD DIODE

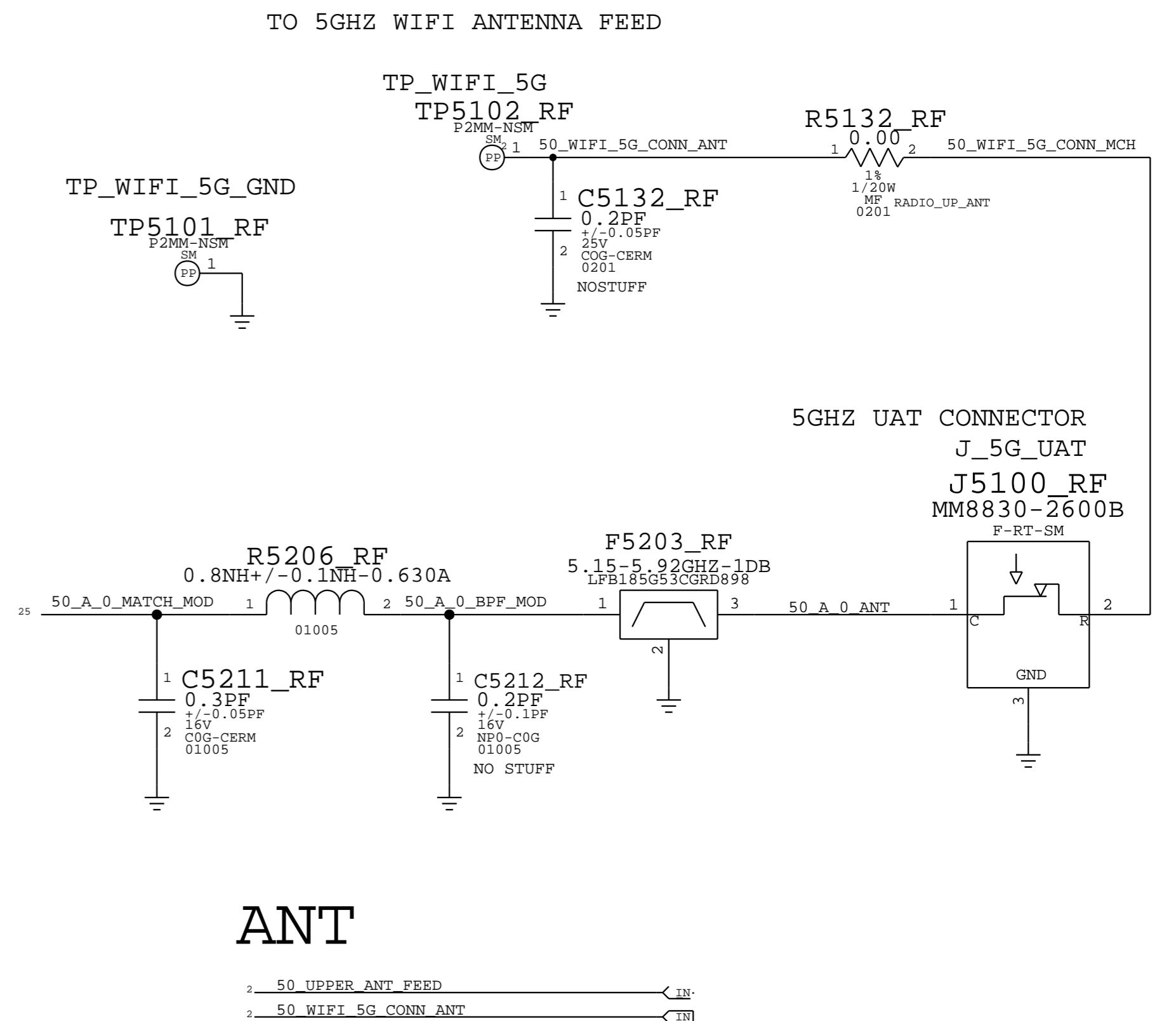
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		REVISION	A.0.0
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N71-SPECIFIC RADIO PAGE 2

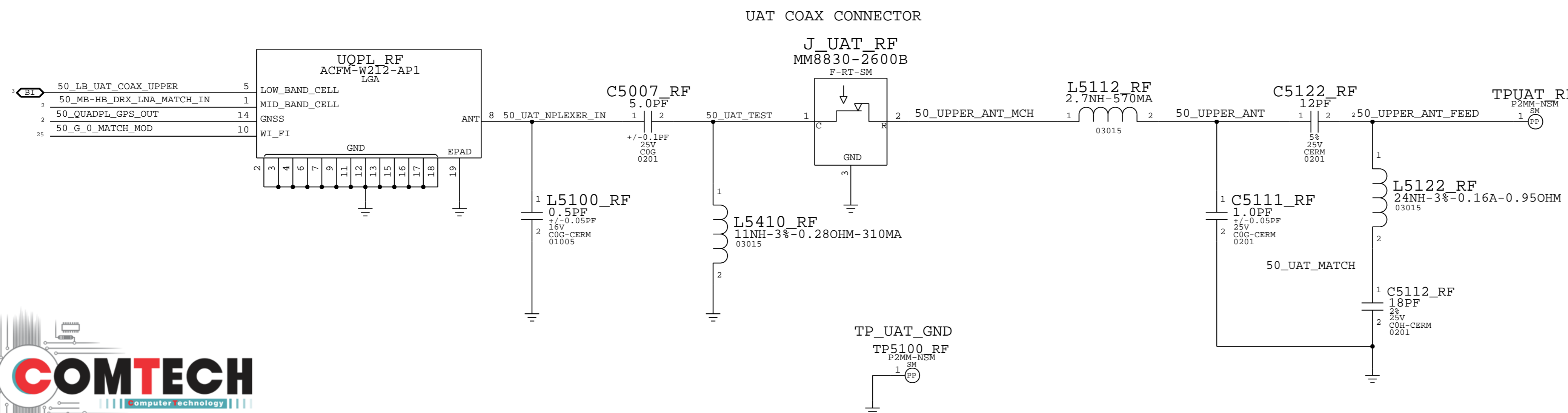
DIVERSITY LNA



WIFI ANT FEED



UAT ANT FEED



ANT

2 50 UPPER ANT FEED
2 50 WIFI 5G CONN ANT

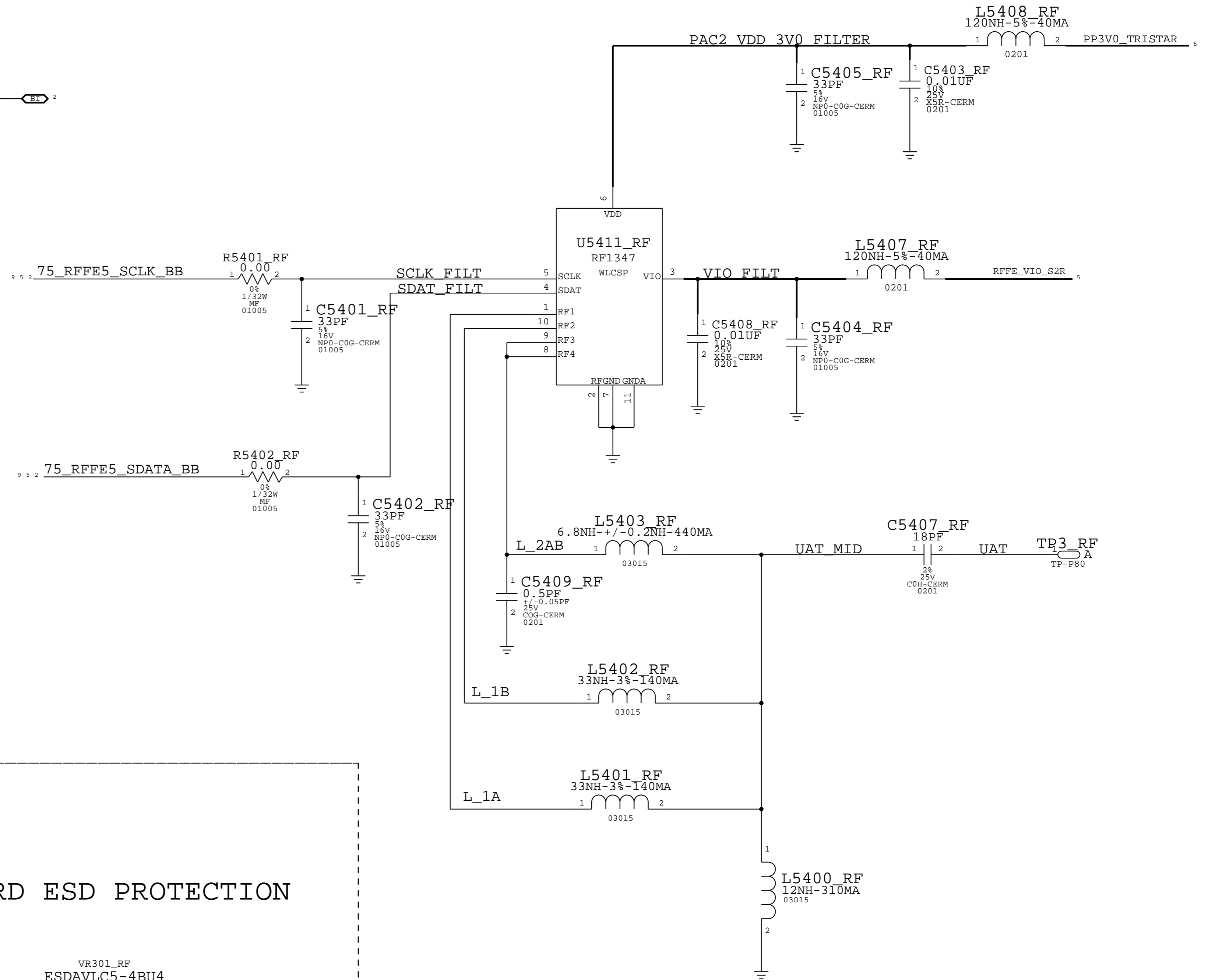
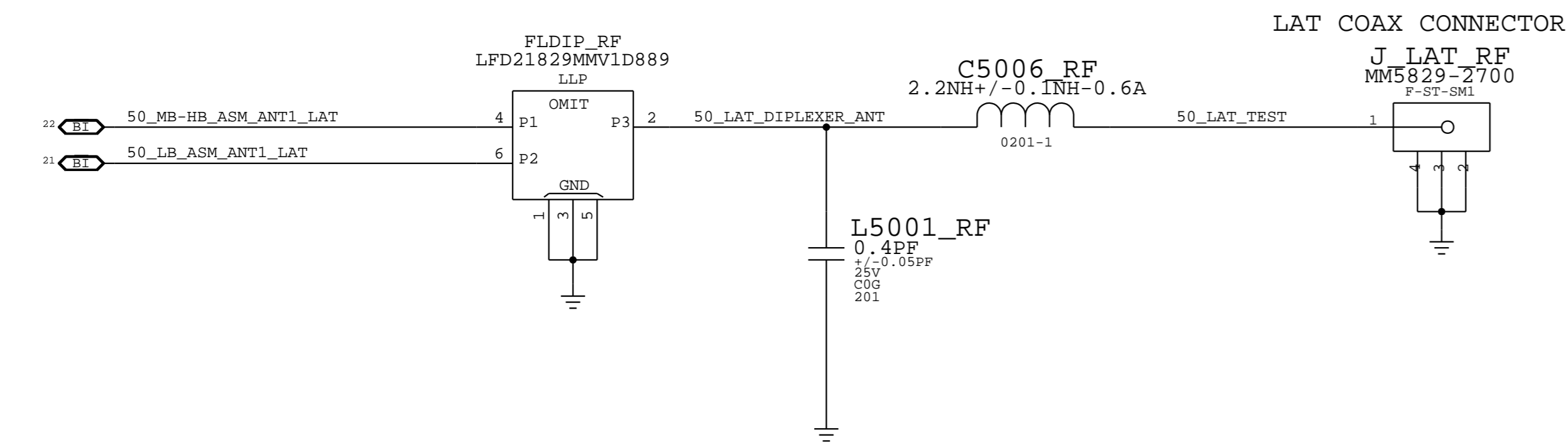
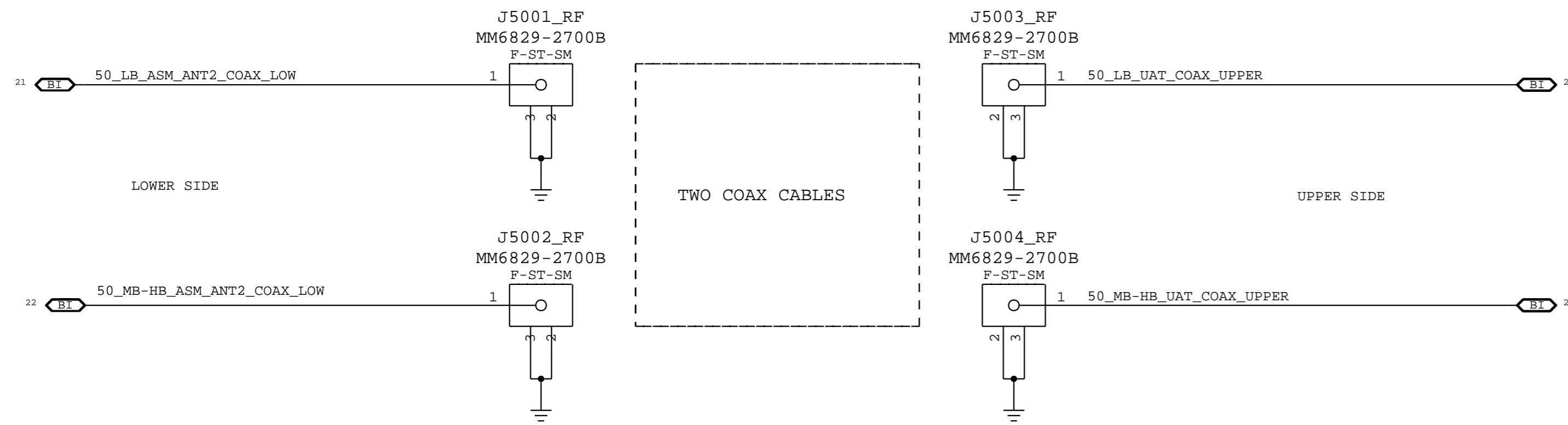


PAGE TITLE ELNA & UAT ANT FEED		DRAWING NUMBER 051-1902	SIZE D
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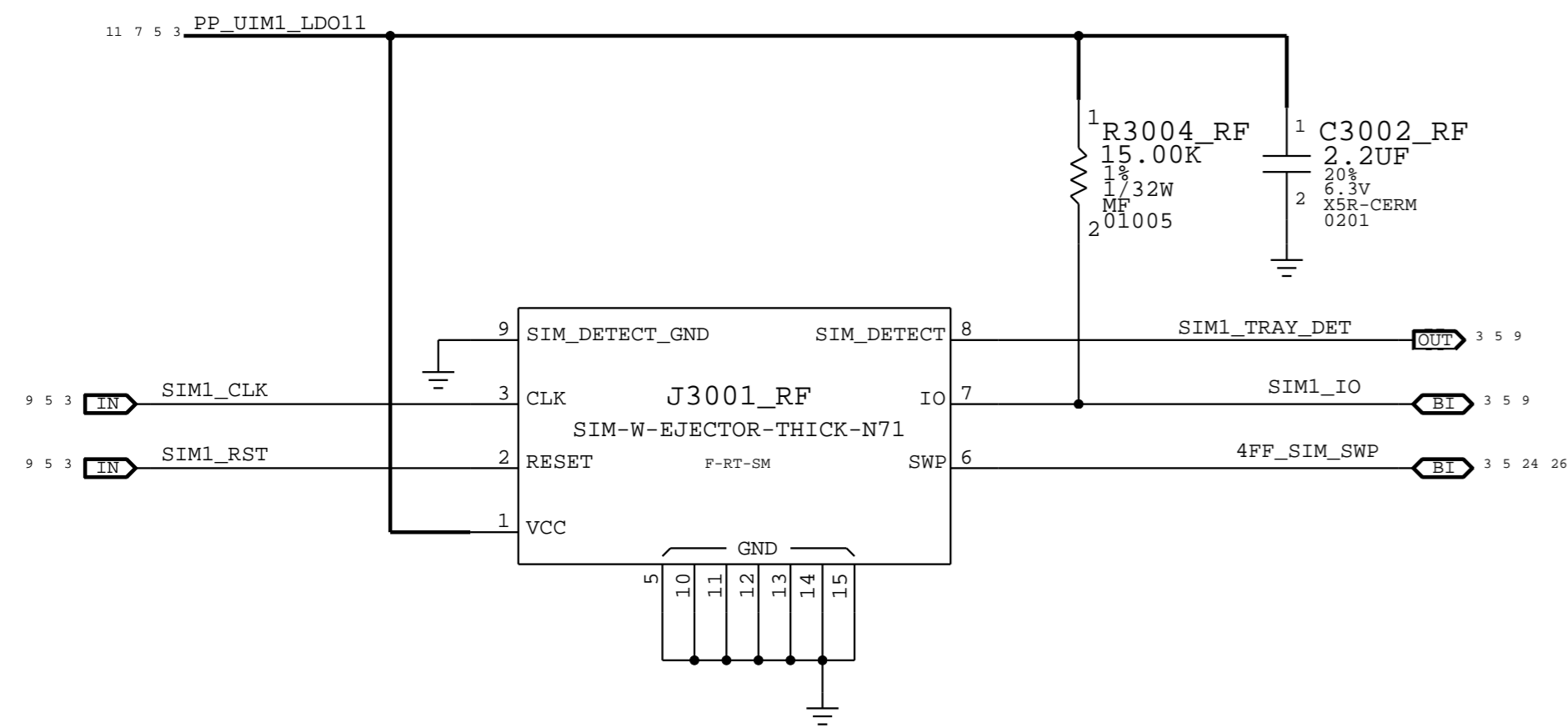
N71-SPECIFIC RADIO PAGE 3

ANTENNA FEEDS AND CONNECTORS

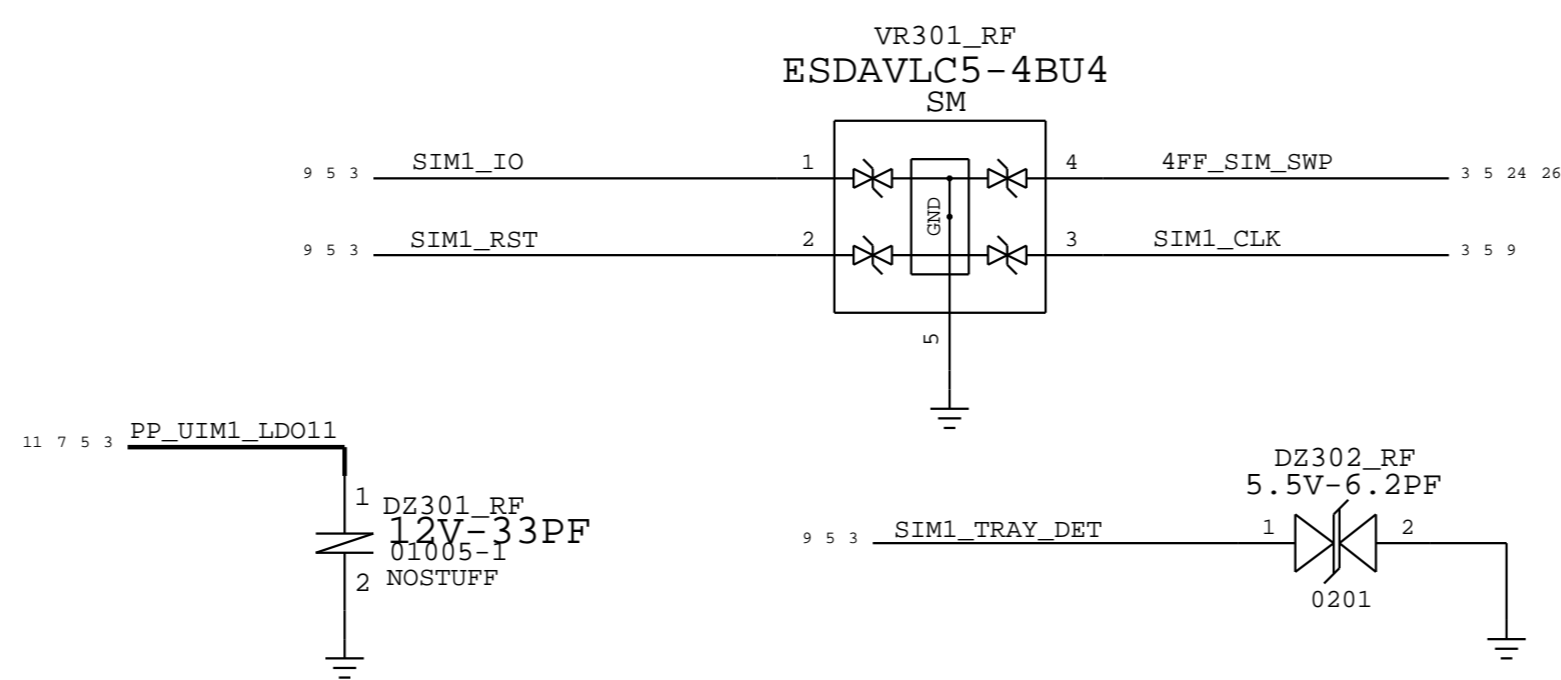
UAT TUNER



SIM CARD CONNECTOR



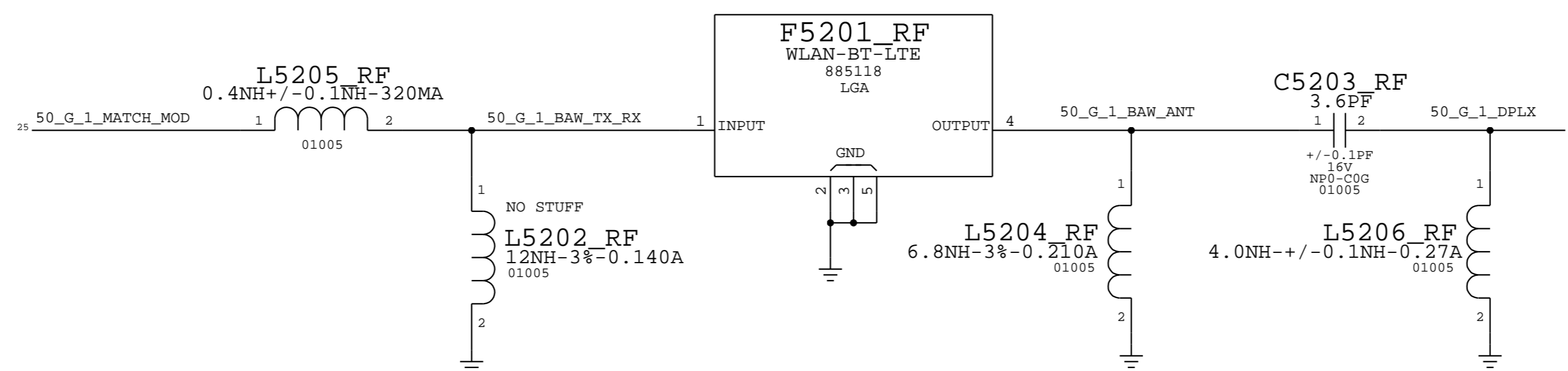
SIM CARD ESD PROTECTION



PAGE TITLE FE: ANT CONNECTORS AND UAT TUNER		
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		SHEET 36 OF 59

N71-SPECIFIC RADIO PAGE 4

WLAN LAT 2.4GHZ BAW BPF



AP TO BB/WLAN/BT/SH CONNECTIONS

MLB PROBE POINTS

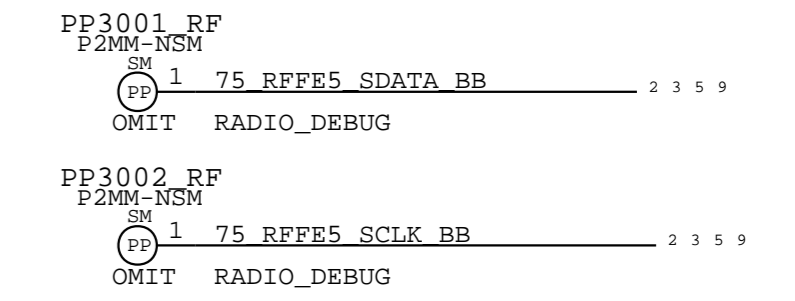
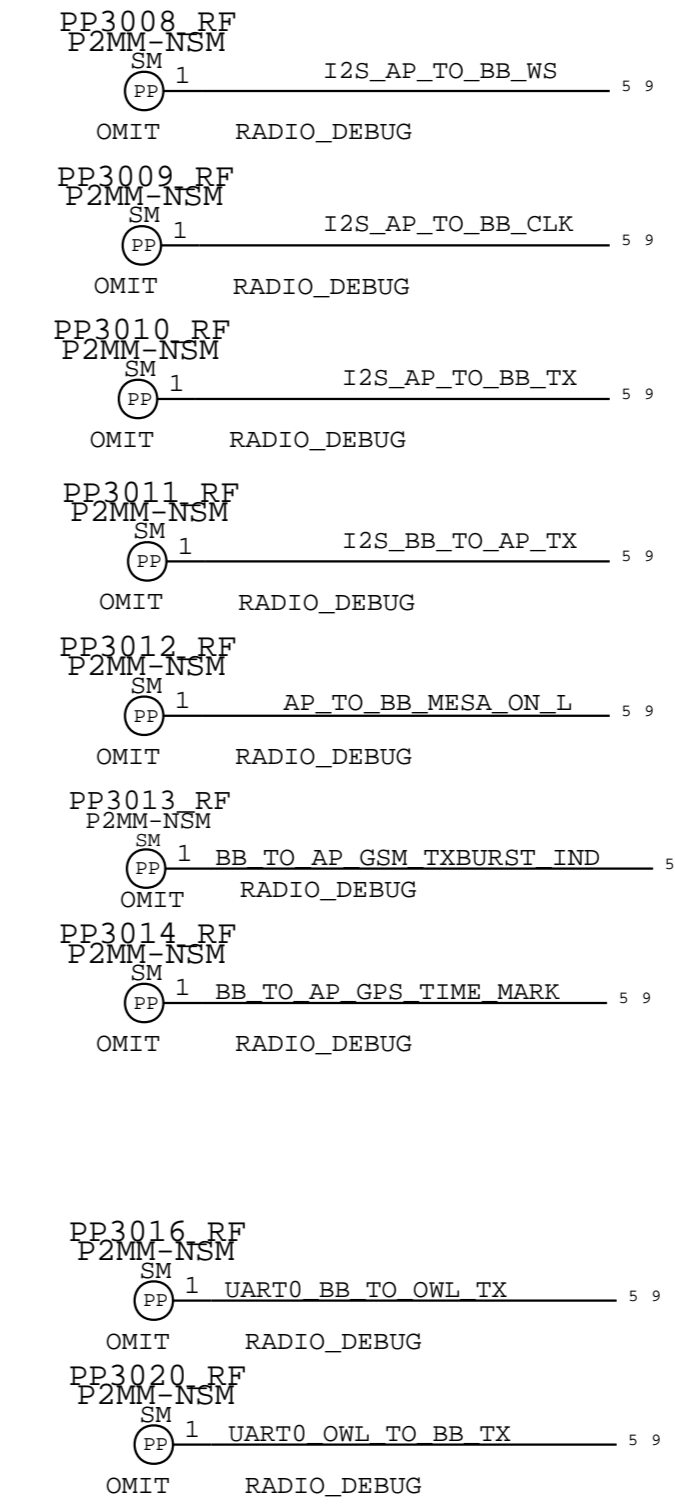
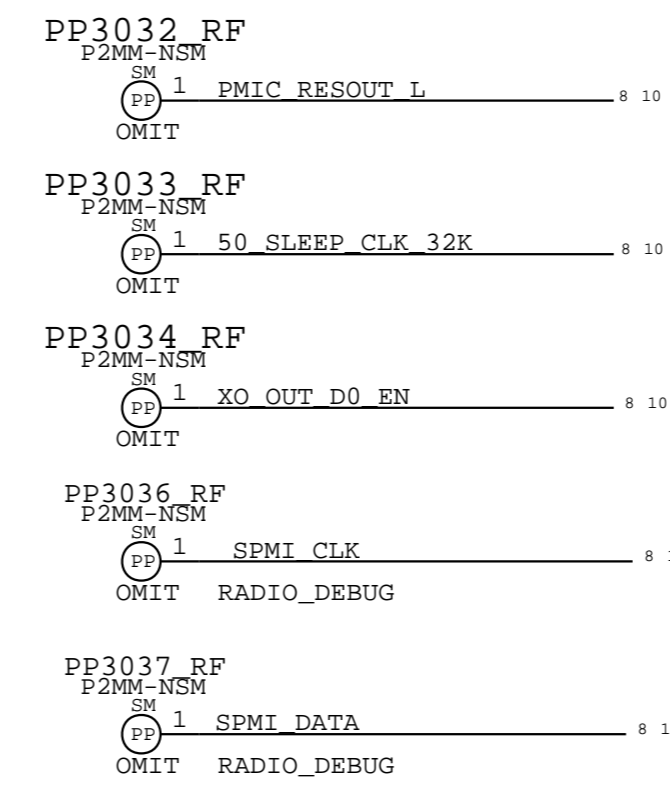
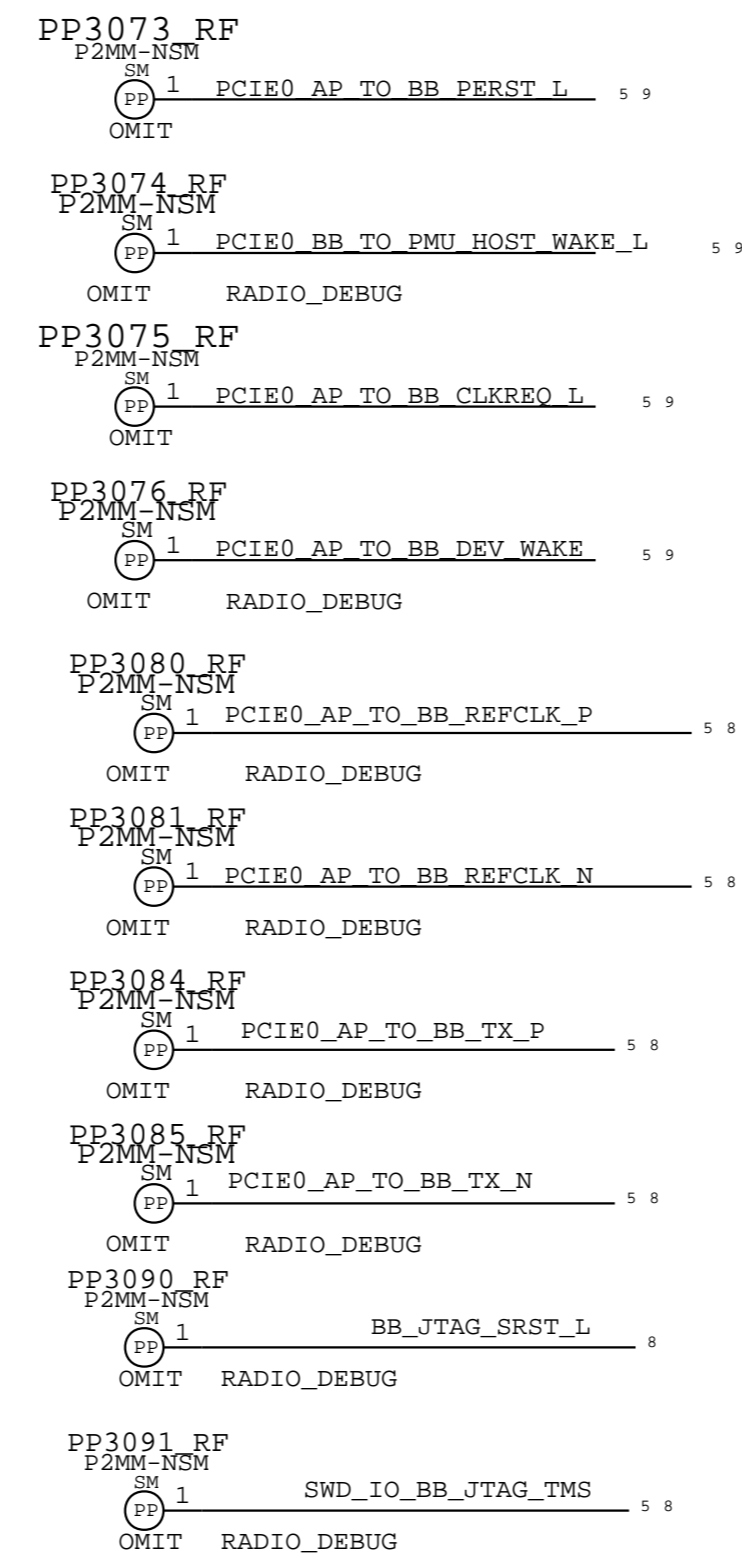
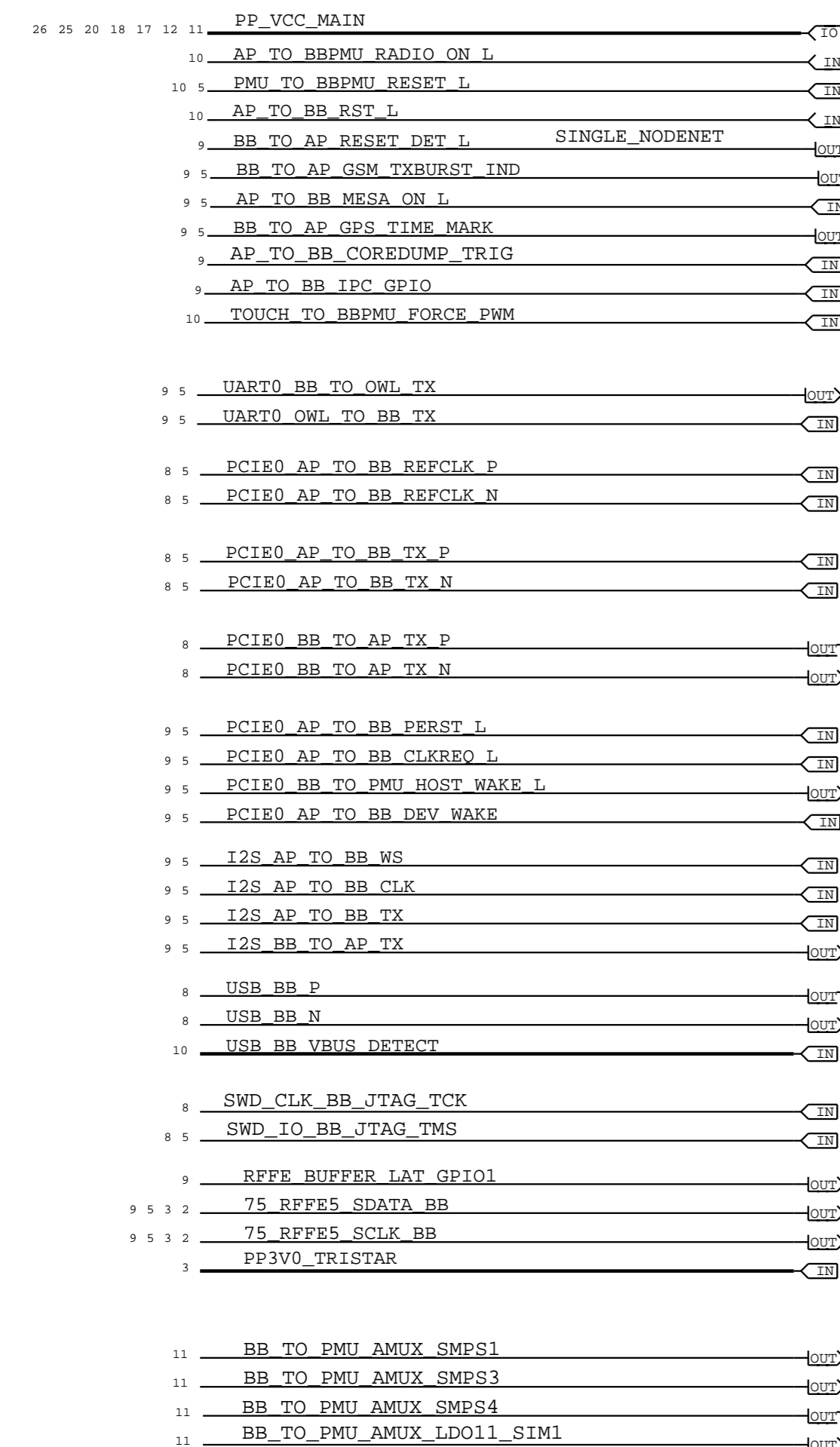
BASEBAND

WLAN/BT

PCIE

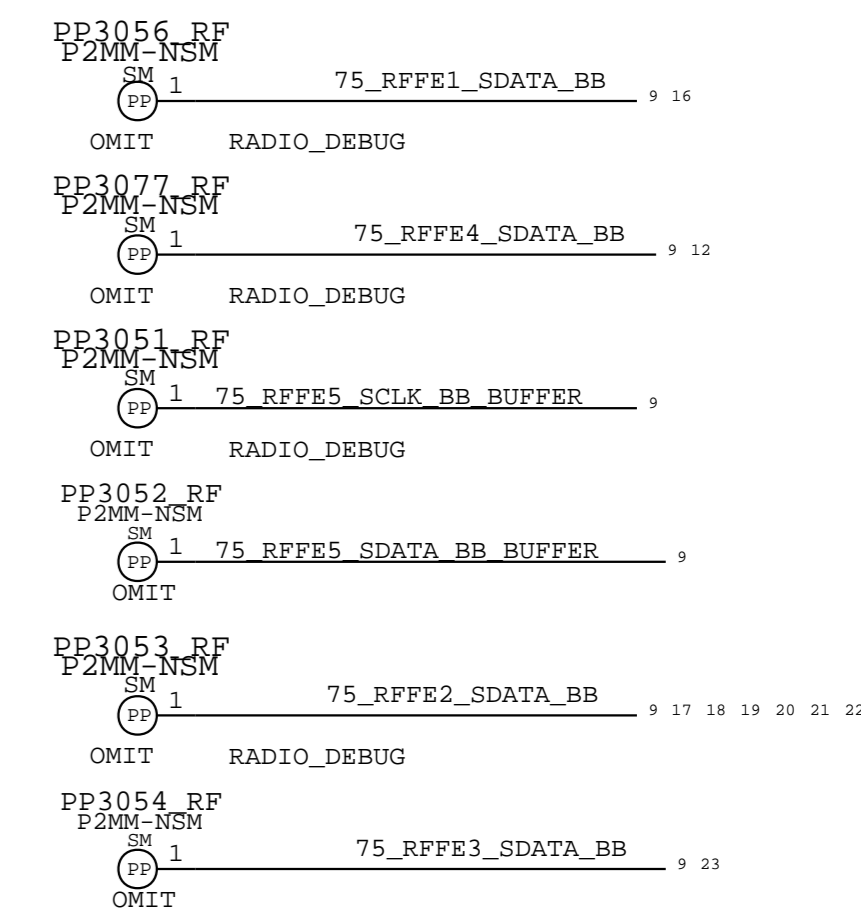
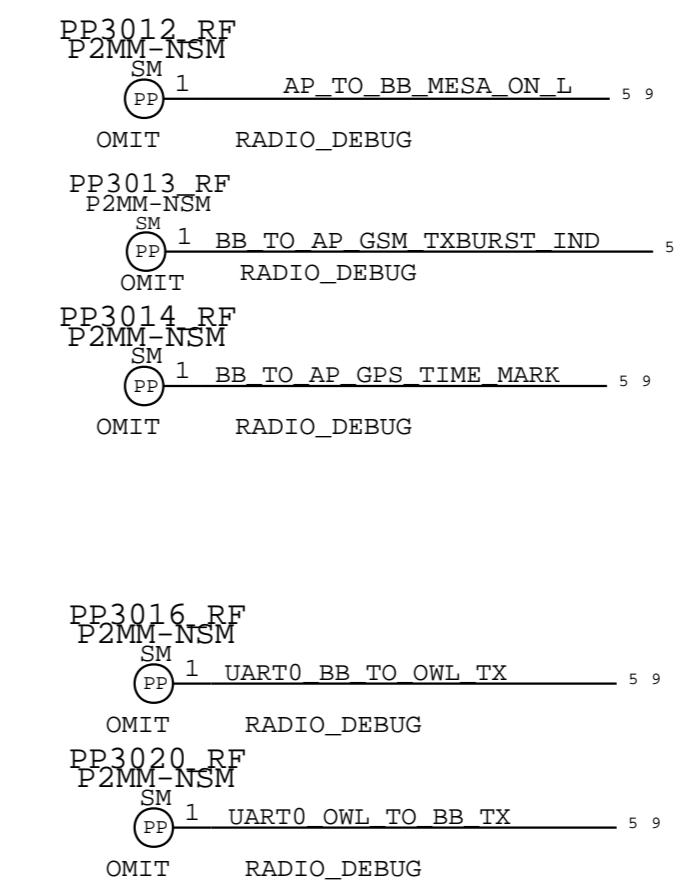
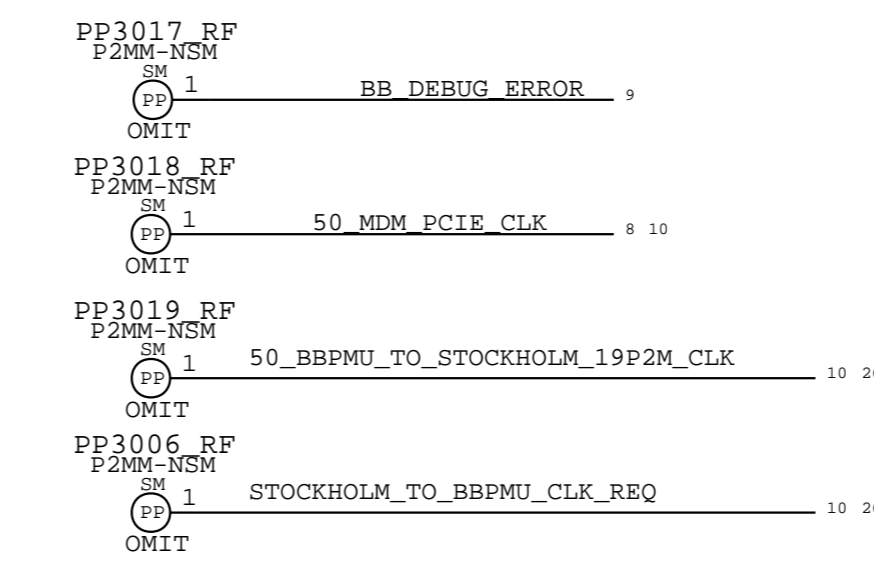
PMU

ANT TUNER



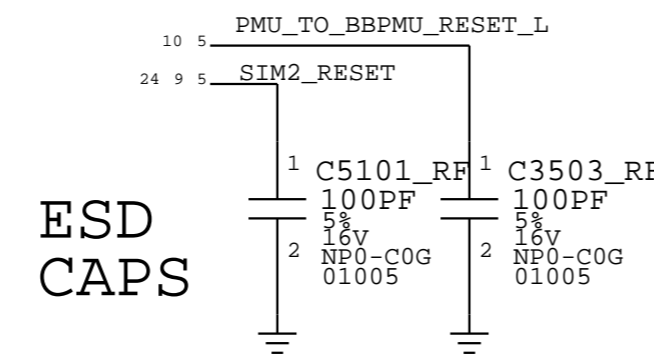
BASEBAND

RFFE



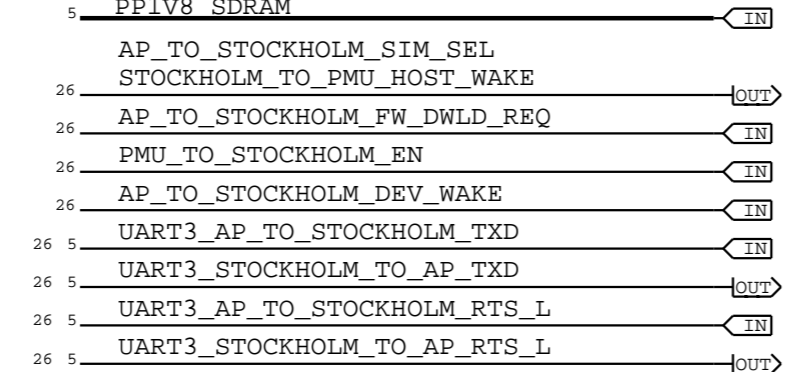
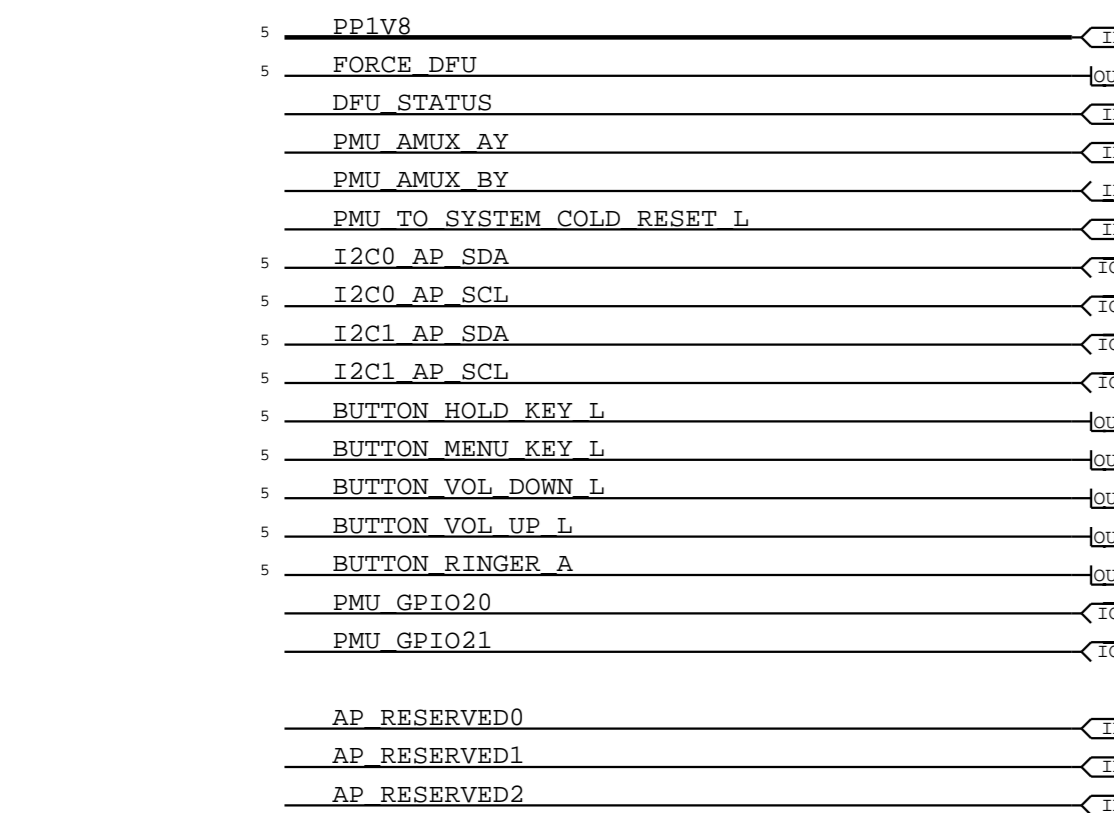
AP DEBUG

STOCKHOLM

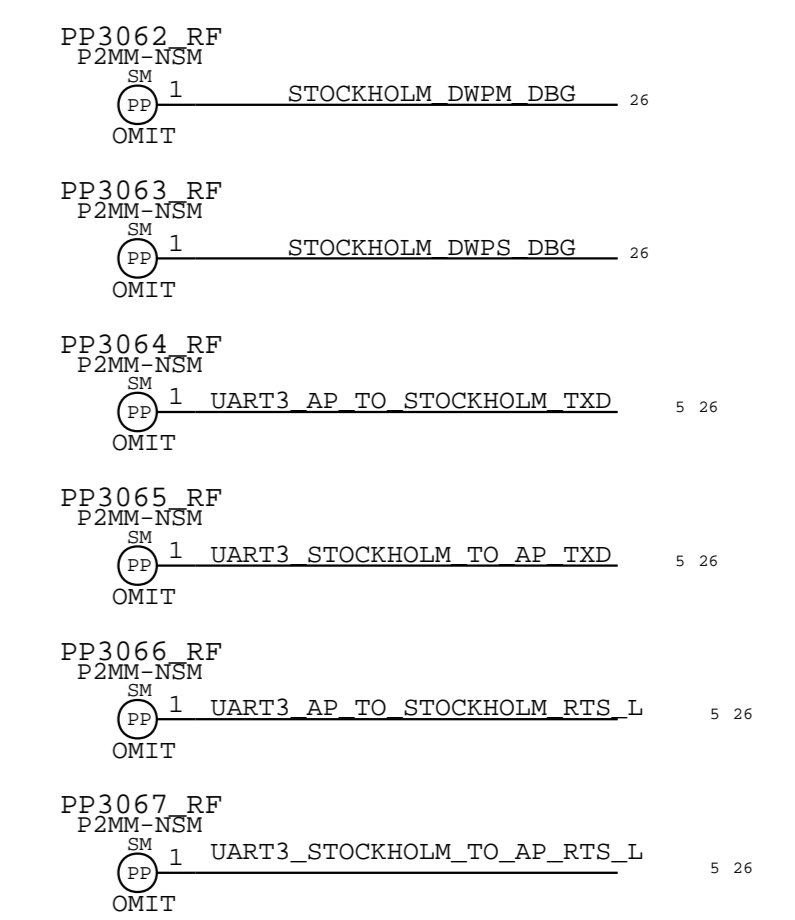
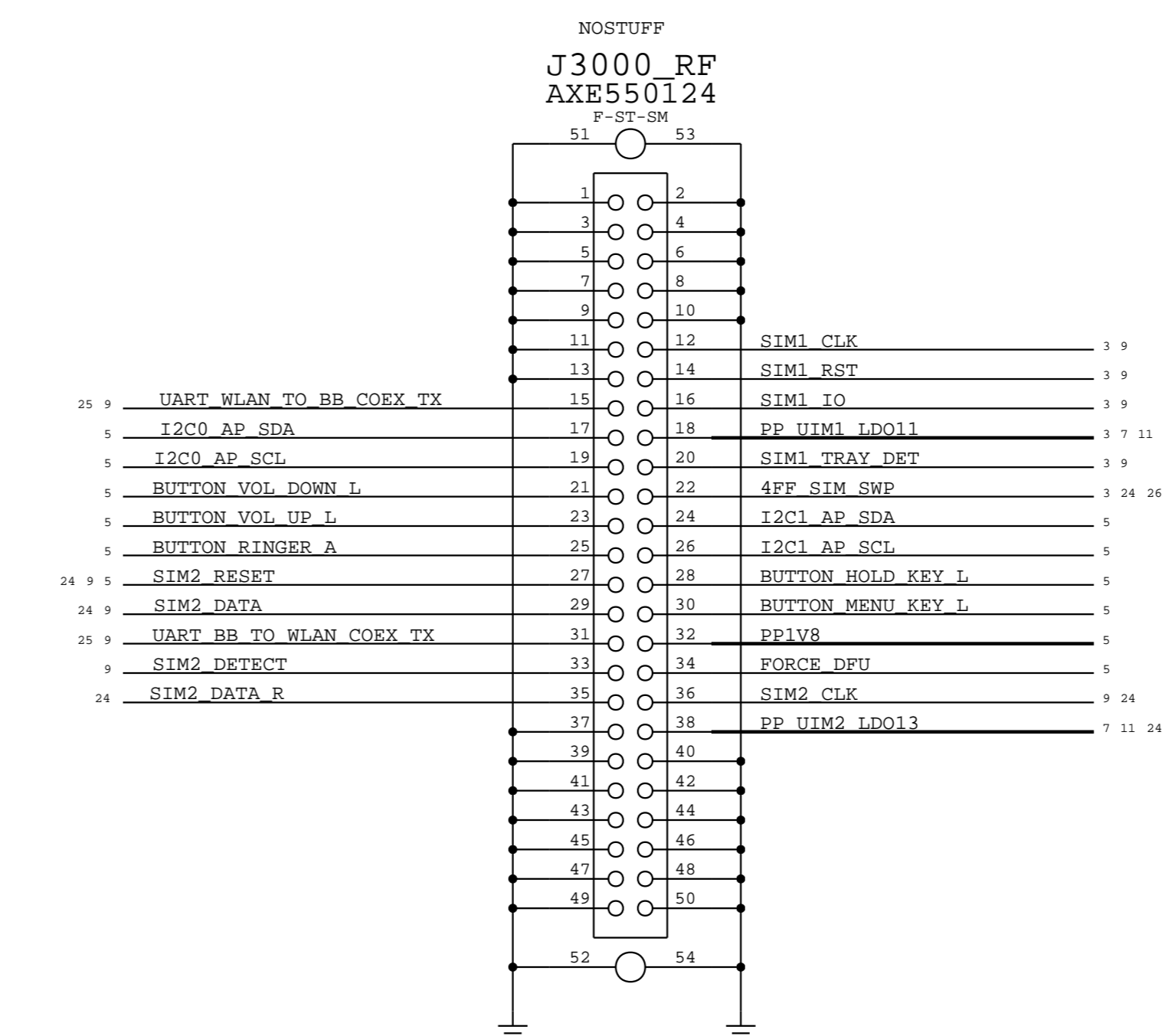
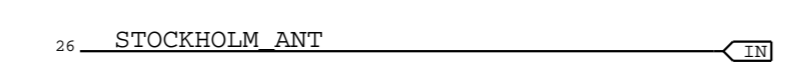


DEBUG CONNECTOR

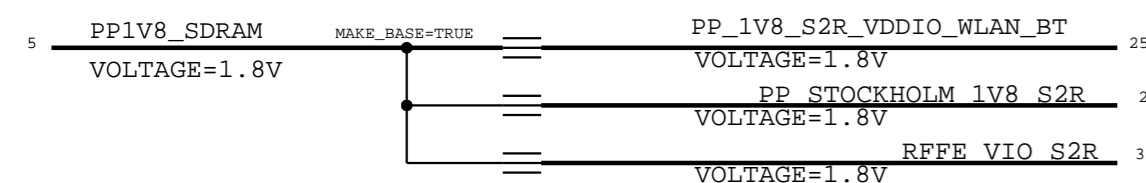
STOCKHOLM



ANT

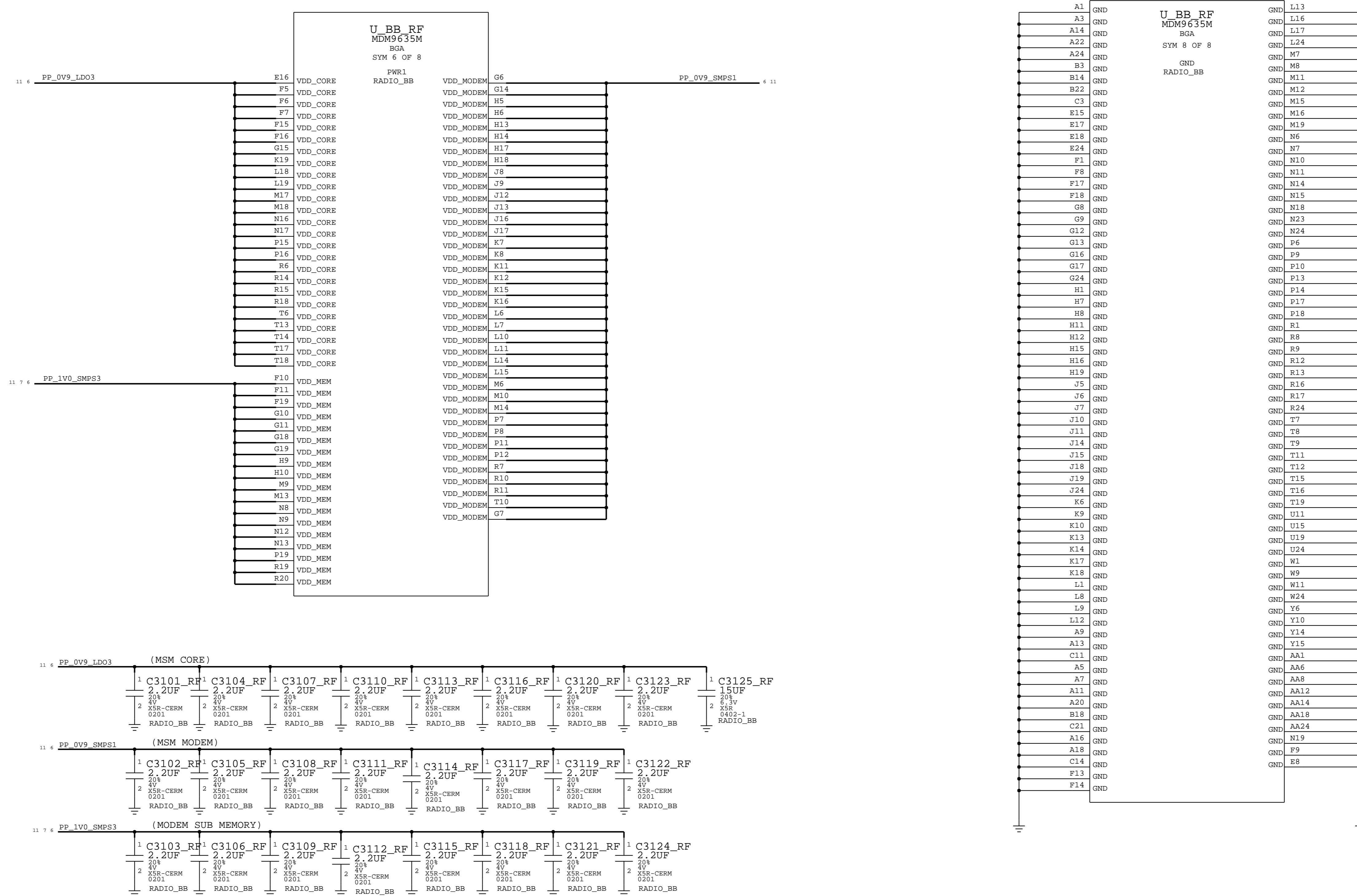


POWER



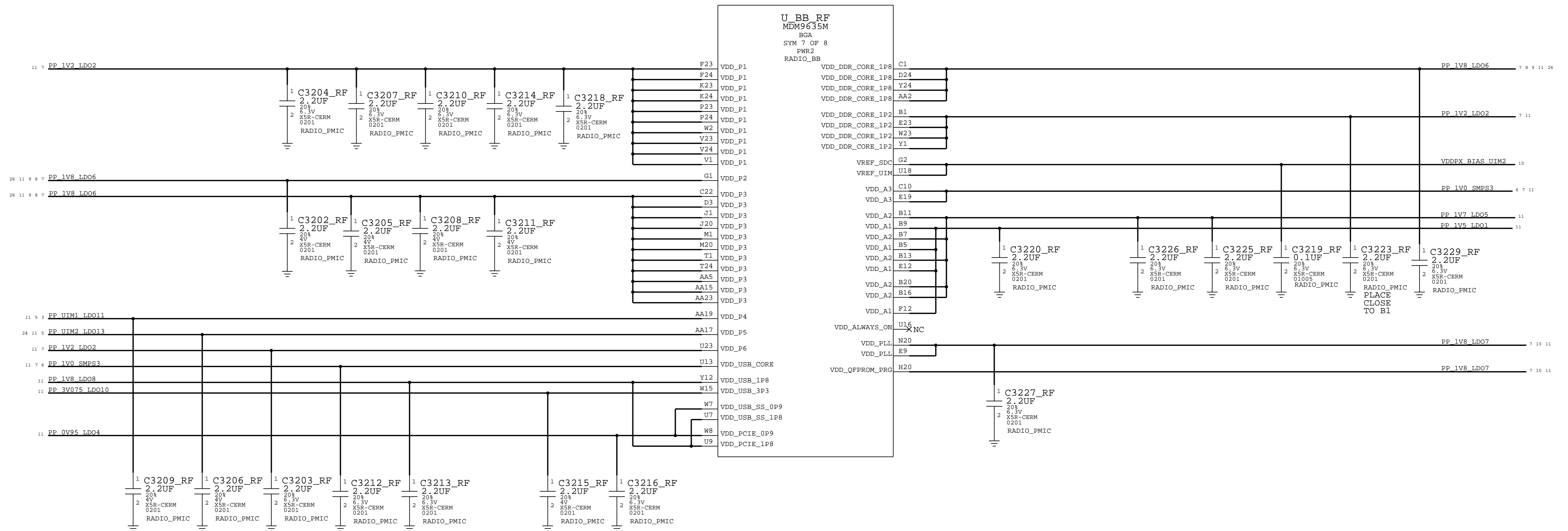
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DEBUG CONN & TEST POINTS		051-1902	D
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BASEBAND: POWER 1

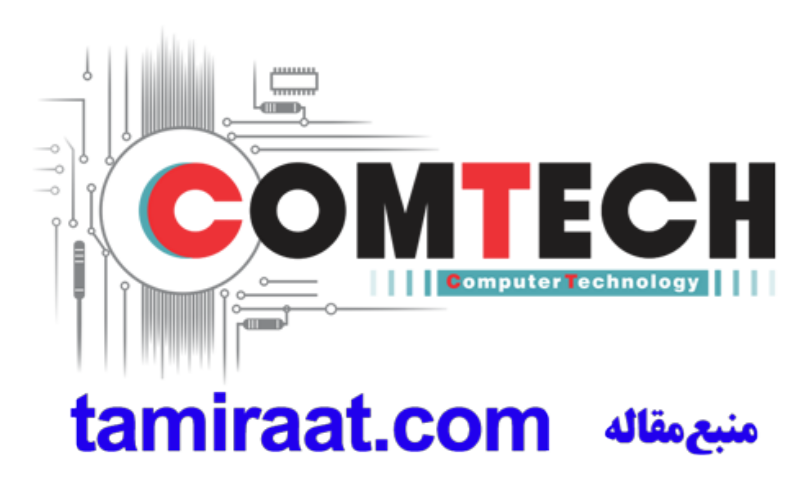
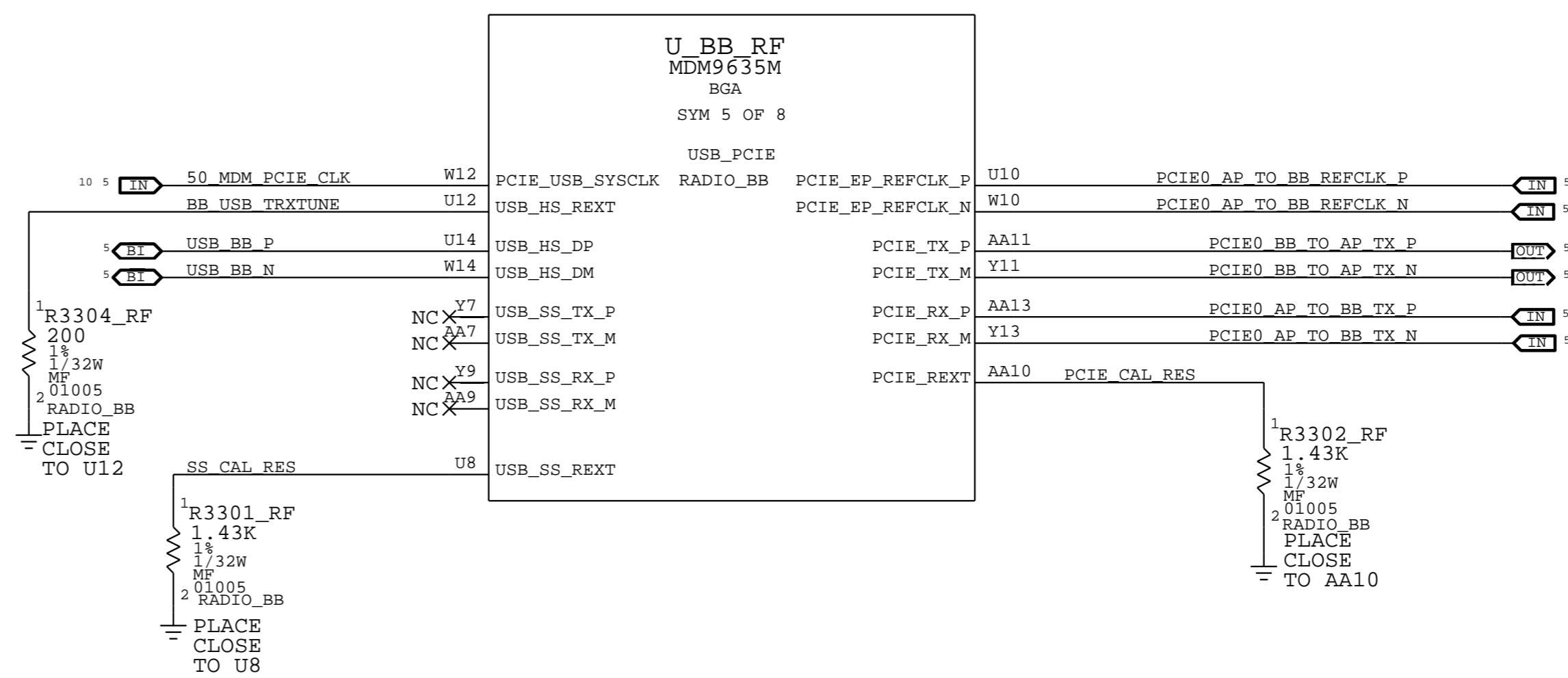
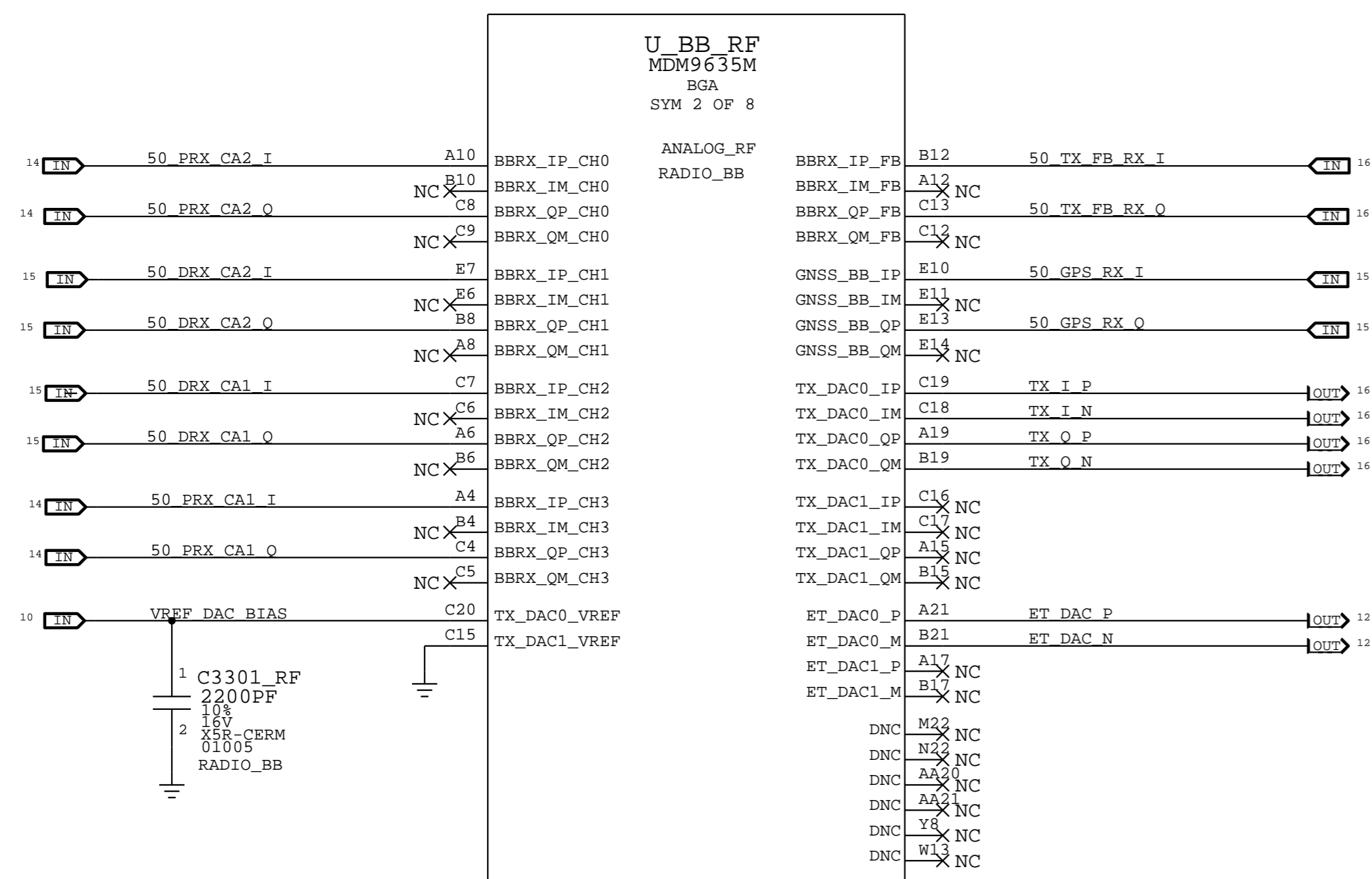
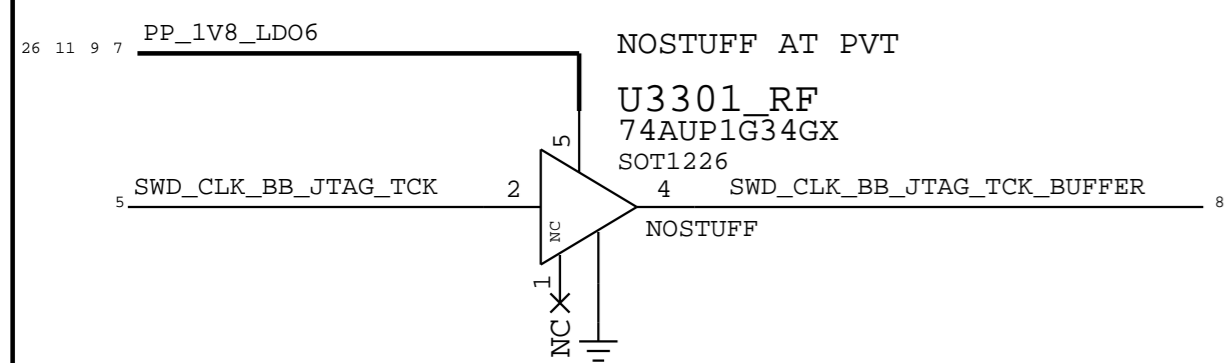
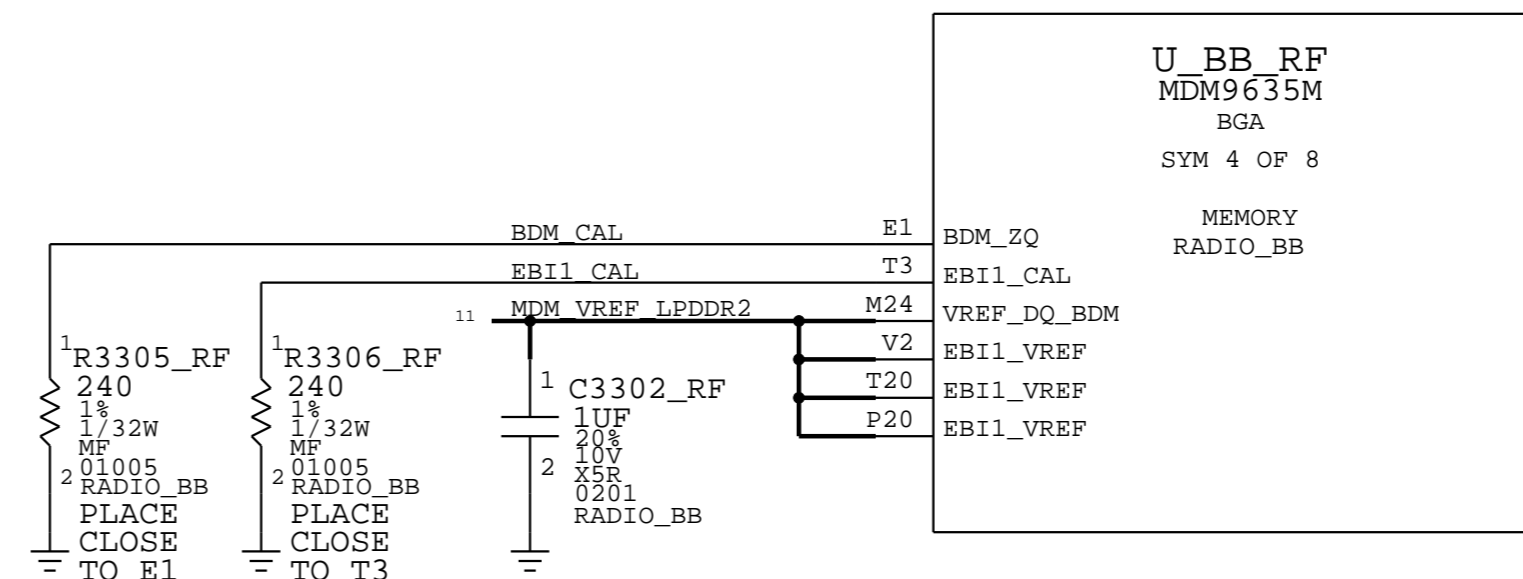
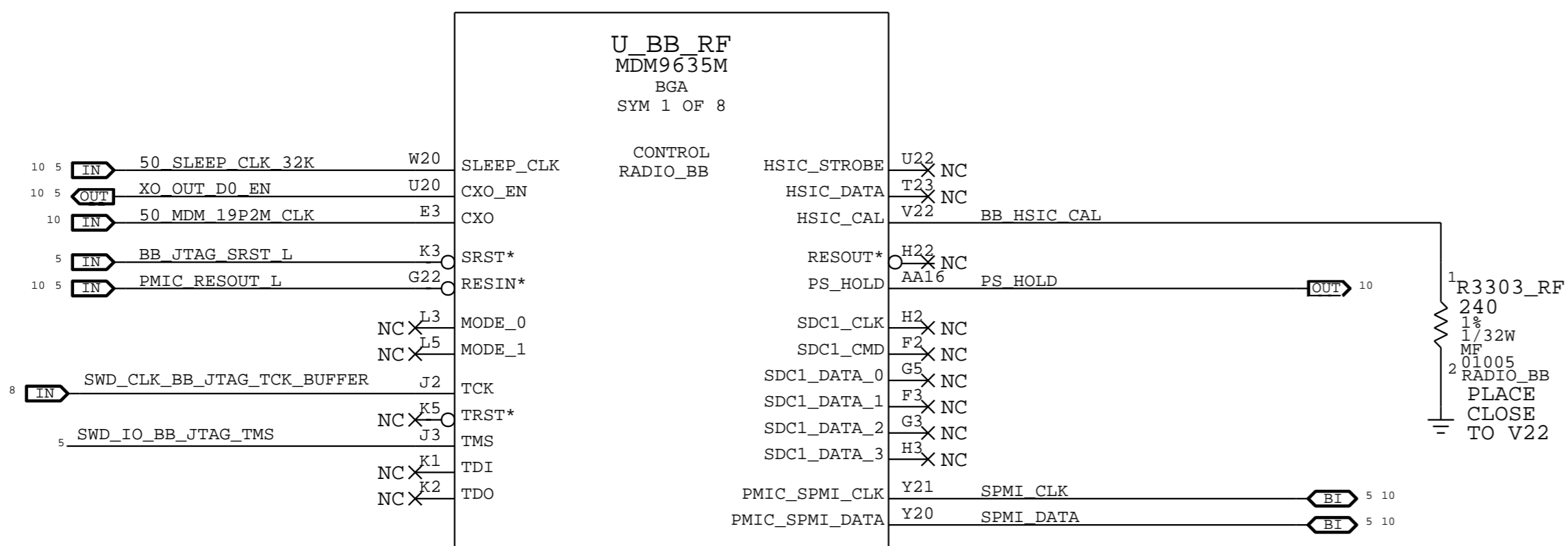


PAGE TITLE CELLULAR BASEBAND: POWER1		DRAWING NUMBER 051-1902	SIZE D
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BASEBAND: POWER 2



BASEBAND: CONTROL AND INTERFACES

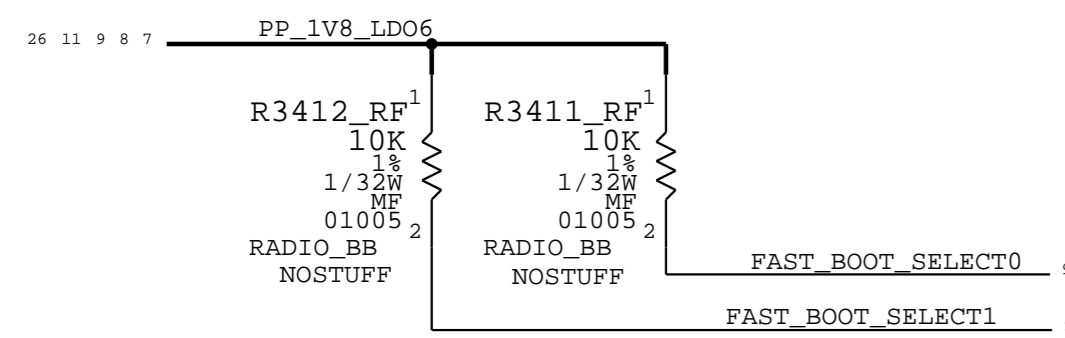
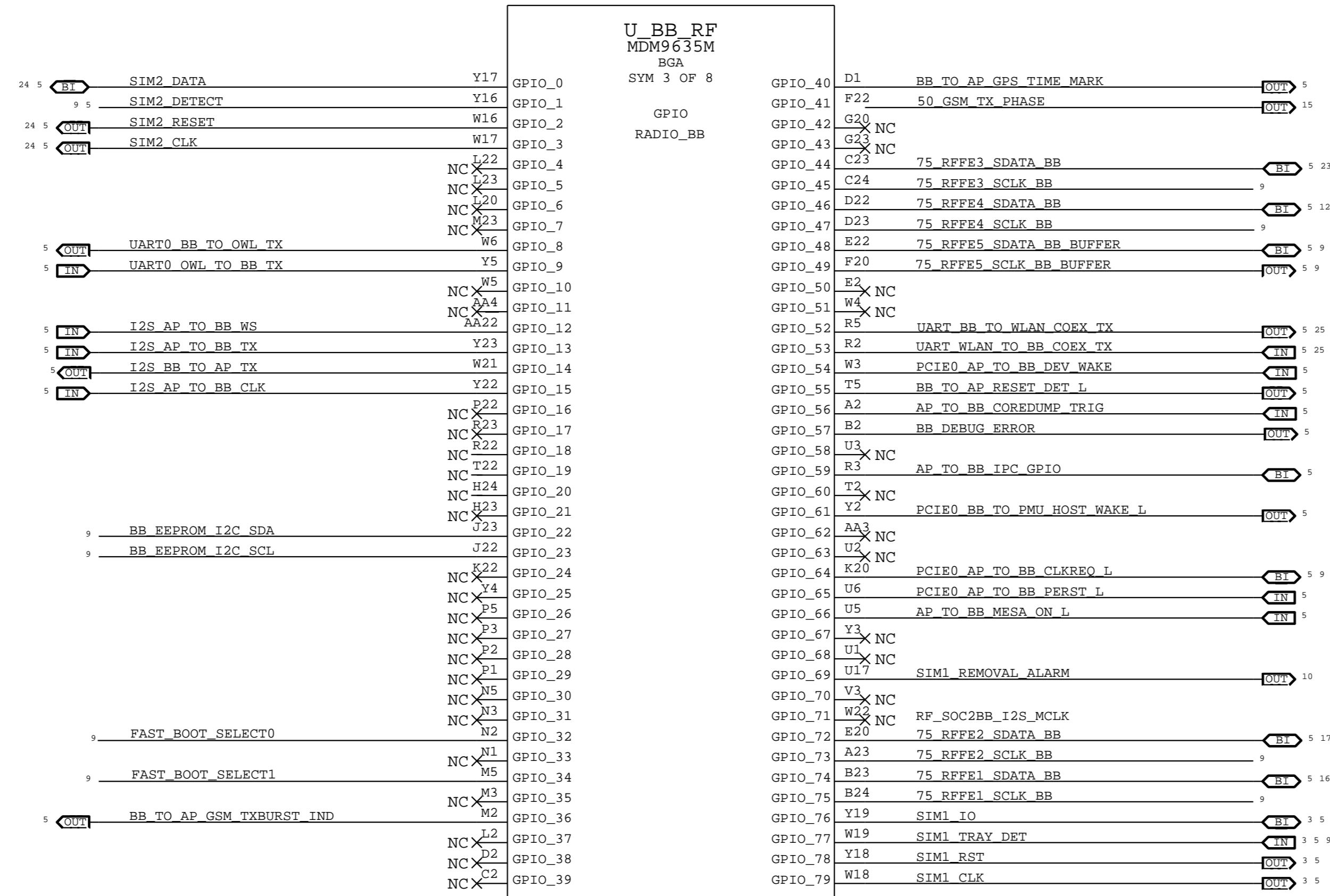


PAGE TITLE		CELLULAR BASEBAND: CONTROL AND INTERFACES	
DRAWING NUMBER		051-1902	SIZE D
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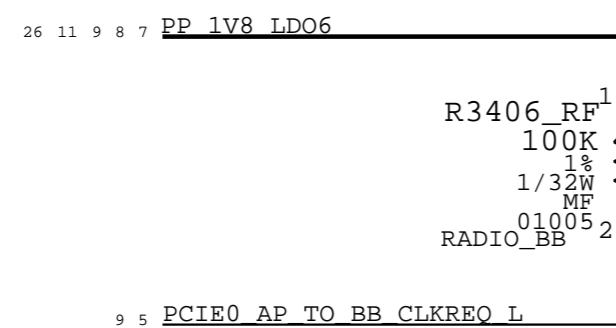
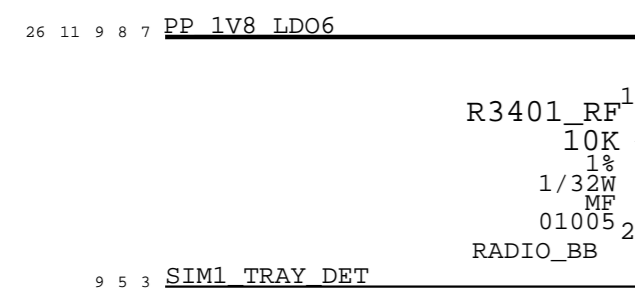
BASEBAND: GPIOS



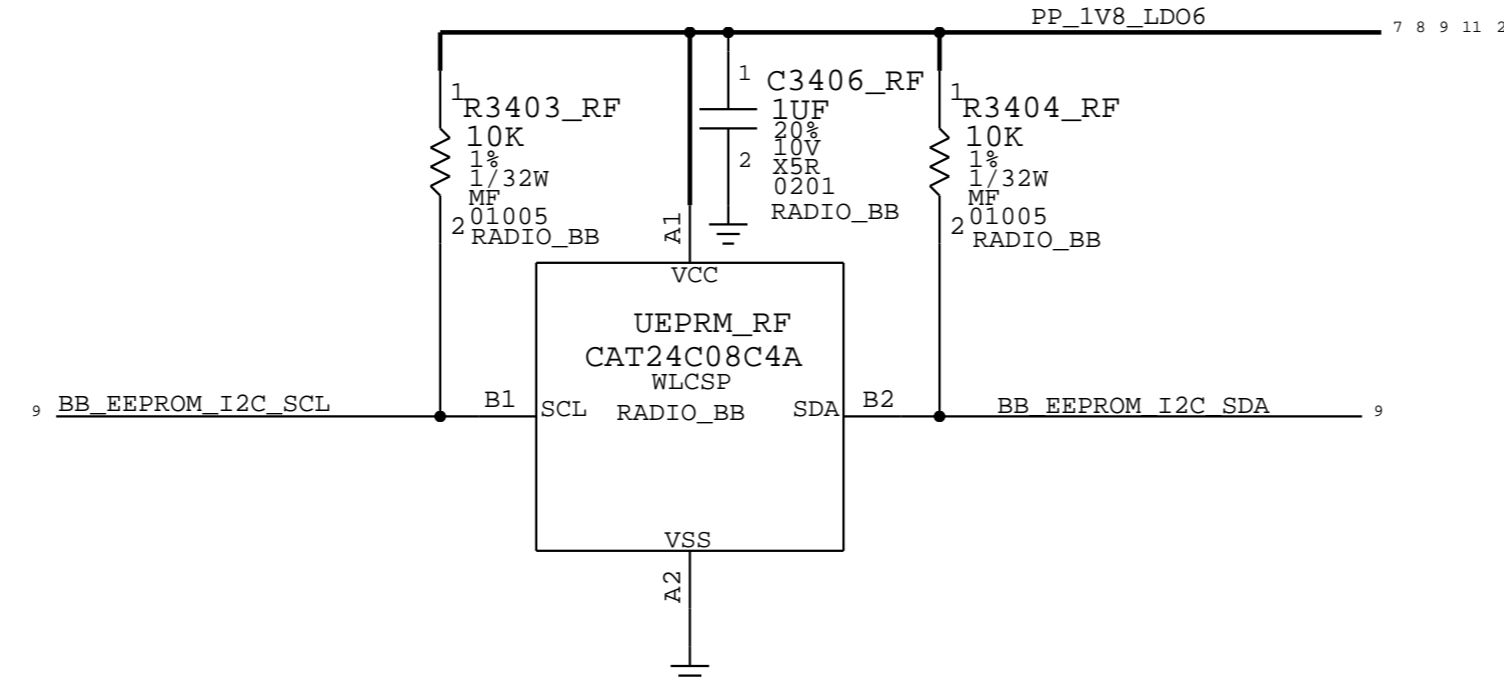
STUFF R3411 FOR PCIE BOOT (UNFUSED BB)
STUFF R3412 FOR USB BOOT (UNFUSED BB)

OPTION	SEL2	SEL1	SELO
GPIO	35	34	32
PCIE	0	0	1
HSUSB	0	1	0
HSIC	0	1	1

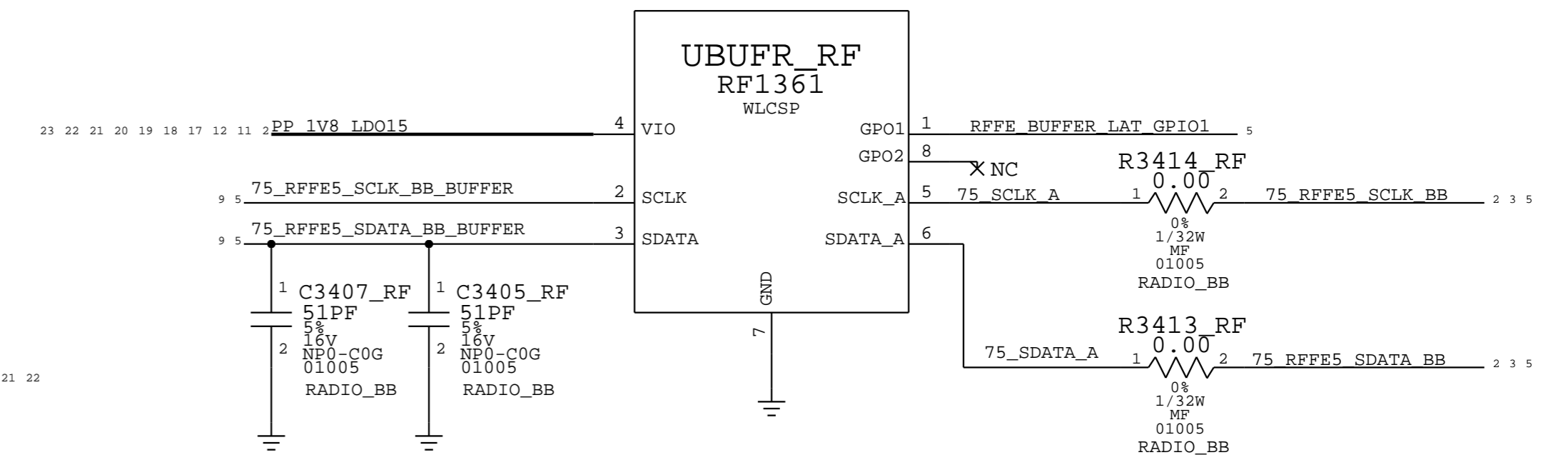
NOSTUFF R3402 WHEN VINYL PRESENT
STUFF R3402 WHEN VINYL NOT PRESENT



BB EEPROM

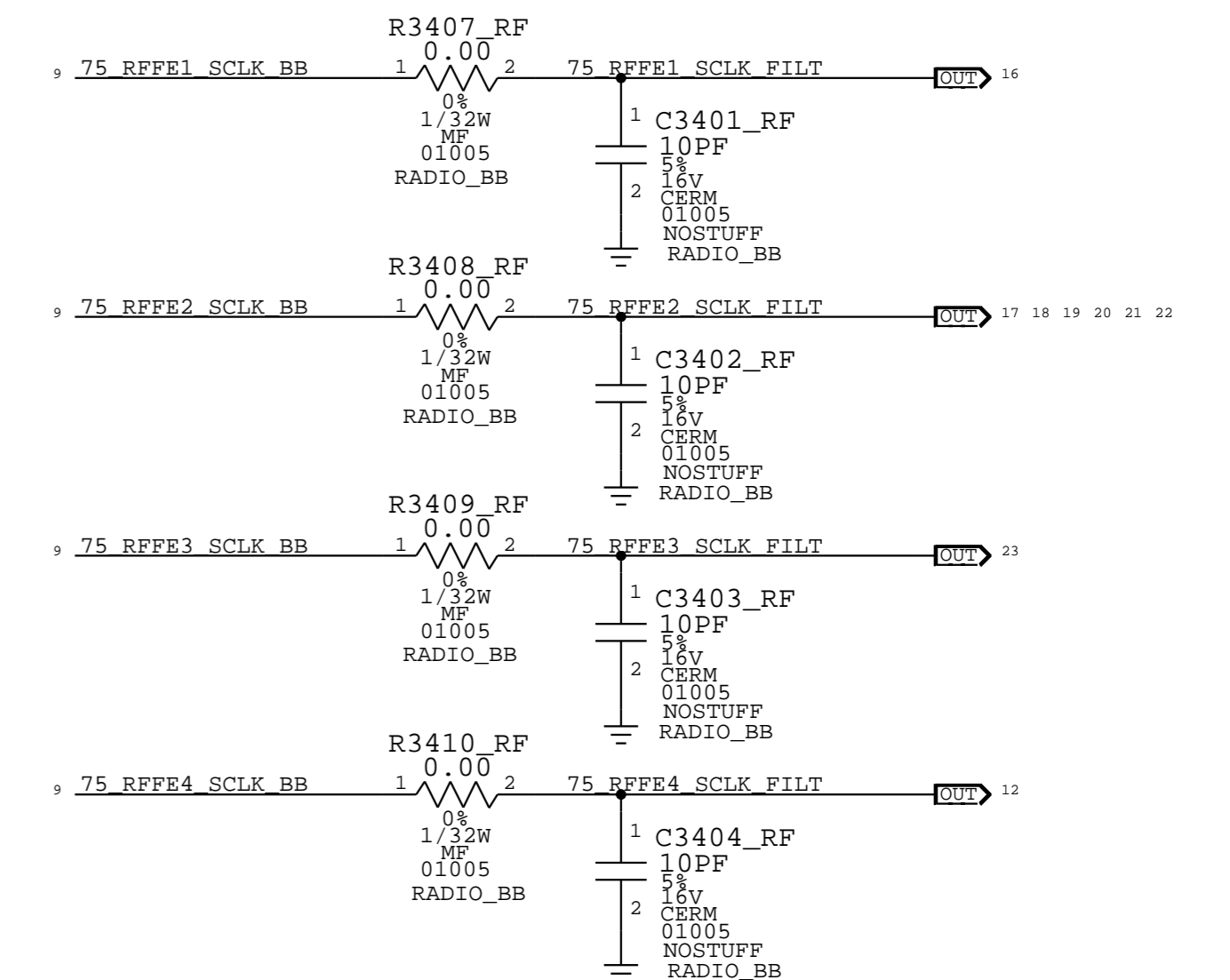


BUFFER ON RFFE5
SCLK/SDATA_A IS OUTPUT



PLACE C3405_RF CLOSE TO BUFFER
PLACE C3407_RF CLOSE TO MDM

RFFE CLOCK FILTERS



PCIE PULL-UPS TO BB RAIL



RFFE USAGE TABLE

- RFFE1 WTR
- RFFE2 LB/MB/HB PAD, 2G PA, LB/MB/HB ASM
- RFFE3 DIV ASM
- RFFE4 QPOET
- RFFE5 DIV LNA, ANT TUNERS

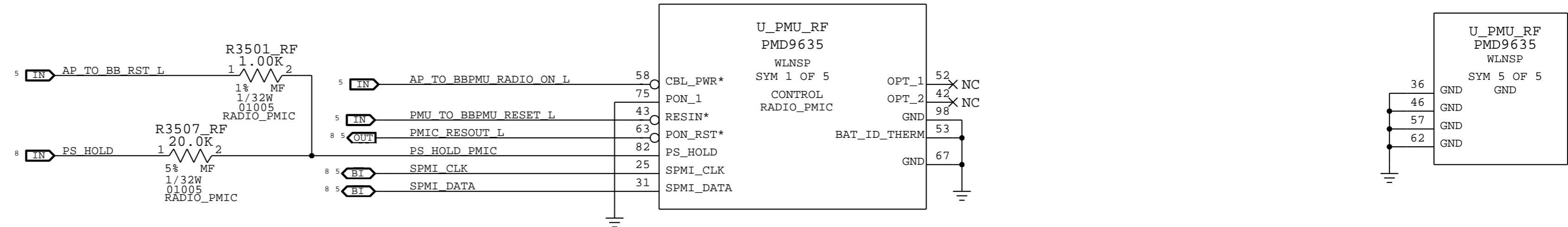
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PMU: CONTROL AND CLOCKS

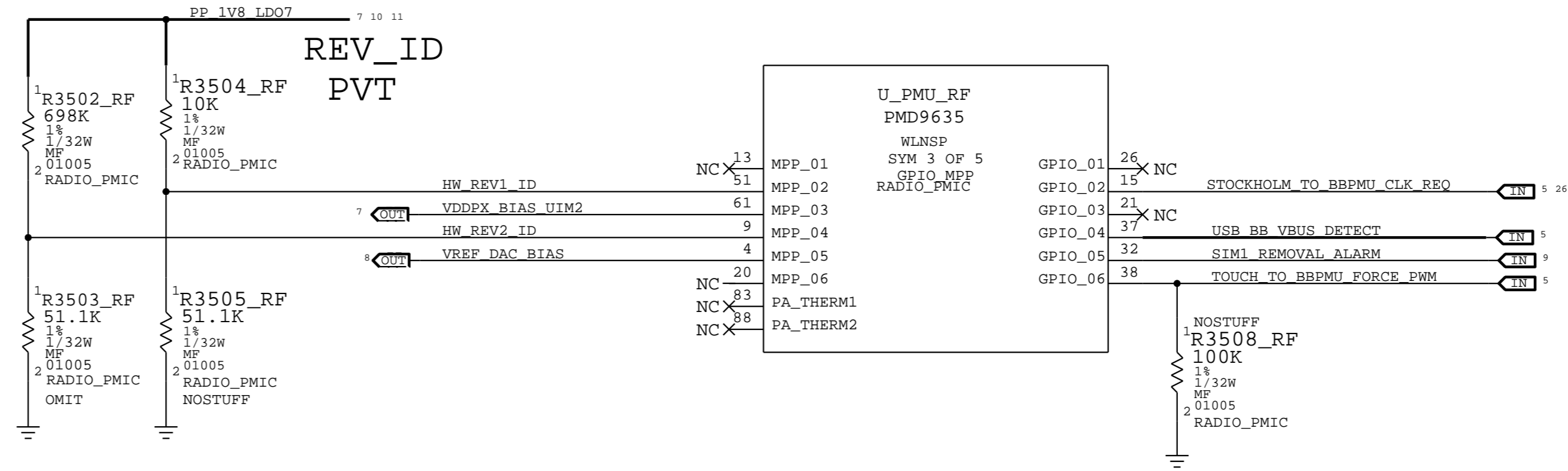
HW_REV2_ID	R3502	R3503	CONFIG
1.80V	698K	-	MLB
0.12V	698K	51.1K	SELF GEN

HW_REV_ID	R3504	R3505	REVISION
0.10V	887K	51.1K	DEV1
0.30V	255K	51.1K	DEV2
0.50V	124K	51.1K	DEV3
0.70V	82.5K	51.1K	DEV4/PROTOMLB1
0.90V	51.1K	51.1K	PROTOMLB2
1.10V	31.6K	51.1K	DEV5/PROTO1
1.20V	50K	100K	PROTO2
1.31V	39K	105K	EVT
1.43V	13.3K	51.1K	EVT_ALT
1.55V	8.25K	51.1K	CARRIER BUILD
1.67V	3.92K	51.1K	DVT
1.80V	10K	-	PVT

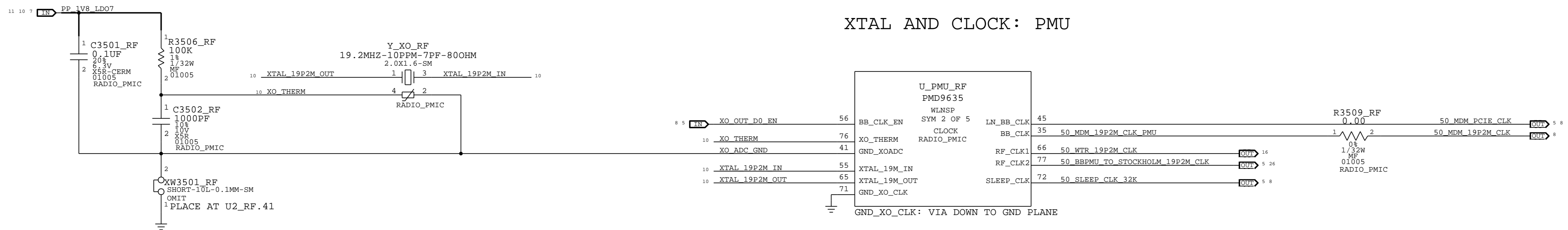
RESET AND CONTROL: PMU



MPPS AND GPIOs: PMU

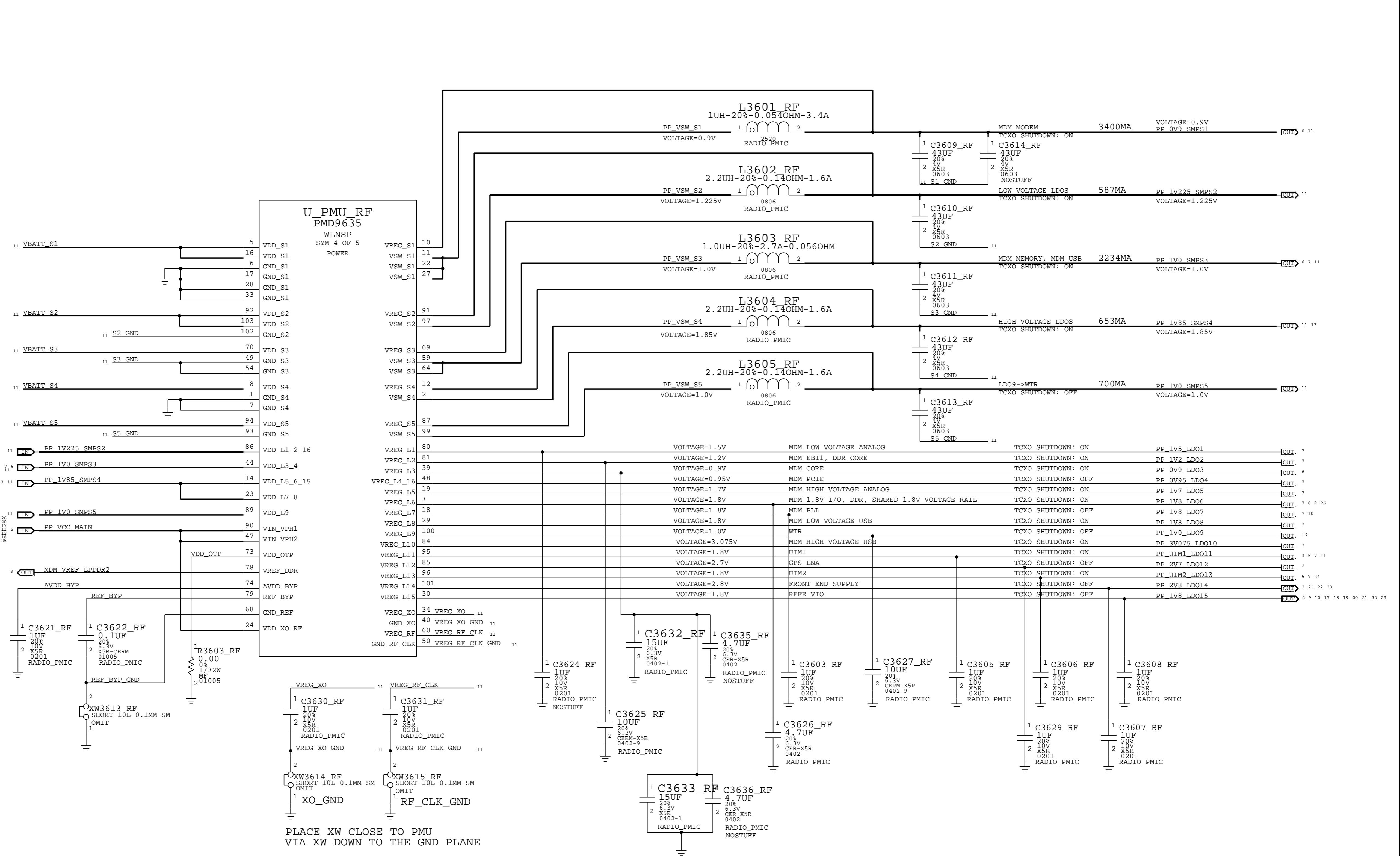
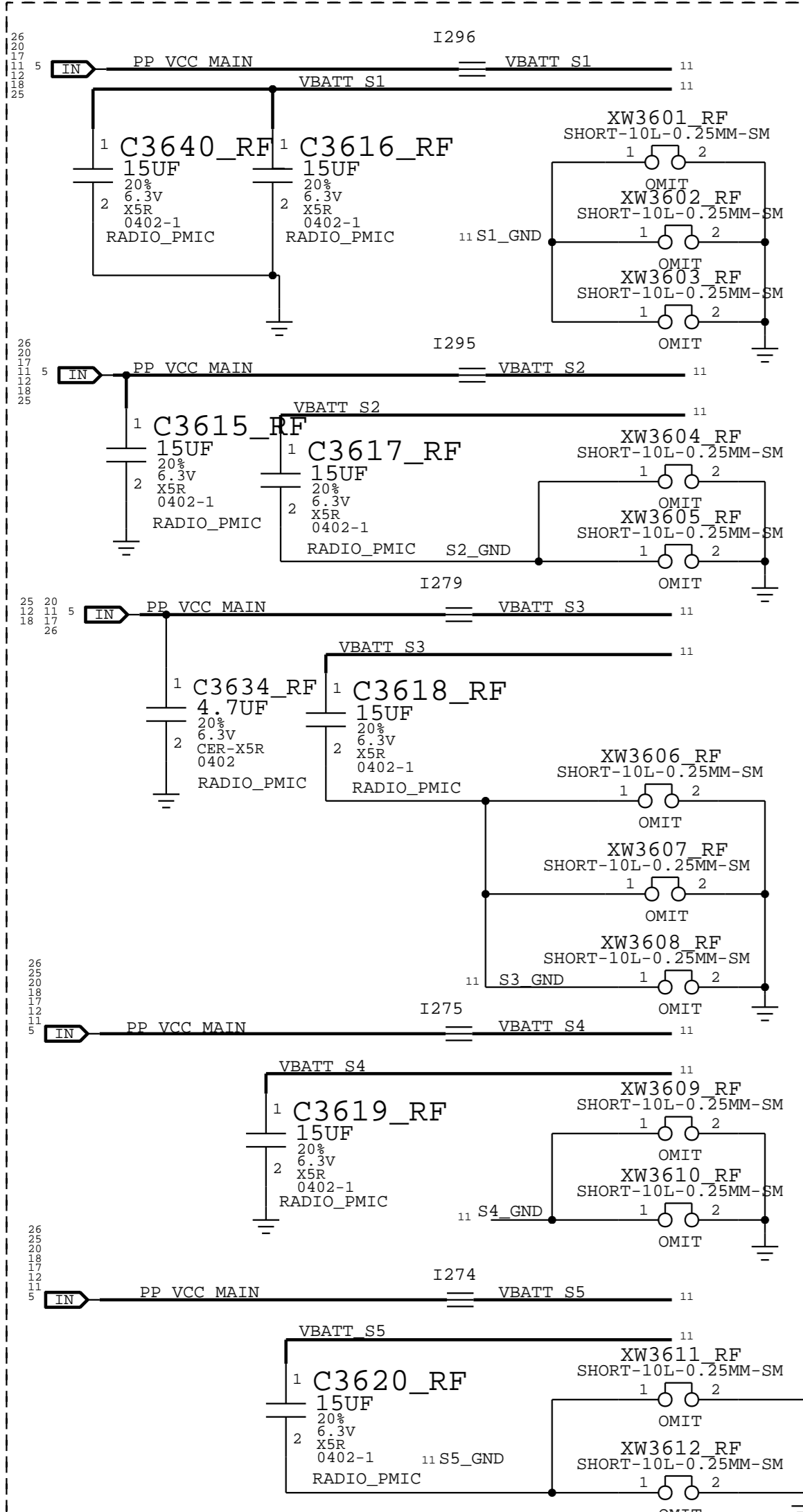


XTAL AND CLOCK: PMU



PMU: SWITCHERS AND LDOS

SWITCHERS BULK CAPS

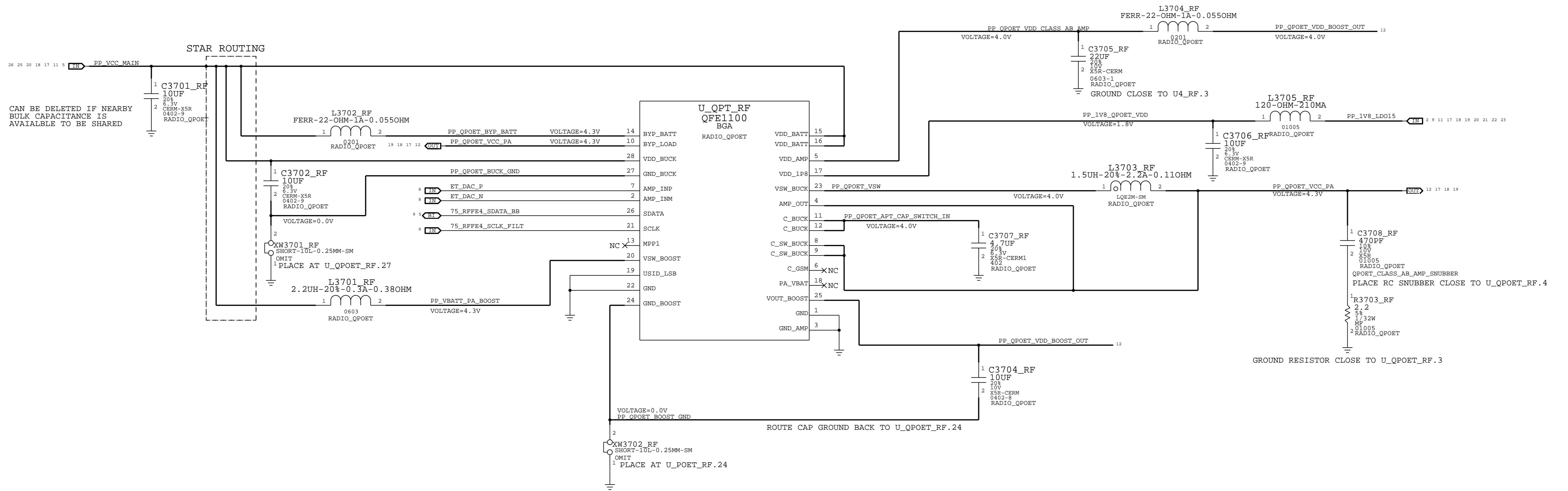


PLACE XW CLOSE TO PMU
VIA XW DOWN TO THE GND PLANE



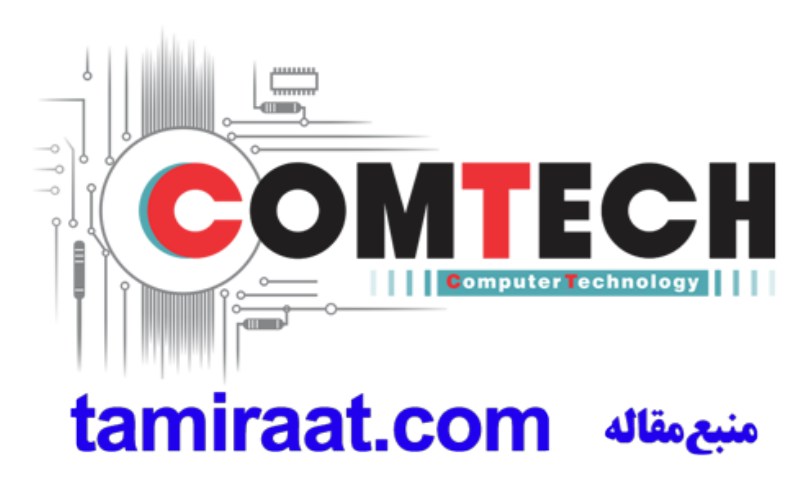
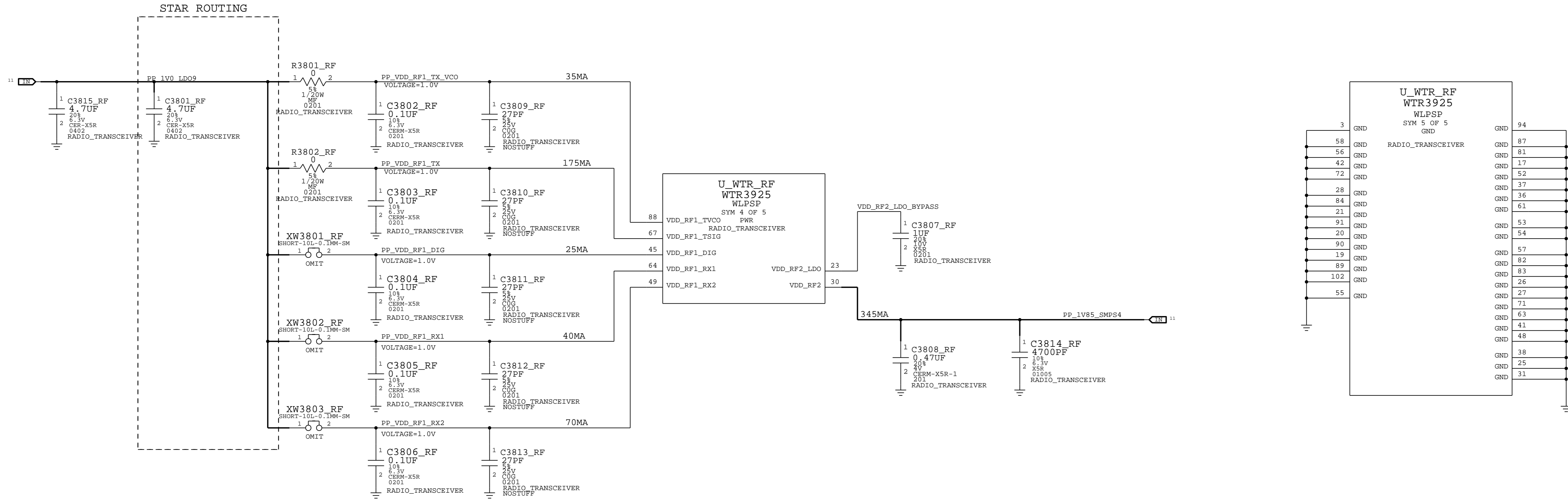
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PMU: ET MODULATOR



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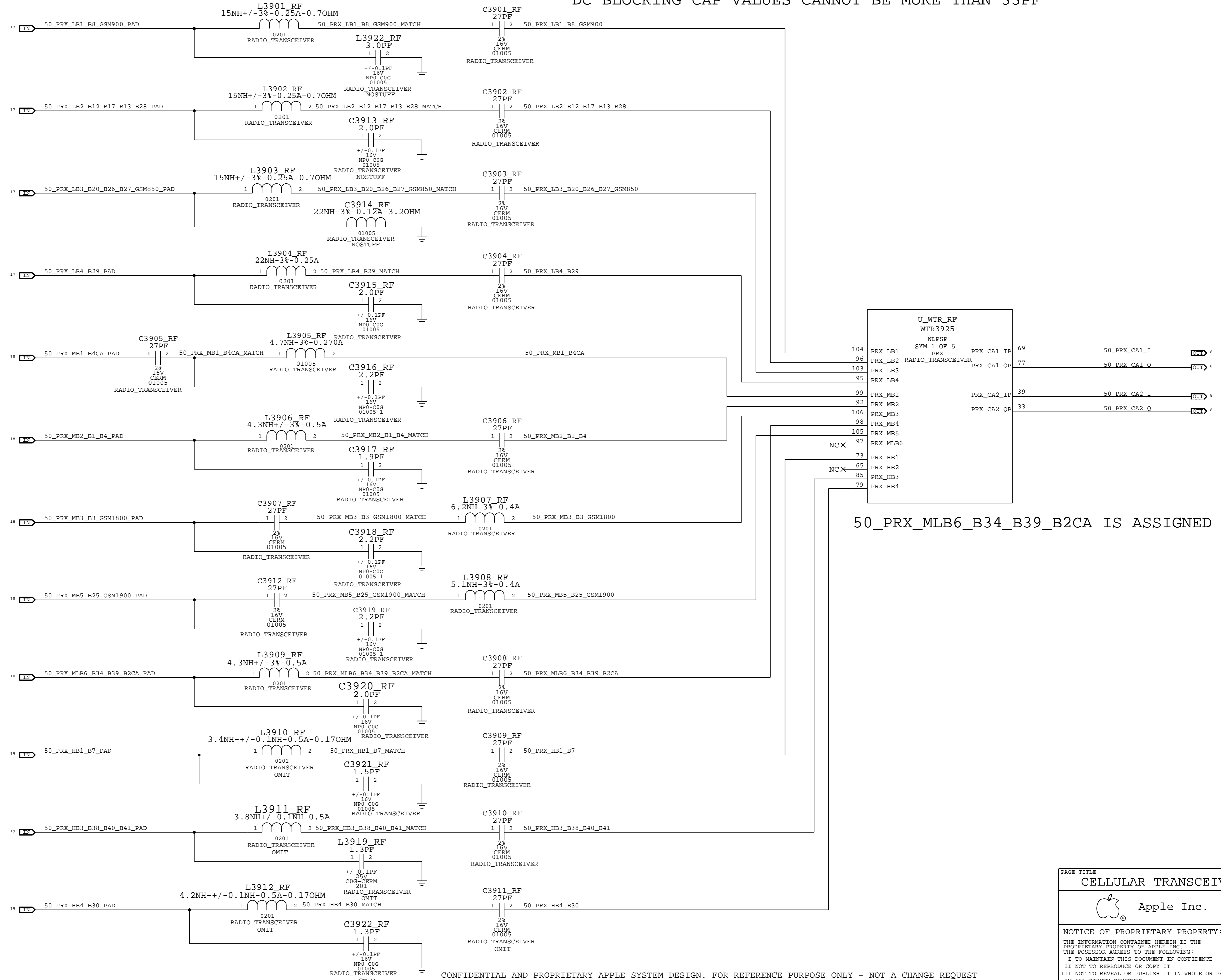
TRANSCEIVER: POWER



PAGE TITLE CELLULAR TRANSCEIVER: POWER		
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SHEET		46 OF 59

TRANSCEIVER: PRX PORTS

DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF



50_PRX_MLB6_B34_B39_B2CA IS ASSIGNED TO MB4



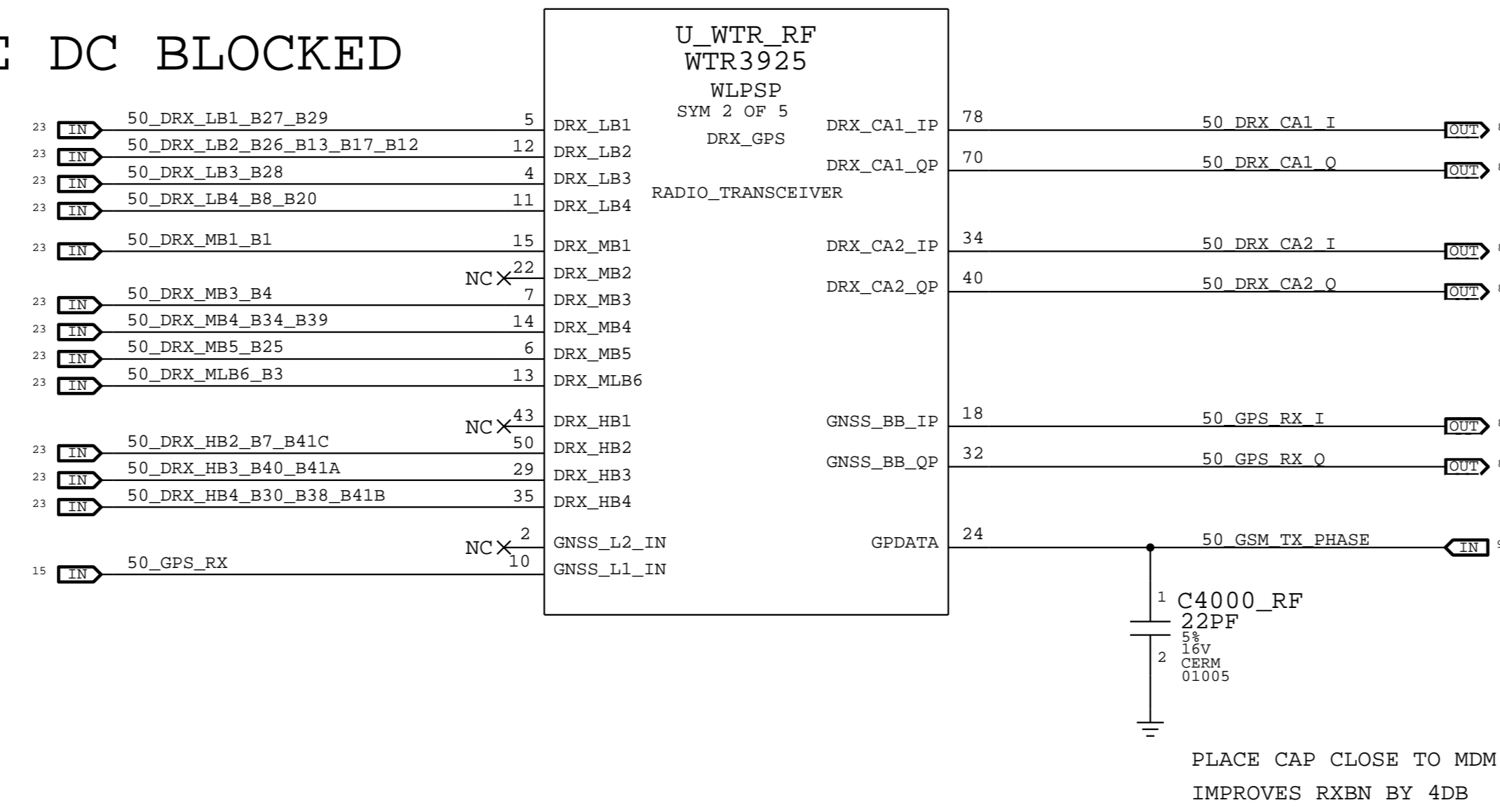
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PAGE		39 OF 51	
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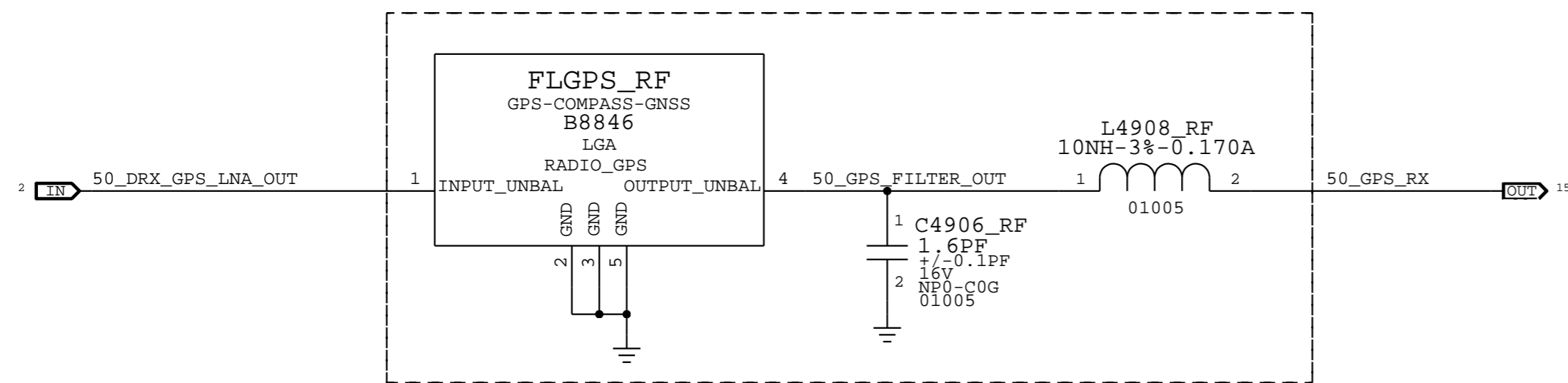
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TRANSCEIVER: DRX/GPS PORTS

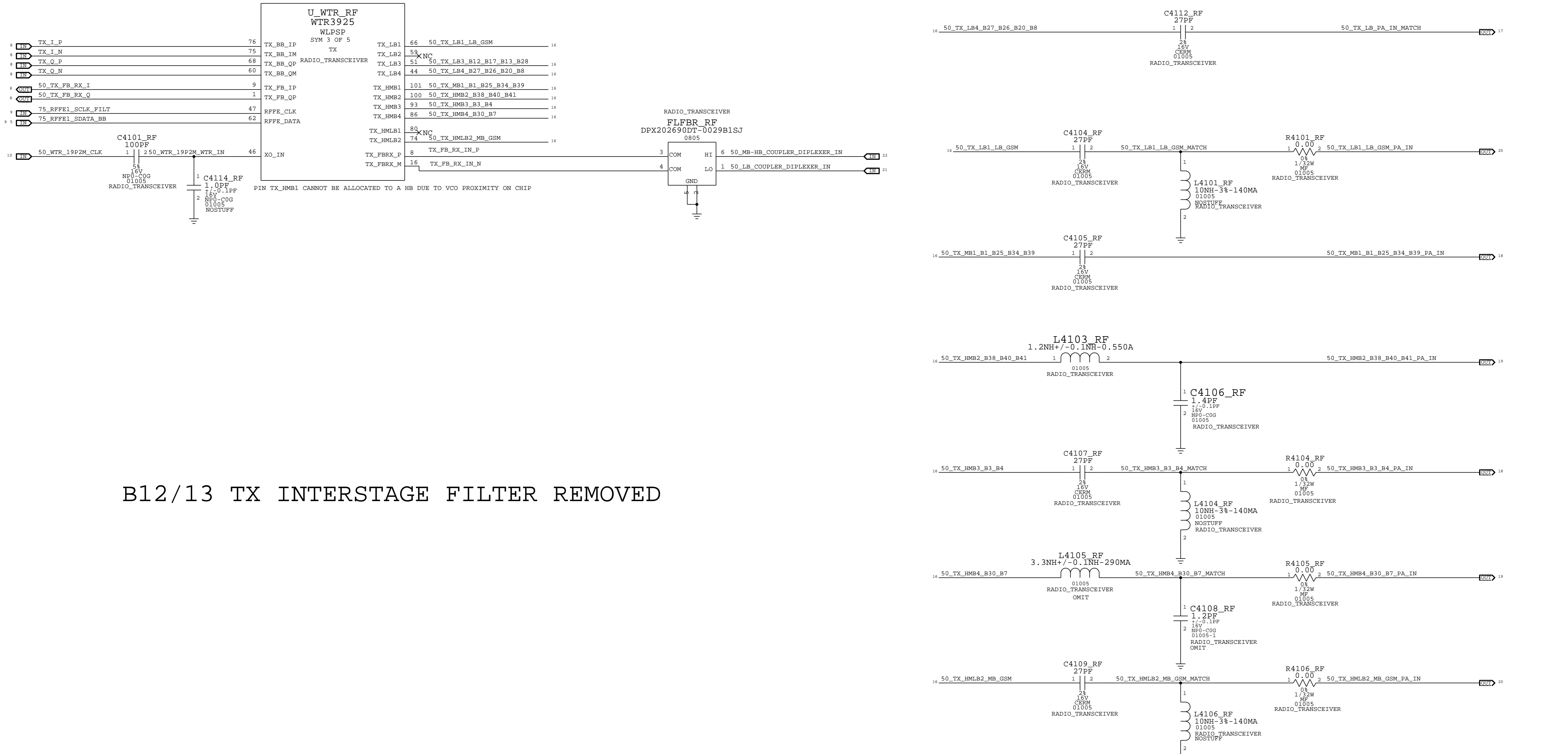
DRX MODULE PORTS ARE DC BLOCKED



GPS FILTER
PLACE NEAR U_WTR

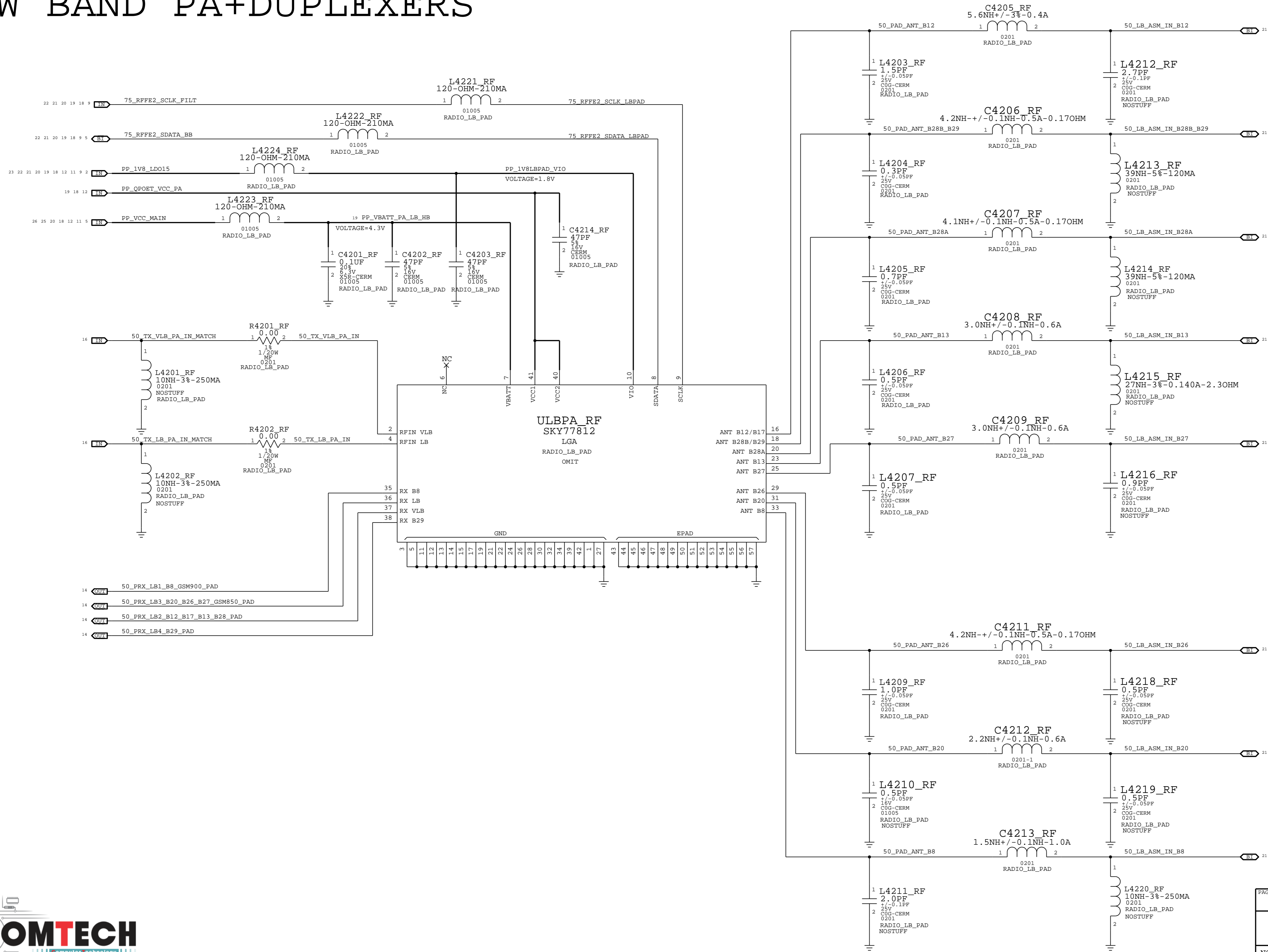


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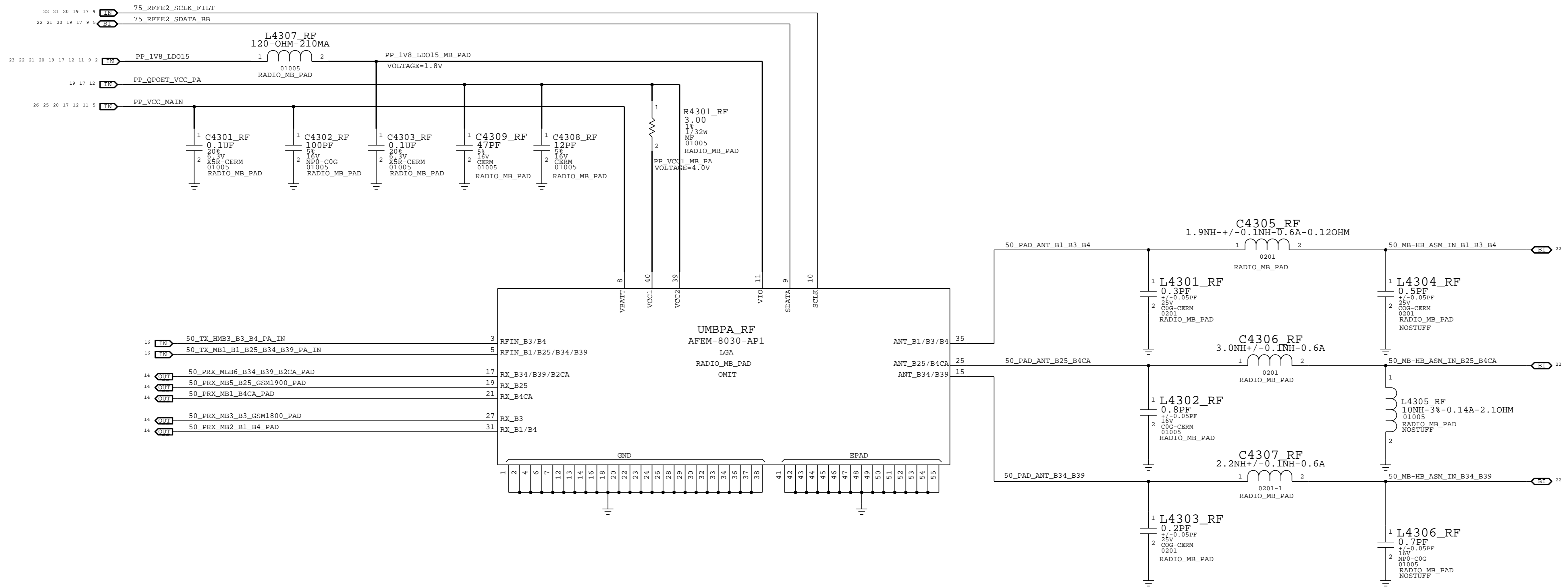
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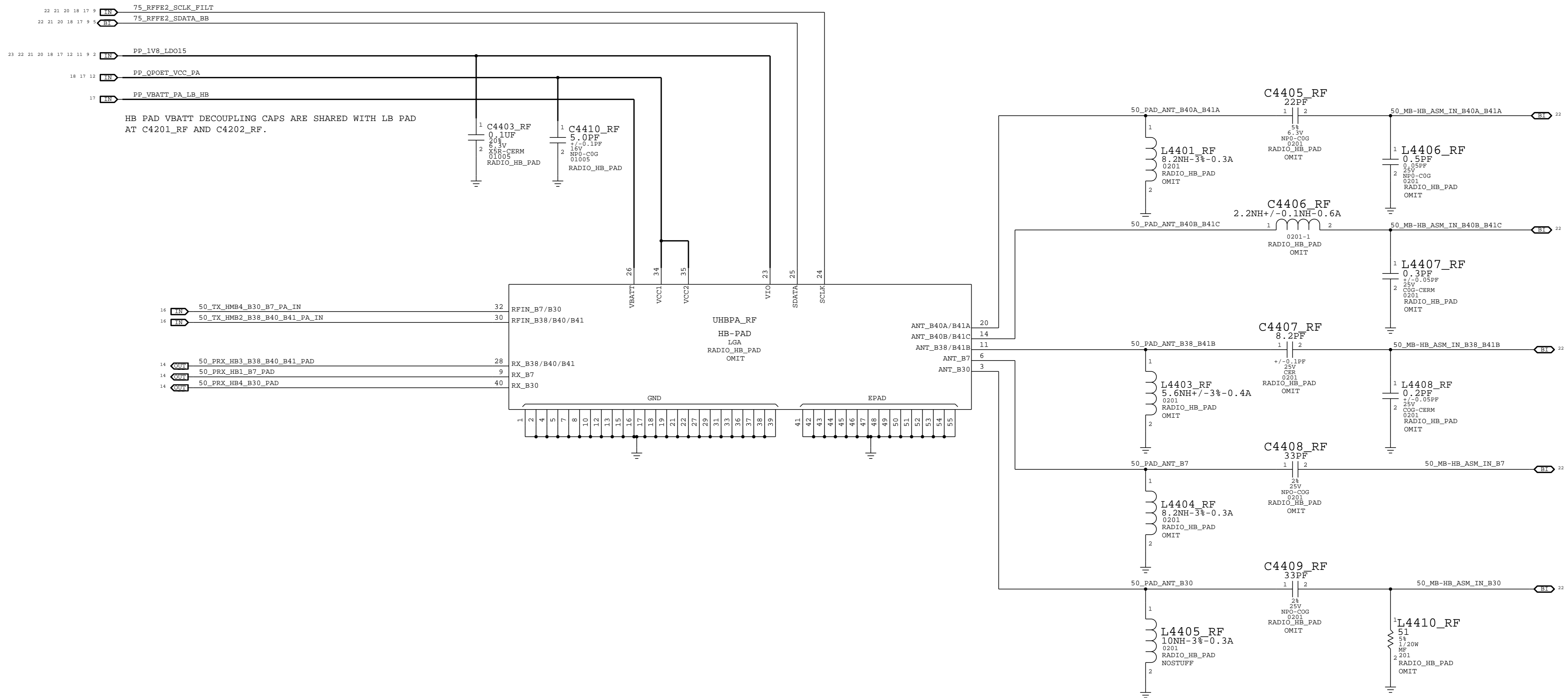
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MID BAND PA+DUPLEXERS



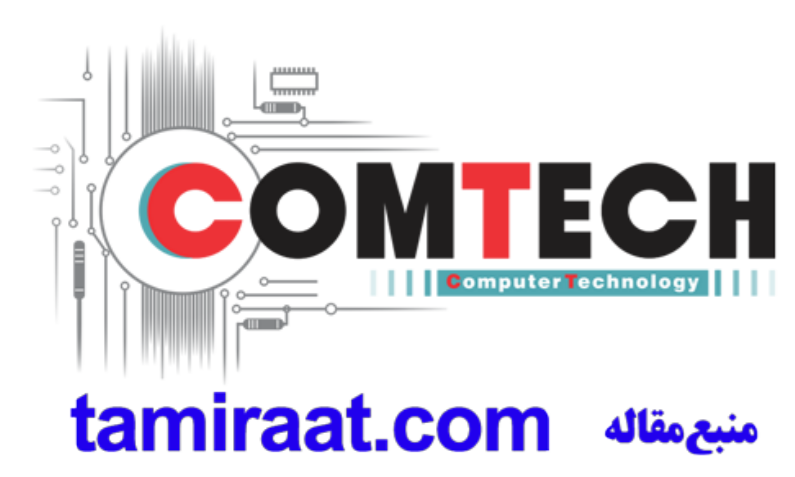
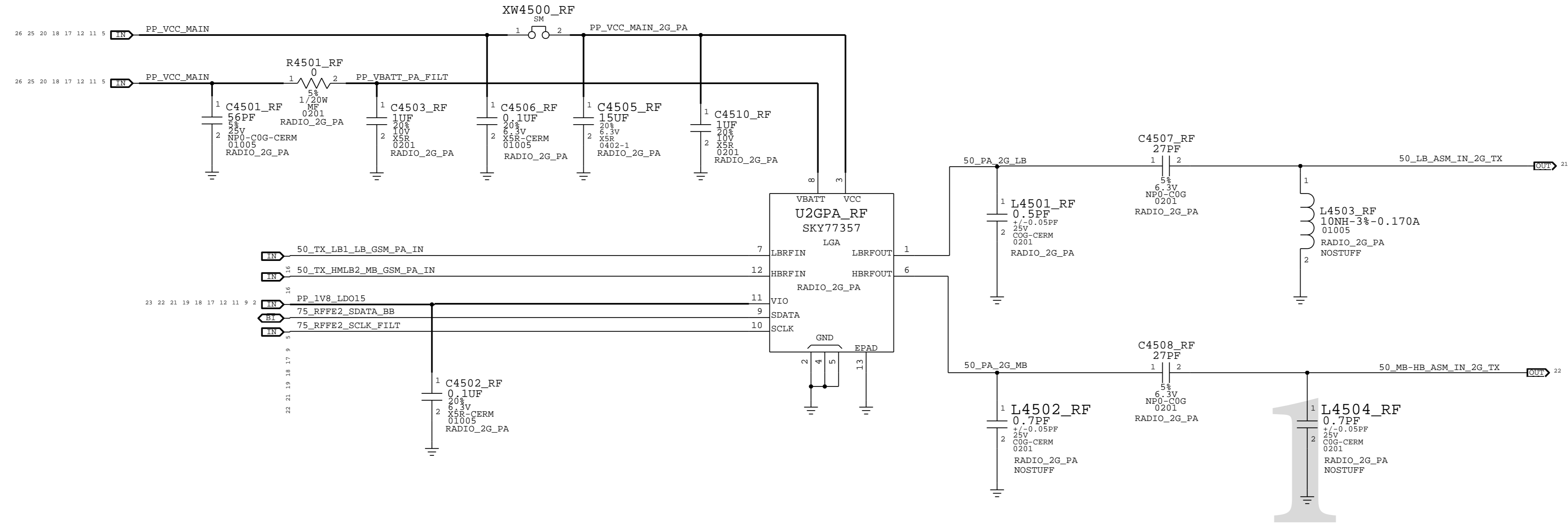
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HIGH BAND PA+DUPLEXERS



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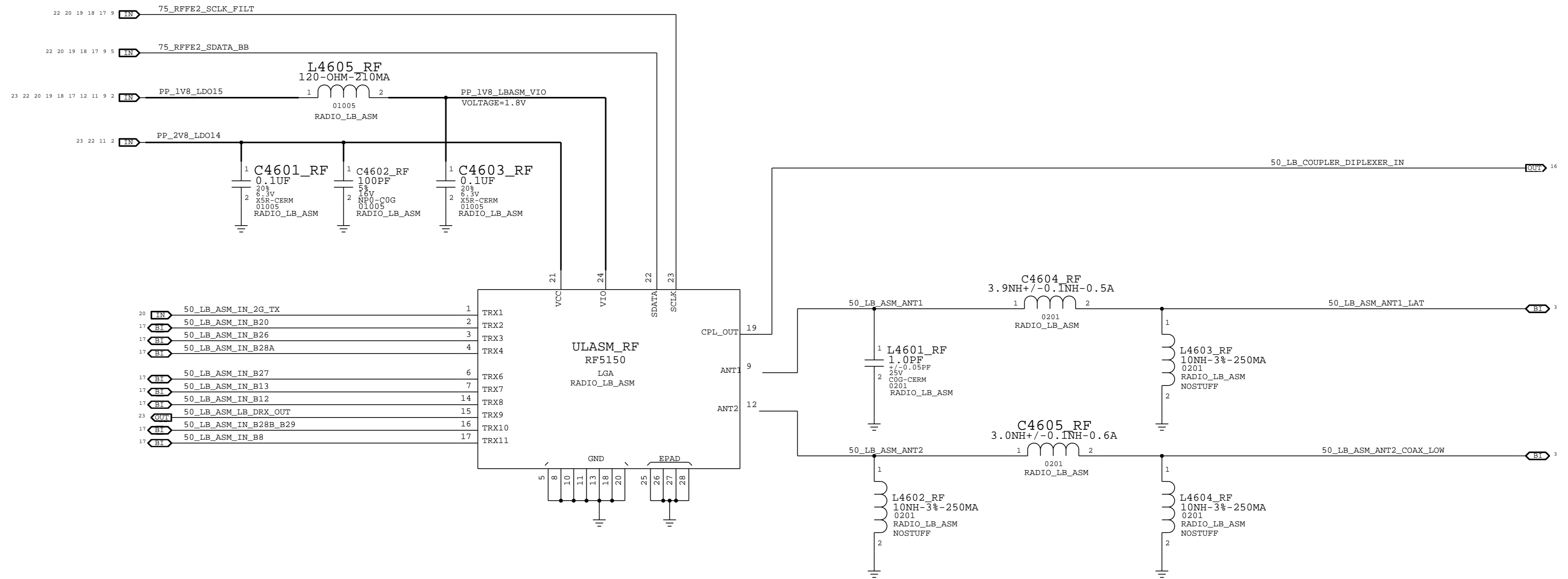
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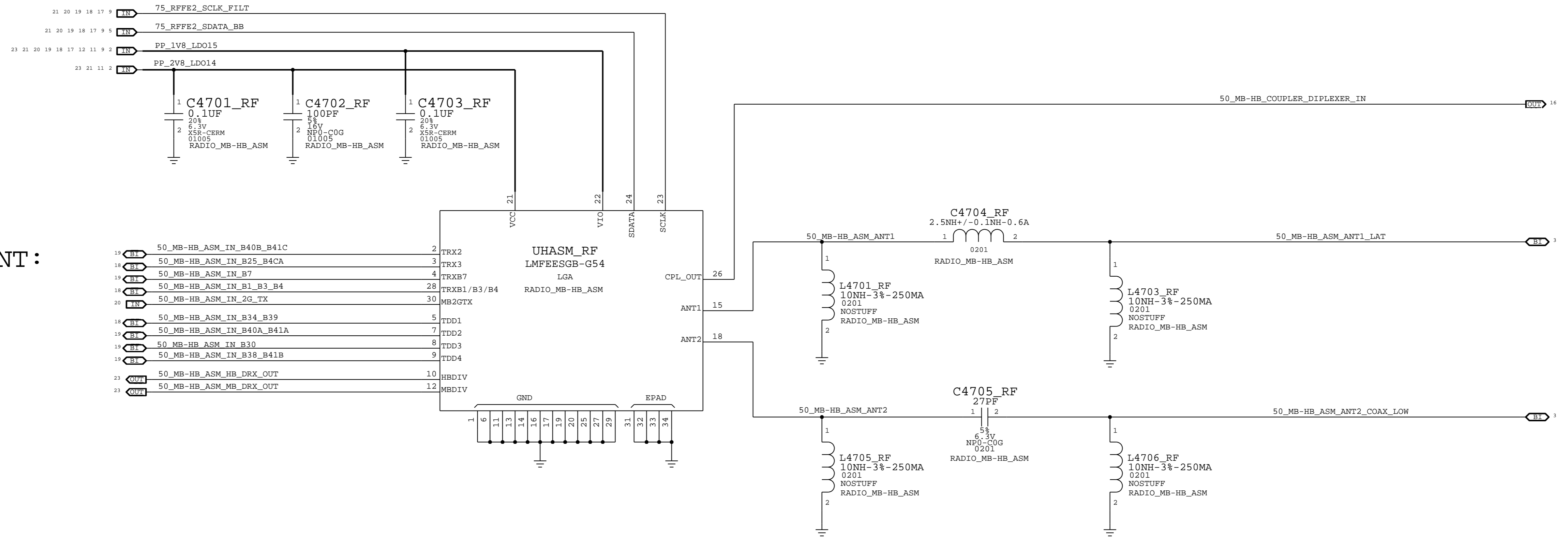
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LOW BAND ANTENNA SWITCH MODULE



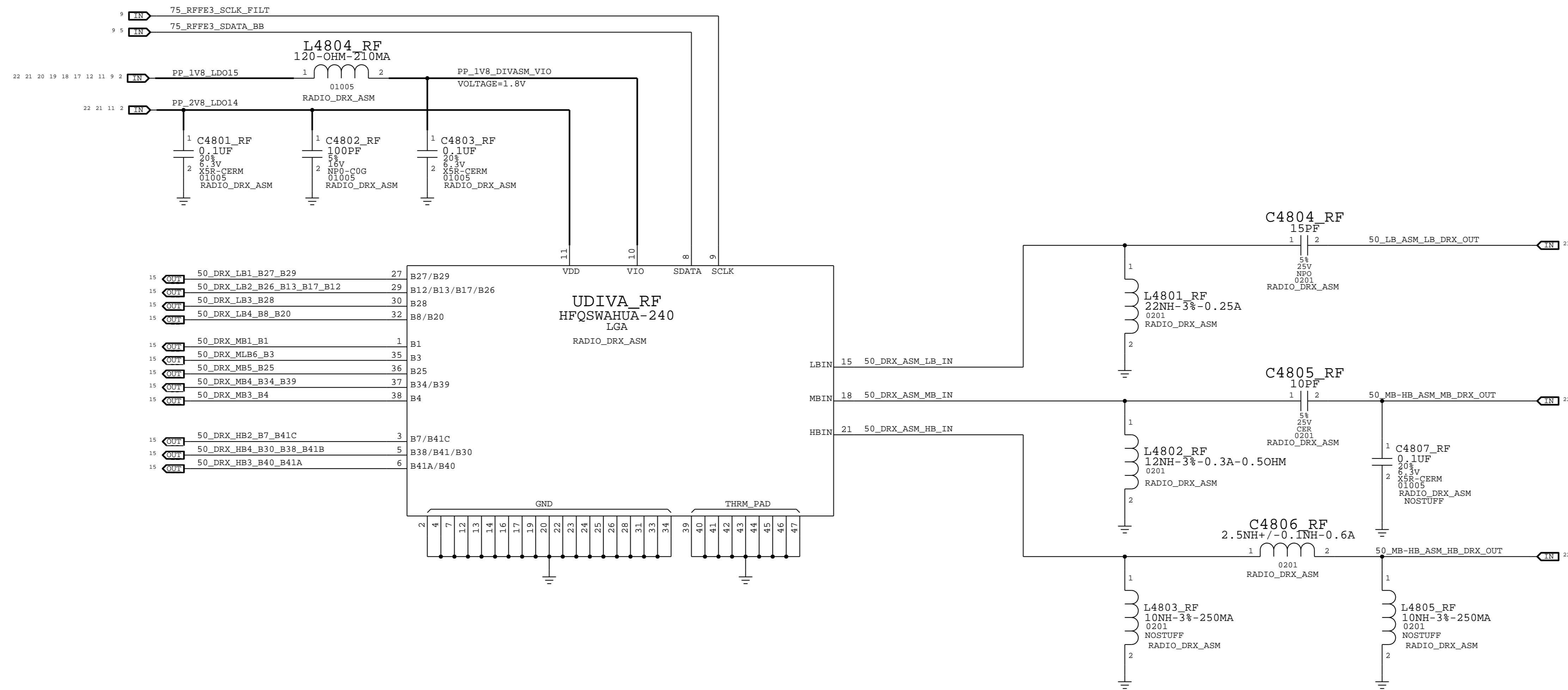
MID-HIGH BAND ANTENNA SWITCH MODULE

EVT ASM ASSIGNMENT:
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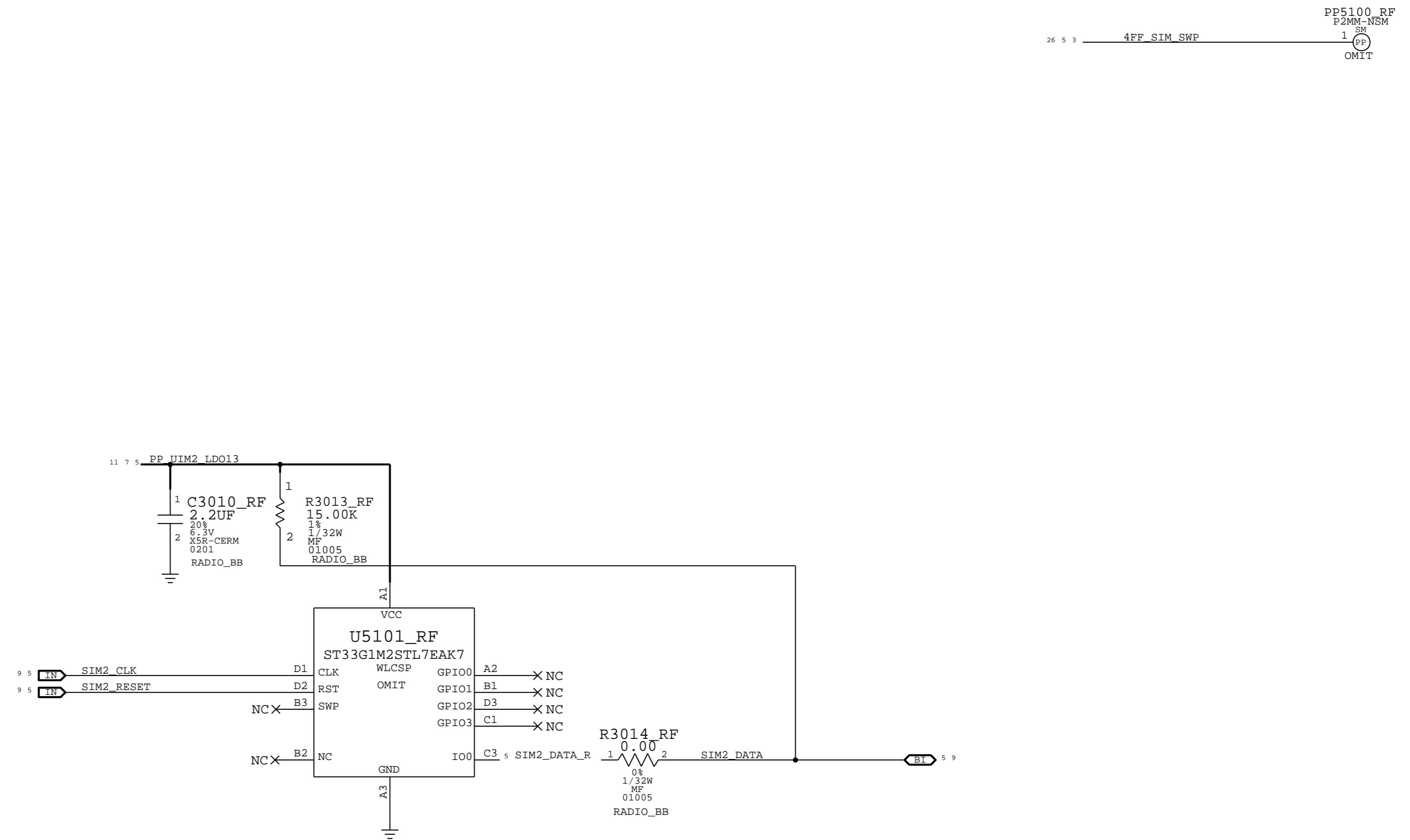
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DIVERSITY MODULE



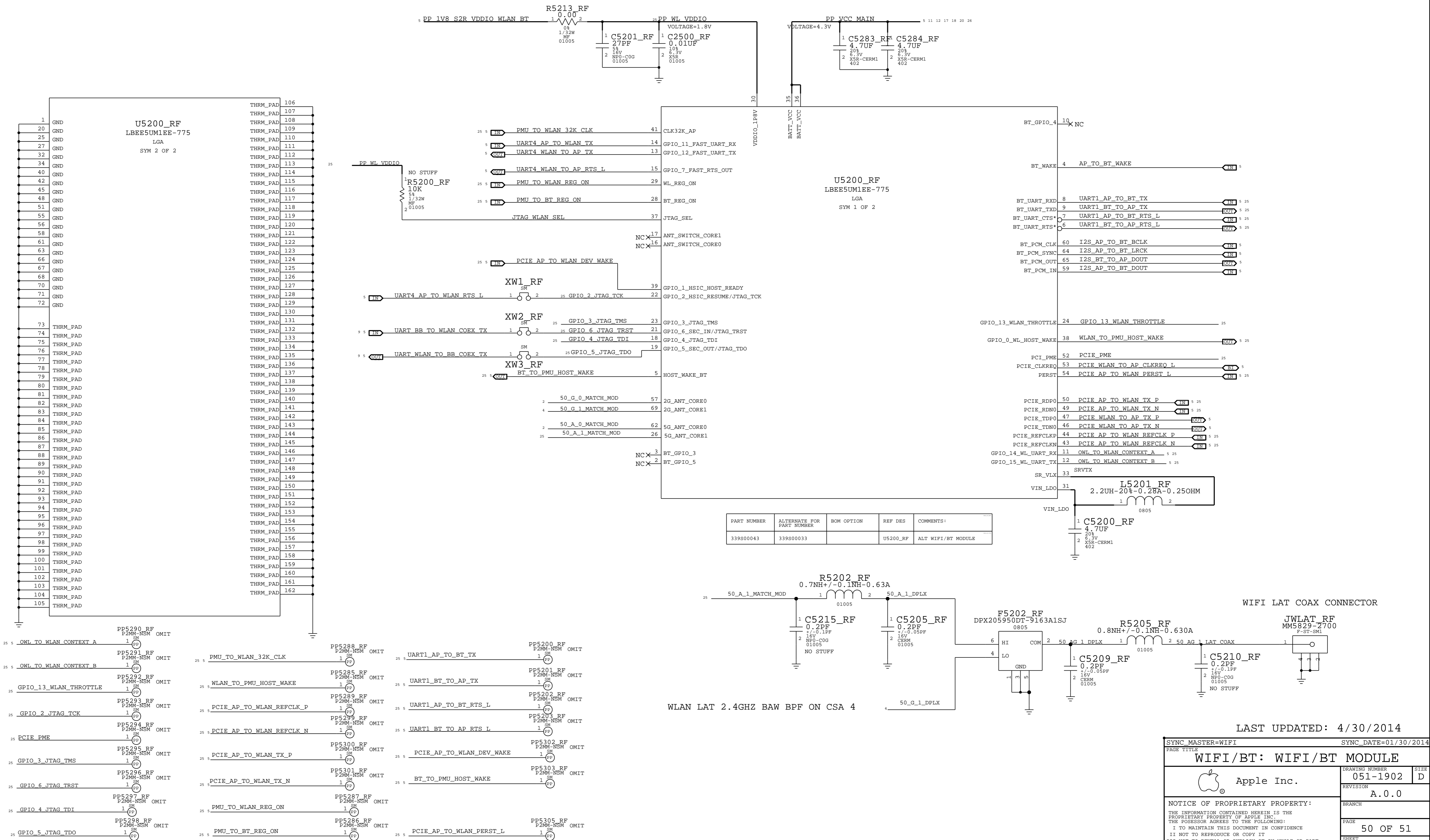
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WIFI / BT



LAST UPDATED: 4/30/2014

SYNC_MASTER=WIFI		SYNC_DATE=01/30/2014	
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STOCKHOLM

ALL NETNAMES NEED TO BE CHECKED

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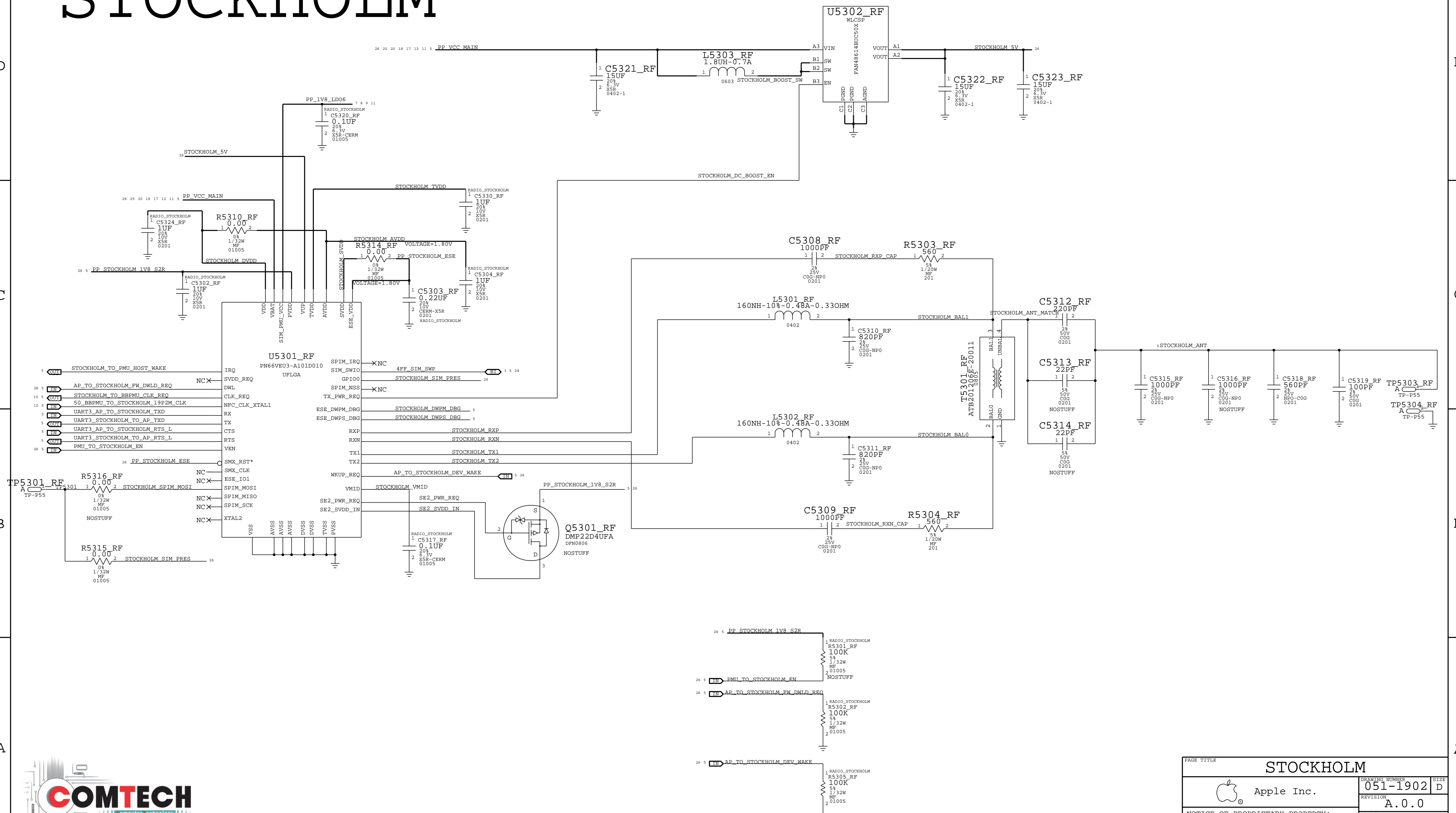
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