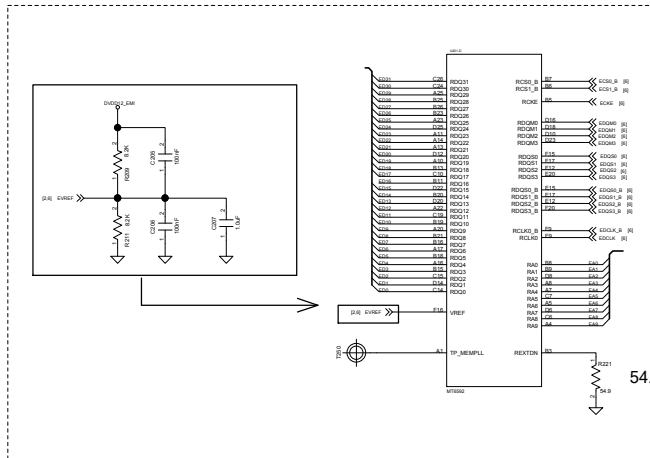
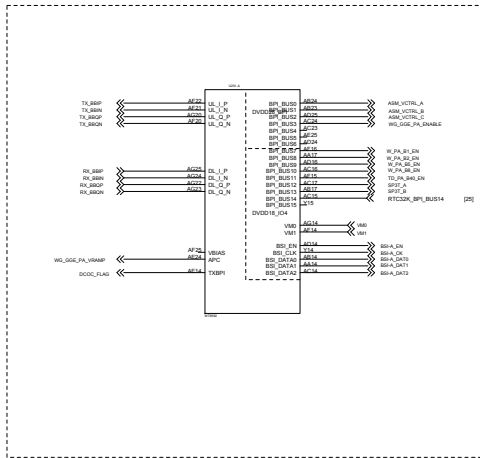
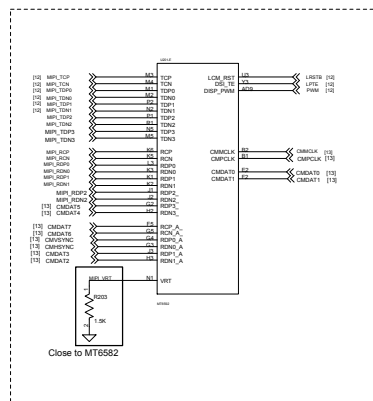
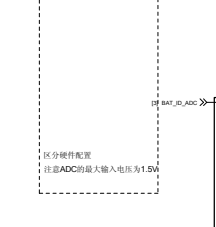
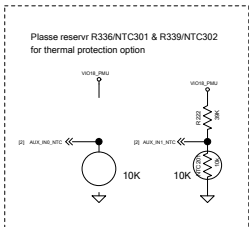
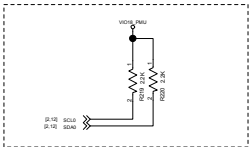
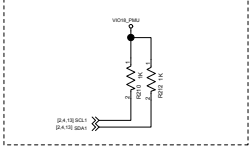
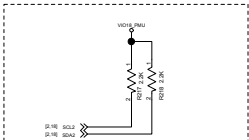
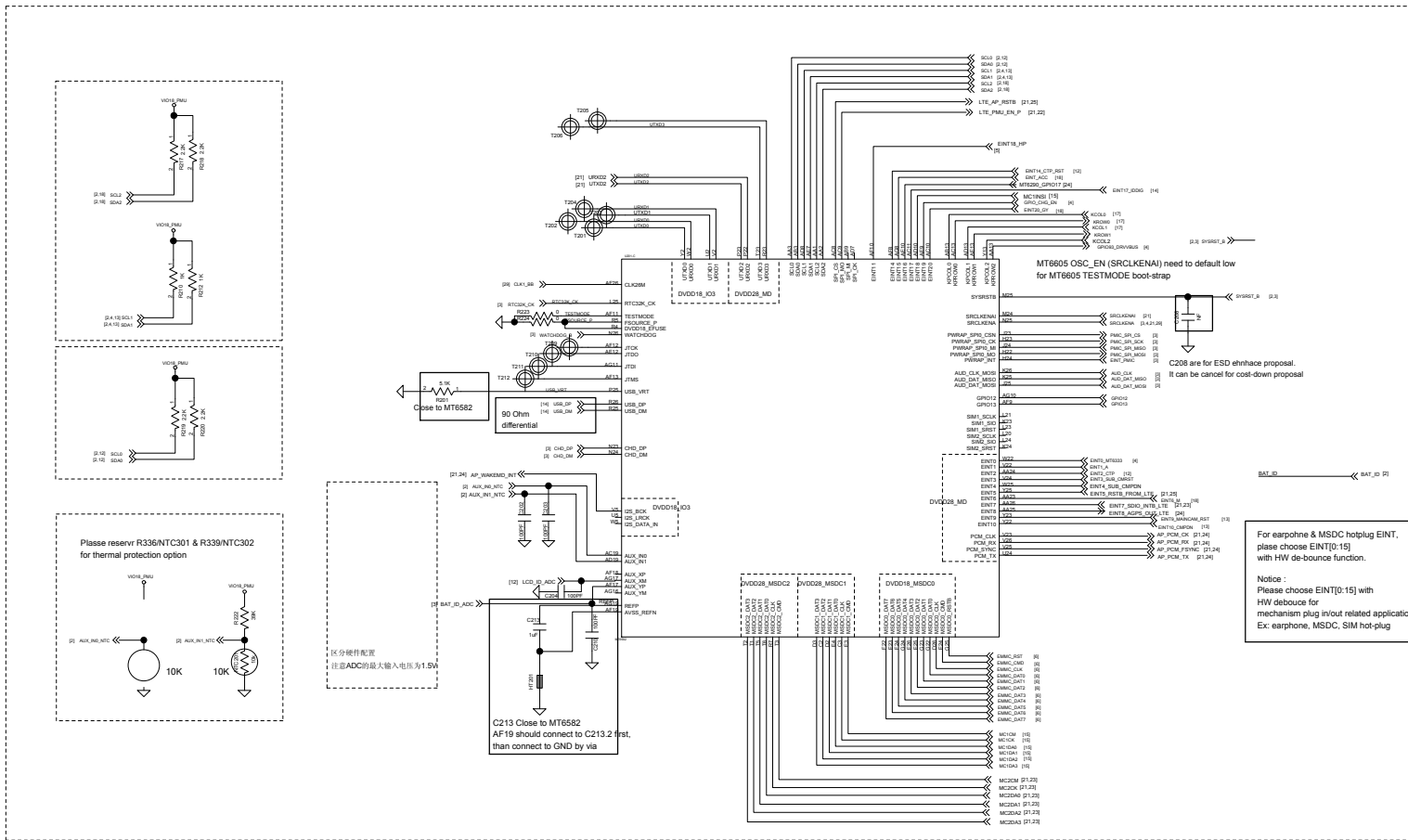


MT6582 R119=NF, R101=0, R102=NF, R120=NF, R110=0
 MT6592 R119=0, R101=NF, R102=0, R120=0, R110=NF

MT6582 C135, C137, 可以NF
 MT6592 C135=22uF, C137=22uF



54.9欧姆, 1%, 0402

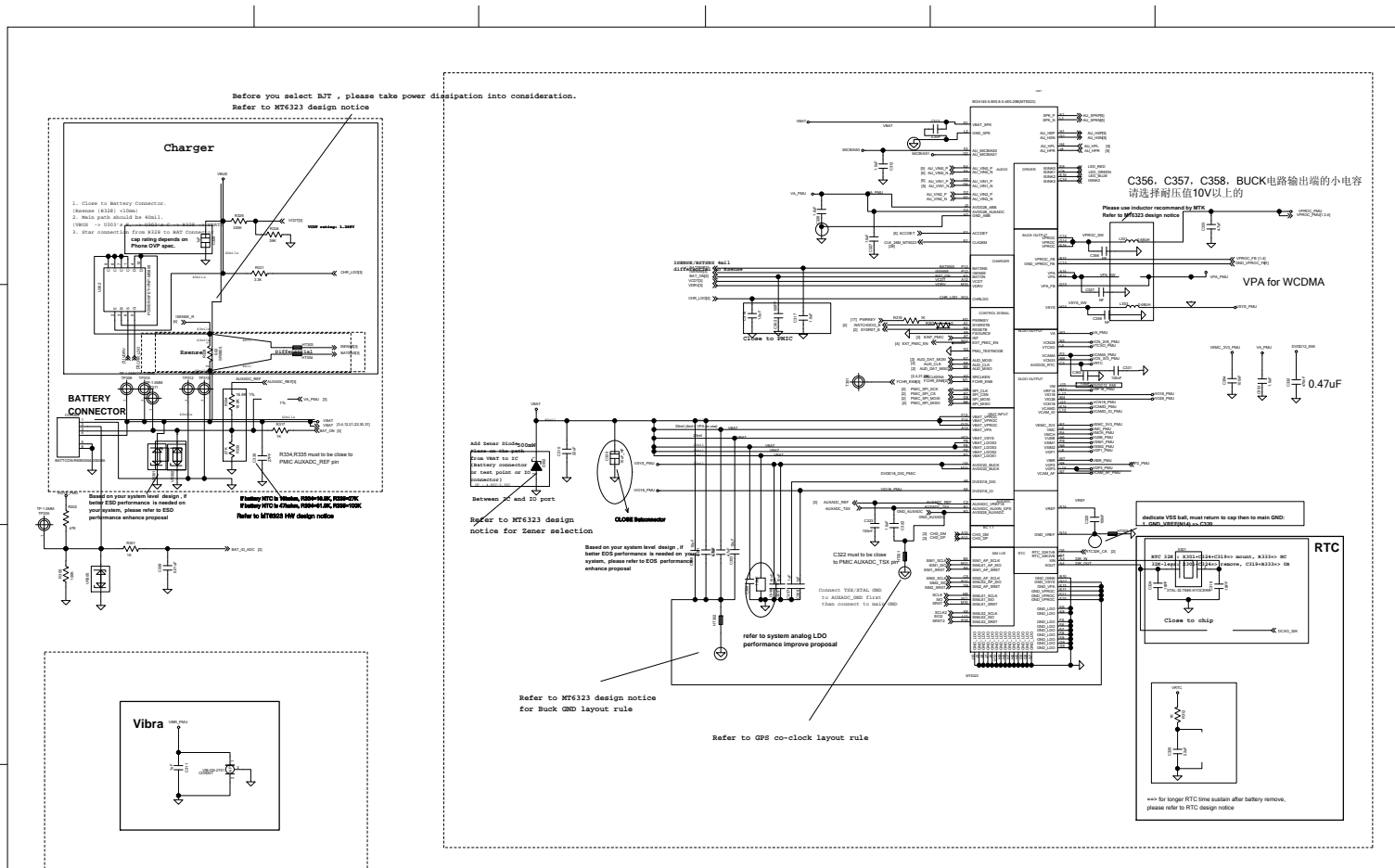


<Parallel Cam./MIP1 CSI Mux Table>

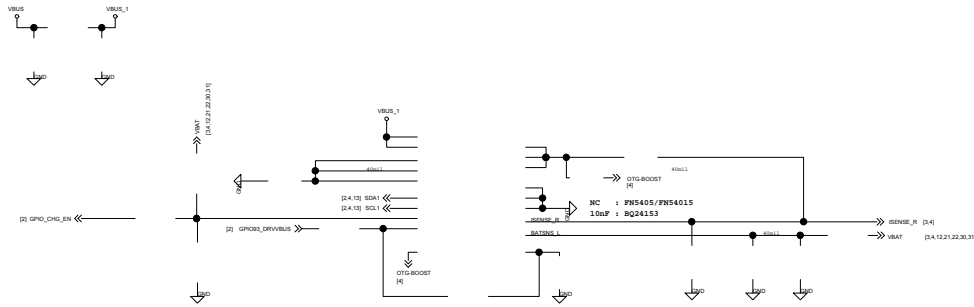
MIP1 CSI 0F Part	Parallel CSI Mux
RD92	CM2A78
RD93	CM2A79
RD94	CM2A7A
RD95	CM2A7B
RD96	CM2A7C
RD97	CM2A7D
RD98	CM2A7E
RD99	CM2A7F
RD9A	CM2A7G
RD9B	CM2A7H
RD9C	CM2A7I
RD9D	CM2A7J
RD9E	CM2A7K
RD9F	CM2A7L
RD9G	CM2A7M
RD9H	CM2A7N
RD9I	CM2A7O
RD9J	CM2A7P
RD9K	CM2A7Q
RD9L	CM2A7R
RD9M	CM2A7S
RD9N	CM2A7T
RD9O	CM2A7U
RD9P	CM2A7V
RD9Q	CM2A7W
RD9R	CM2A7X
RD9S	CM2A7Y
RD9T	CM2A7Z

Notice :
Please choose EINT[0:15] with HW debounce for mechanism plug in/out related application.
Ex: earphone, MSDC, SIM hot-plug

Regulator	Output Voltage(V)	Output Current(A)	Input Decoupling	Output Decoupling	Notes
VPRD0	0.7-1.4	2800	>10uF	L=0.68uH C=10uF*4	Total output cap=10uF
V8V8	2.2	1200	>10uF	L=0.68uH C=10uF*2	Total output cap=20uF
VPA	0.5-3.4	600	>4.7uF	L=2.2uH C=2.2uF*2	Output cap range 4uF ~1.20uF
LDO	Output Voltage(V)	Output Current(A)	Input Decoupling	Output Decoupling	Notes
VM	1.24 (1.38V1.54V1.84)	700	10uF	20%~20%	Fair-end bypass cap
VW18	1.825	200	1uF	20%~200%	Fair-end bypass cap
VON18	1.8	300	4.7uF	20%~200%	Fair-end bypass cap
VON18	1.8	150	1uF	20%~200%	Fair-end bypass cap
VCAM0	1.2 (1.31V1.51V1.8)	150	1uF	20%~20%	Fair-end bypass cap
VCAM_IO	1.8	100	1uF	20%~20%	Fair-end bypass cap
VOP3	1.2 (1.31V1.51V1.8)	200	1uF	20%~20%	Fair-end bypass cap
VA	2.8	150	1uF	20%~20%	Fair-end bypass cap
VTCK0	2.8	40	1uF	20%~20%	Fair-end bypass cap
VON28	2.8	30	1uF	20%~20%	Fair-end bypass cap
VCAMA	2.8	150	3.2uF	20%~20%	1uF rear-end 2.2uF Fair-end bypass cap
VON33	3.3/3.4/3.5/3.6	250/0/1322	4.7uF	20%~20%	Fair-end bypass cap
VOC8	2.8	200	2.2uF	20%~200%	Fair-end bypass cap
VUSB	3.3	20	1uF	20%~20%	Fair-end bypass cap
VAC	1.8 (3.3)	100	1uF	20%~20%	Fair-end bypass cap
VMCH	3.0 (3.3)	400	2.2uF	20%~20%	Fair-end bypass cap
VEMC_3V3	3.0 (3.3)	400	4.7uF	20%~20%	Fair-end bypass cap
VCAM_AF	1.2/1.31V1.51V1.8 2.8/3.0/3.3	100	1uF	20%~20%	Fair-end bypass cap
V8M1	1.8 (3.0)	50	1uF	20%~20%	Fair-end bypass cap
V8M2	1.8 (3.0)	50	1uF	20%~20%	Fair-end bypass cap
VOP1	1.2/1.31V1.51V1.8 2.8 (3.0/3.3)	100	1uF	20%~20%	Fair-end bypass cap
VOP2	1.2/1.31V1.51V1.8 2.8 (3.0/3.3)	100	1uF	20%~20%	Fair-end bypass cap
V8R	1.2 (1.31V1.51V1.8 2.8/3.0/3.3)	100	1uF	20%~20%	Fair-end bypass cap
VON15	1.8	21	1uF	20%~20%	Fair-end bypass cap
VRTC	2.8	2	0.1uF to 1000pF	20%~20%	Fair-end bypass cap



当使用Switching charger :Rsense R328 use 56m ohm

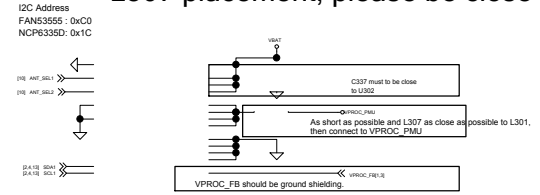


FN5405 R410=10K,R412=NF, C402=NF
 FN5402 R410=NF, R412=10K,C402=NF
 BQ24158 R410=10K,R412=NF, C402=10nF

If switching charger is used:
 (1) R1801-R1805, C1801-C1806, L1801, U1801 are needed
 (2) U303, U304 change to NC
 (3) R328 change to 56k Ohm

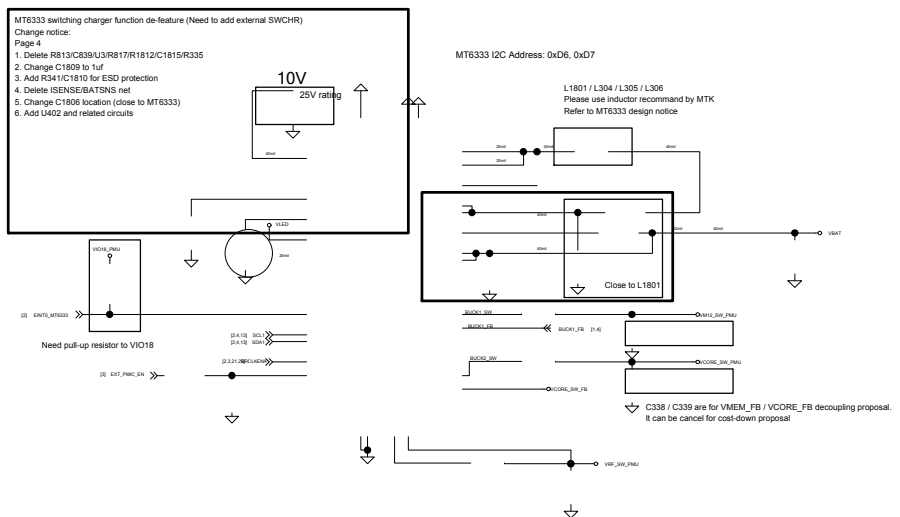
Switching Charger

U302 placement, please be close to MT6322
 L307 placement, please be close to L301



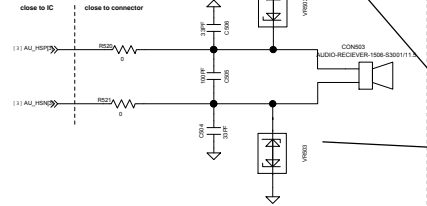
External DC-DC for VPROC

EXT_PMIC

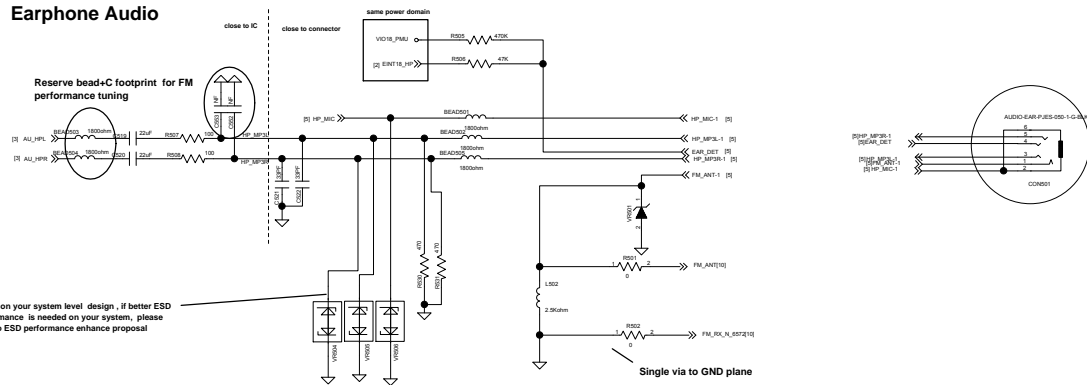


TITLE	<TITLE>	REV	<REV>
DOCUMENT NO.	04_POWER_SWCHR_EXT_PMIC	SIZED	A1
DEPARTMENT	Hardware DEPT.		
COMPANY			
DESIGNER	<DESIGNER>	Last Saved Date	2014-7-30
		SHEET	4 of 33

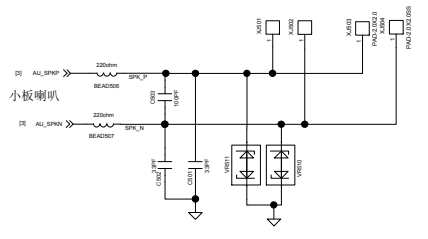
Based on your system level design , if better desense performance is needed on your system, please refer to desense Receiver proposal



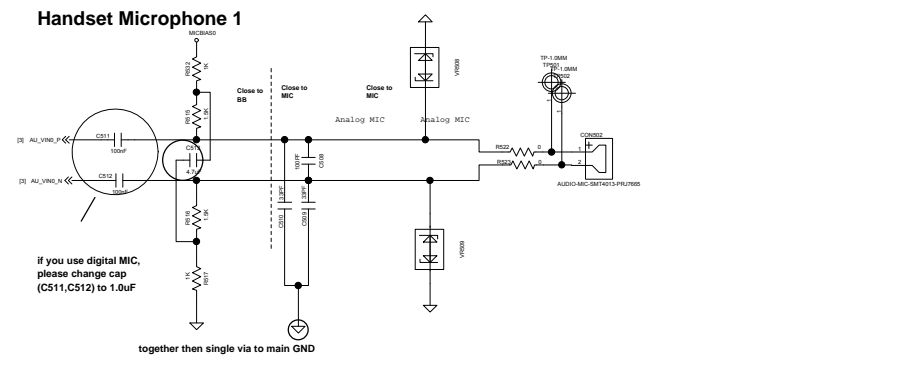
Earphone Audio



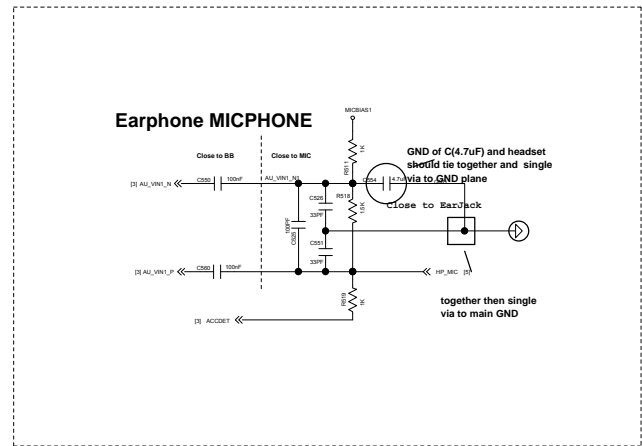
小板喇叭



Handset Microphone 1

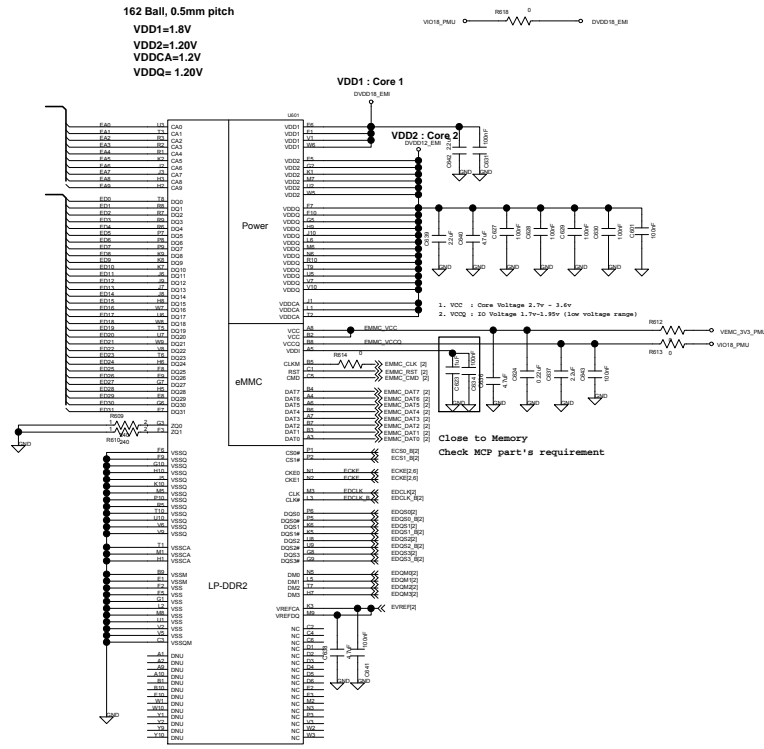


Earphone MICPHONE

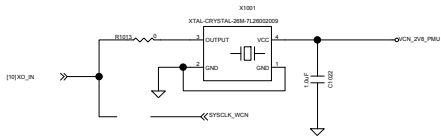


eMMC+LPDDR2

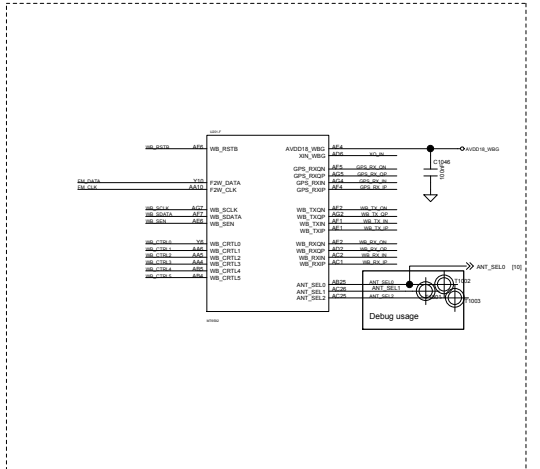
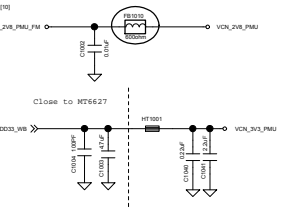
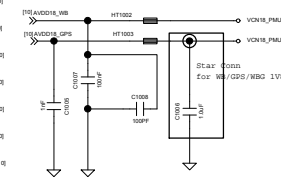
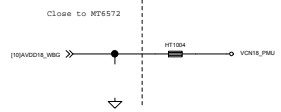
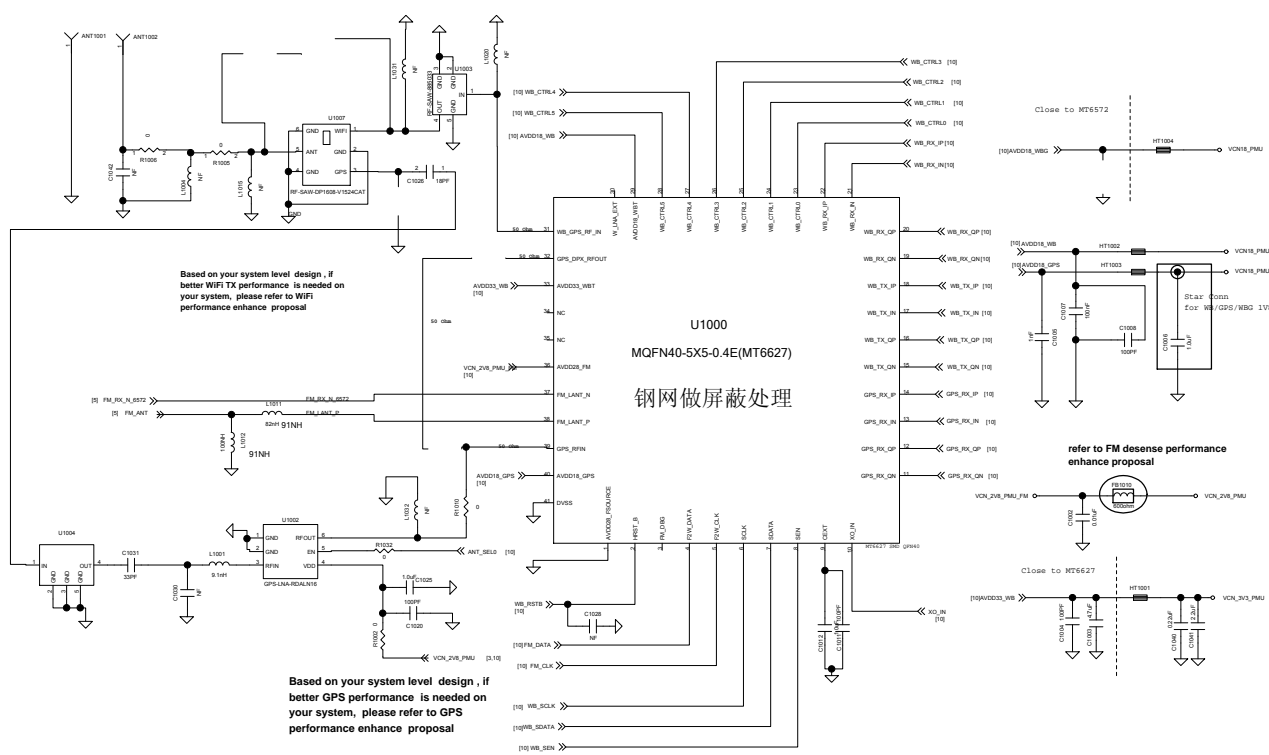
162 Ball, 0.5mm pitch
 VDD1=1.8V
 VDD2=1.20V
 VDDCA=1.2V
 VDDQ=1.20V



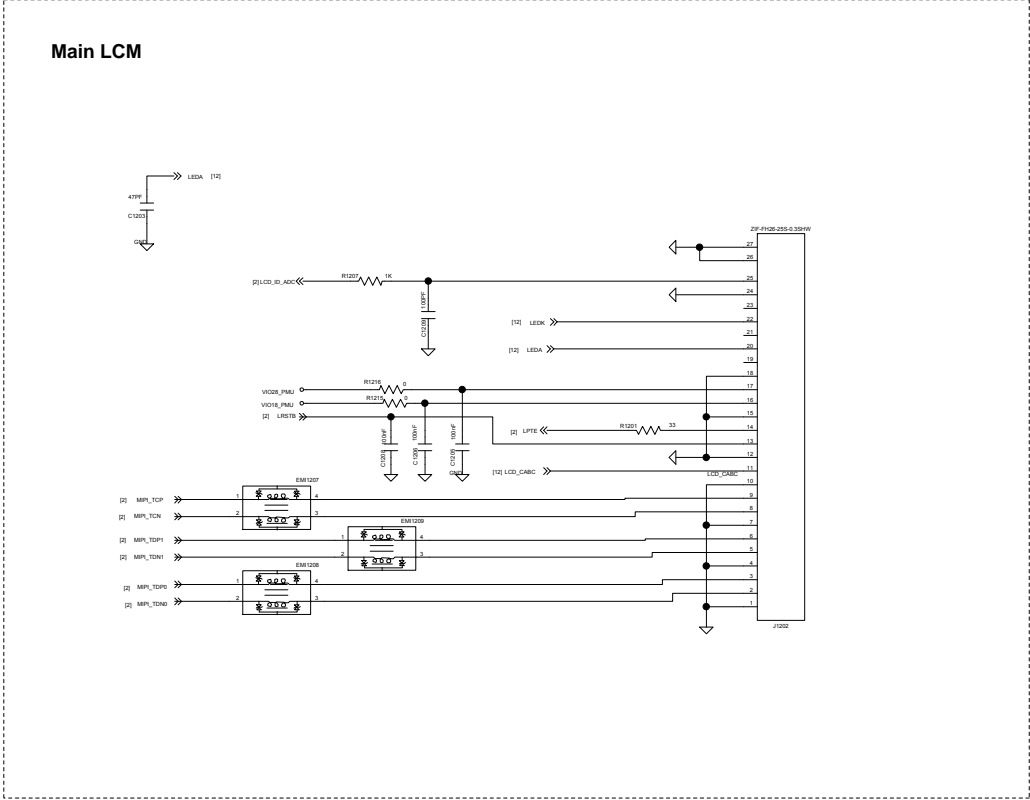
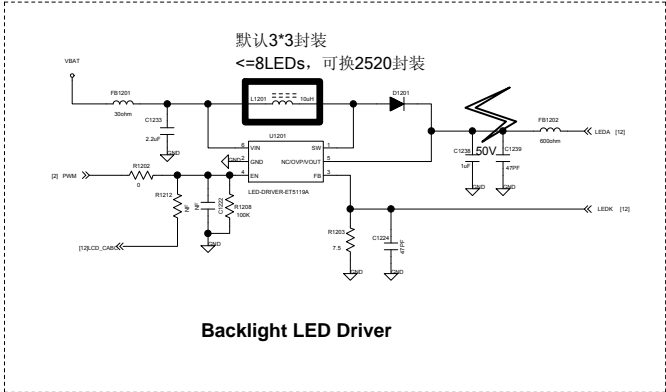
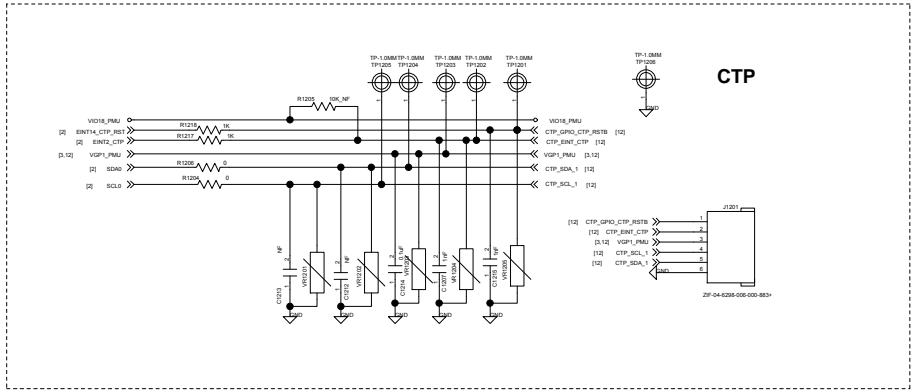
SG116D-12X13.5-0-0-SP60-271011P20240000MCP300mm MT28D2240THTCF-06

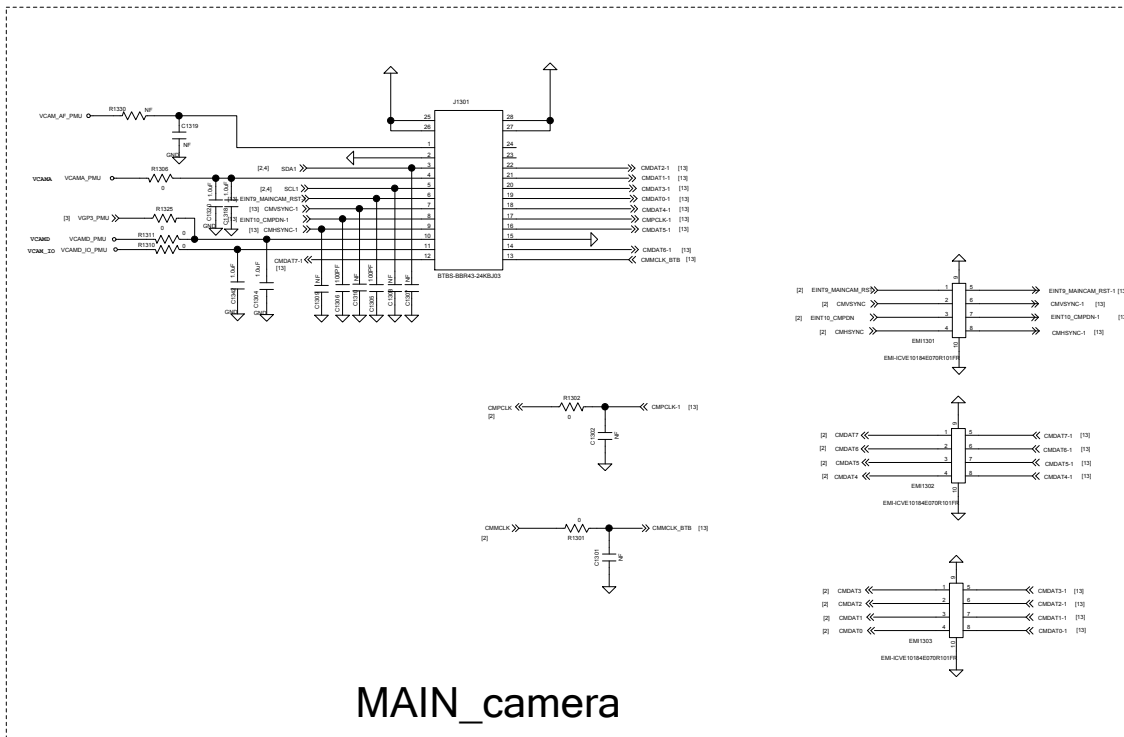


WiFi/BT/GPS Single ANT Ref.

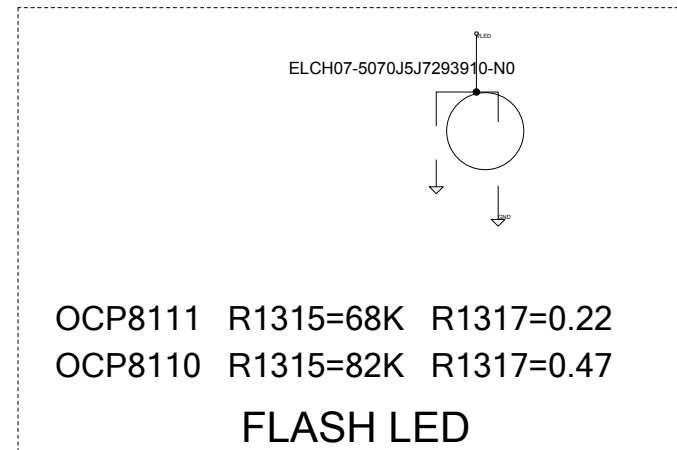


NFC





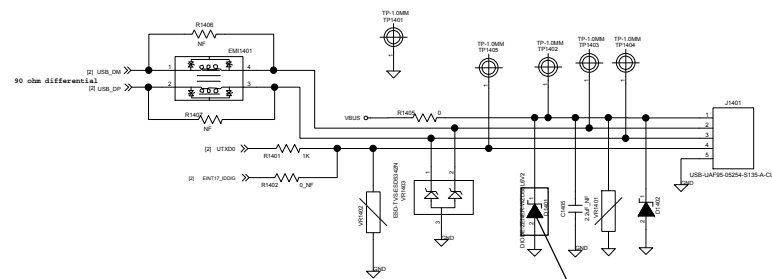
MAIN_camera



light sensor 0x90 Write(STK3171)
 light sensor 0x46 Write (LTR-558ALS)

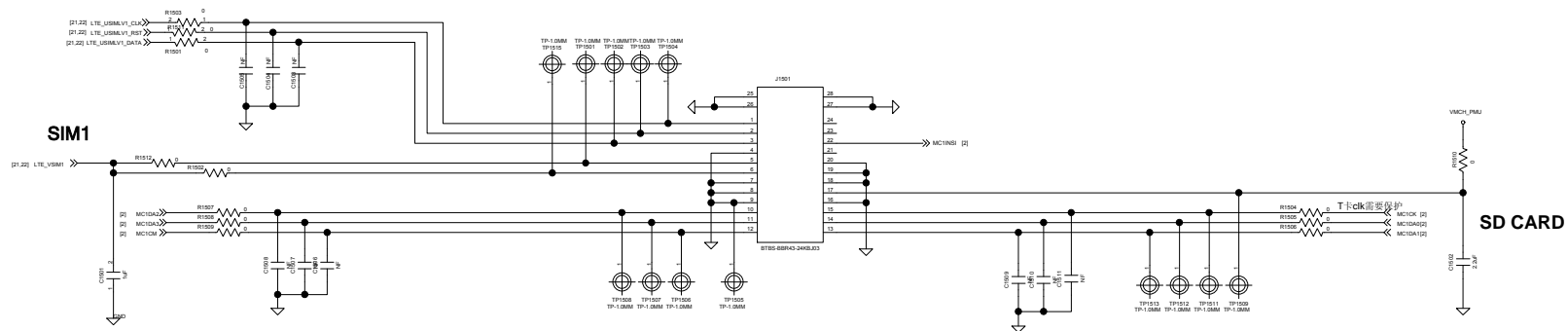
Main Camera / Sub Camera share power domain design
 should double check the voltage level is compatible

USB HS IF

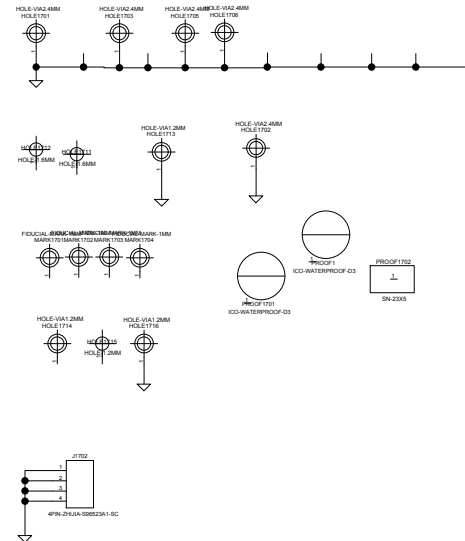
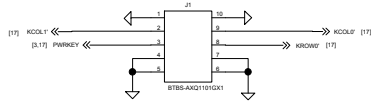
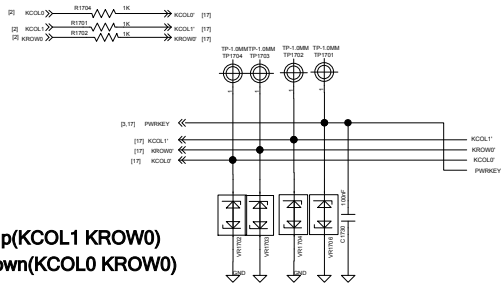


华为项目请预留此二极管，有要求

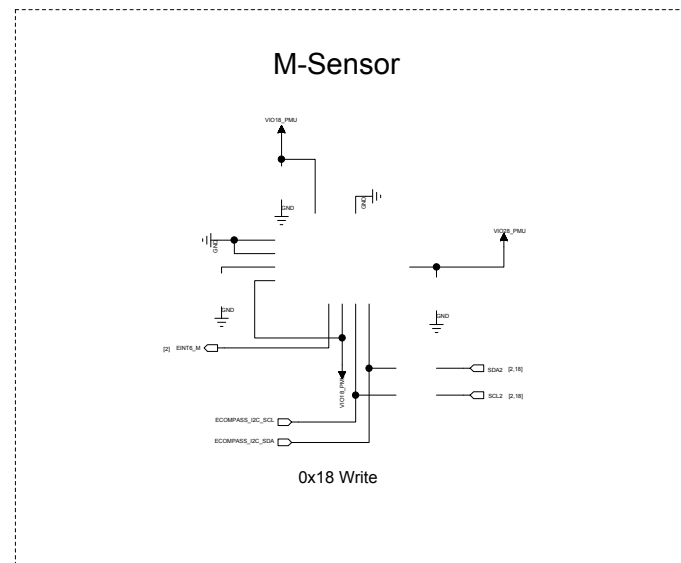
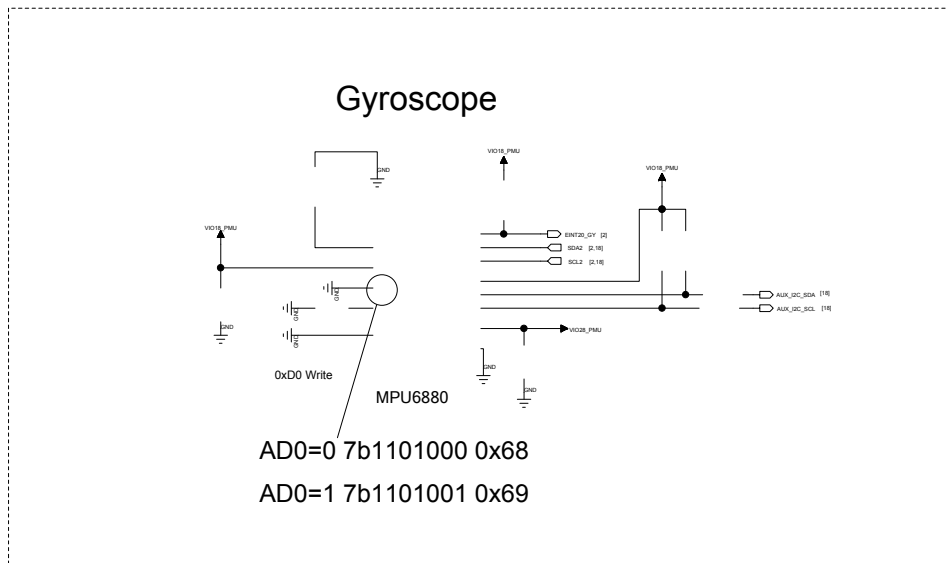
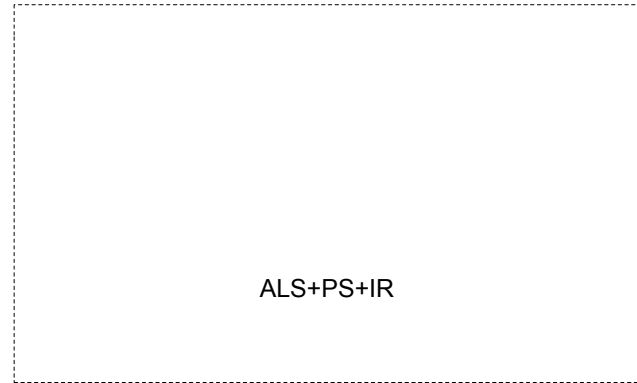
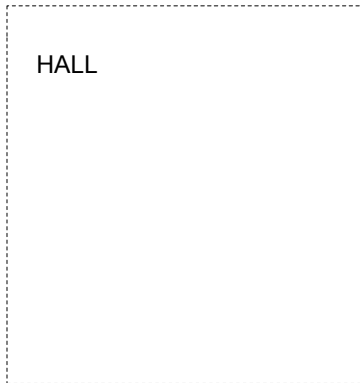
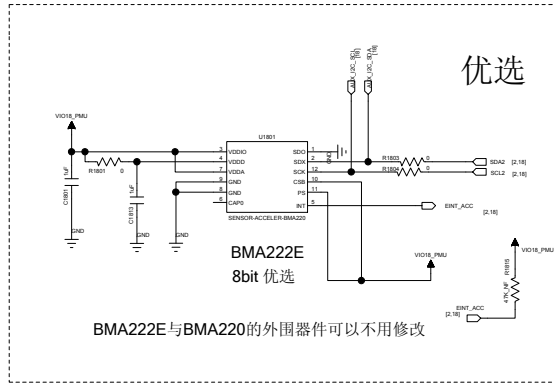
1. 为方便软件调试打LOG，将UTXD0接至USB接口的Pin 4；
2. 如果有OTG功能，R708=0 欧姆



T-card ESD protection is optional depends on
T-card's type and position

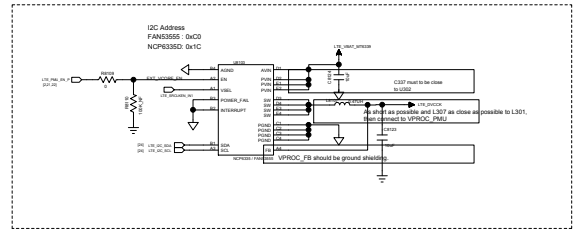
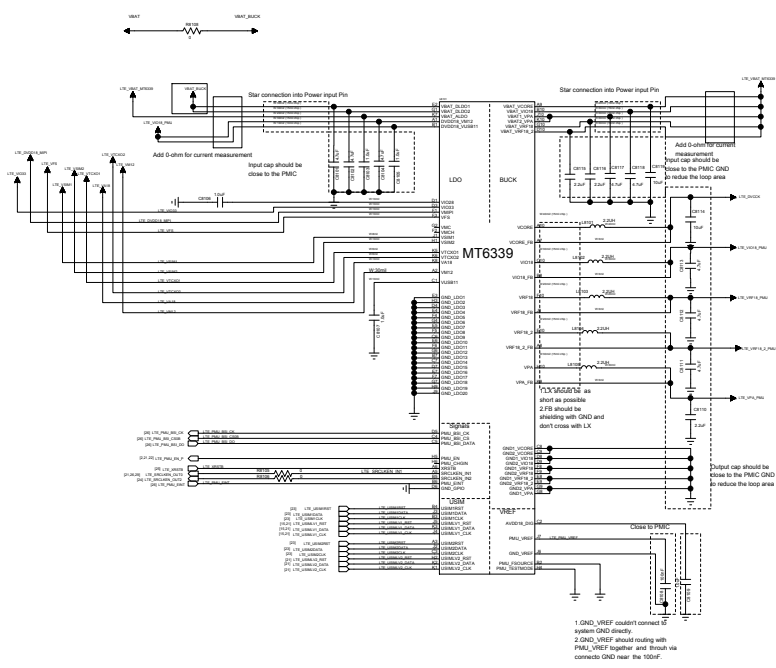


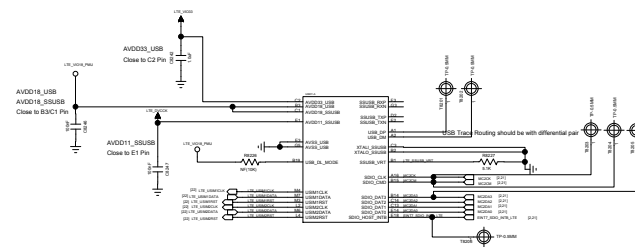
TITLE	<TITLE>	REV	<REV>
DOCUMENT NO.	17_KEY_LEDS	SIZED	A1
DEPARTMENT	Hardware DEPT.		
COMPANY			
DESIGNER	<DESIGNER>	Last Saved Date	2014-7-30
		SHEET	17 of 33

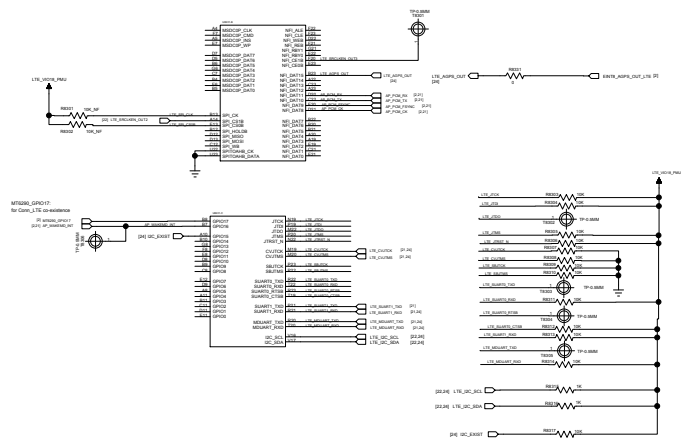


Version History

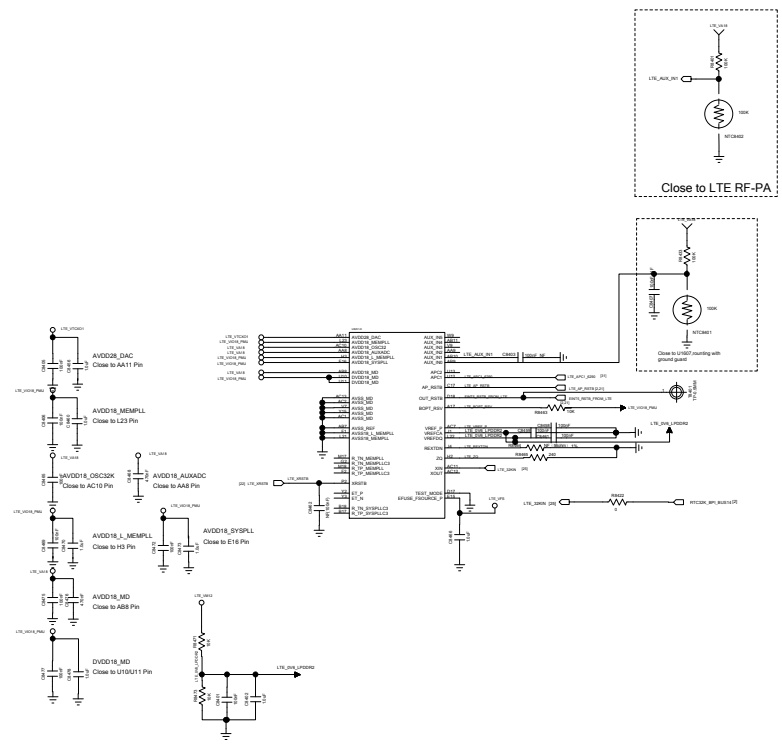
Date	Version	Page	Description
2013/03/11	V0.1		Draft release
2013/03/22	V0.11	01	1. Add R107/R103
			2. Delete R605/R107
		03	1. Add R326/R328
		04	1. Delete U1602 and related circuit 2. Add U401 and related circuit
		06	1. Add R418
		07	1. Delete R636/R634
		10	
		15	
		1	
		3	
		7 8 9	
2013/03/22	V0.12	1 3 6 12	
		10	1. C222 change to 4.7uF
		12	1. Add C914 / R904
			1. Swap U2 (MT6627) pin 36, 37 and 38 and their circuitry. 2. Remove R1070, R1073 3. F201 change footprint to DIPLEXER@SMD/DP1608 1. Change C1218, C1219 to 22pF, Change C1214, C1215 to 1.2pF, Change C1216, C1217 to 68pF, Change c1203 4.7uF
			1. Remove C138, c153, C145, Change C140, C148 to 1u 1. Add C353, R339, C243 2. Modify Isense trace. 1. Update RF matching (L615, L600, L619, L616, L622, C670, L623, L626, L625, C629, C627, C603, C662, C654, L617, C624, R617, C651, C655, C652, L610, C648, C657, L609, L608, L611, C649, C626, L607, R620, C634, L631, L627, L629, L636) 1. Add R360-R367, R101, R108, R105, R106, R109, R102, R104, R116, R117, R118, R115, R114, R113, R111 R418, R512, R513, R915, R916 for low-power testing

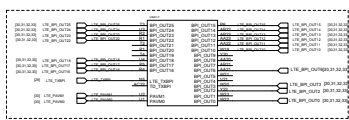
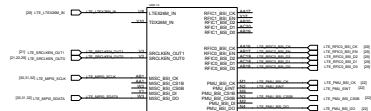


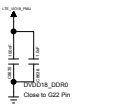
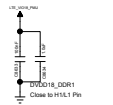
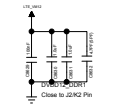
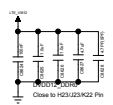
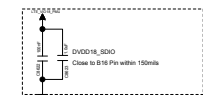
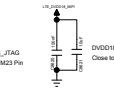
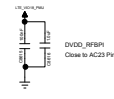
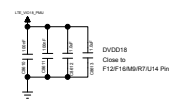
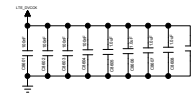
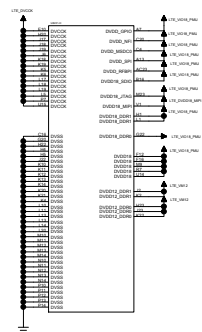


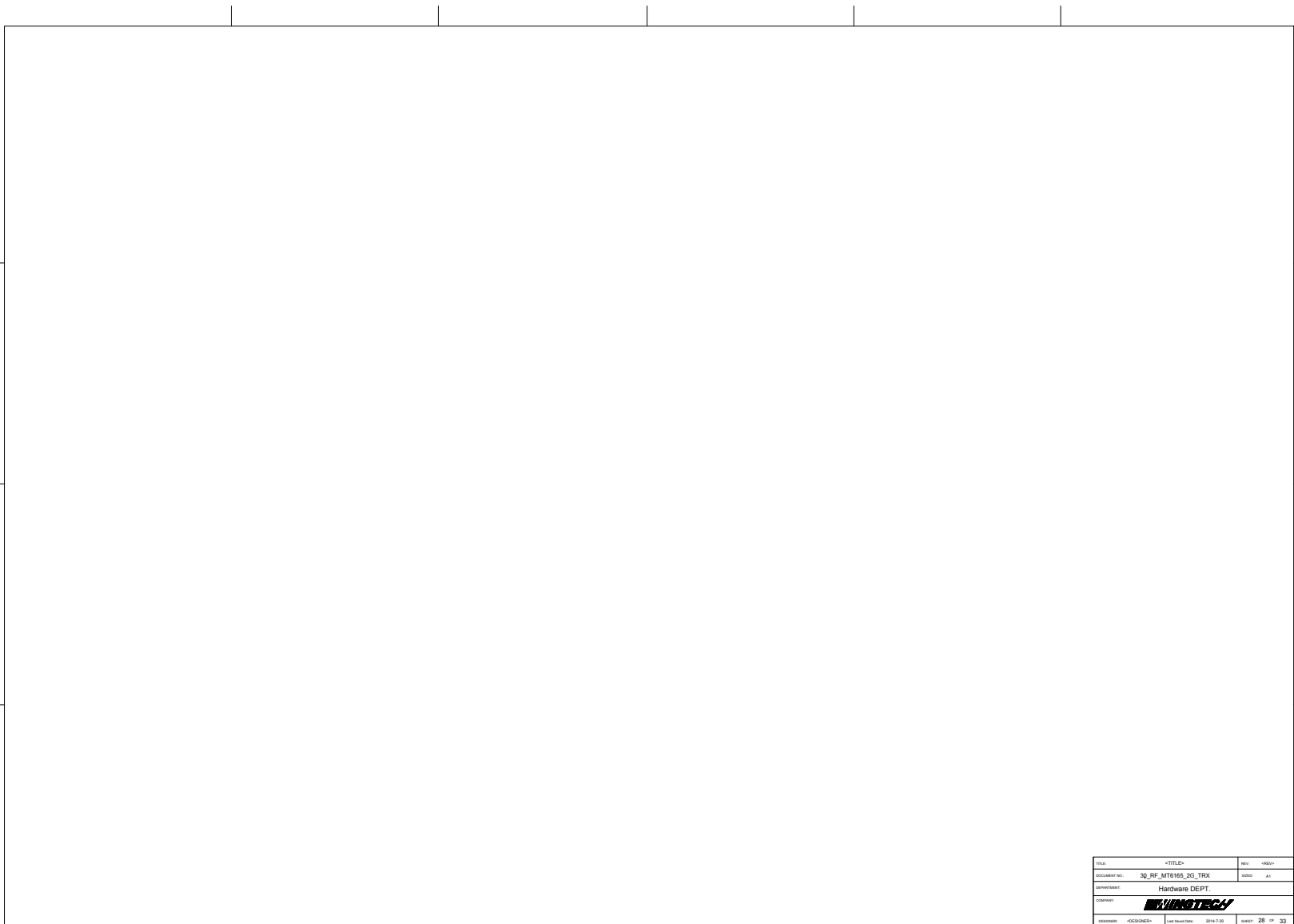


Sheet:	<TITLE>	REV:	<REV>
DocId: 34516705	63_MTE200M_MSDC/SPIN/UART/GP	REV:	A1
Department:	Hardware DEPT.		
Company:			
Drawn:	<DESIGNER>	Lot Release Date:	05/06 7:30
Sheet:	24	of	33

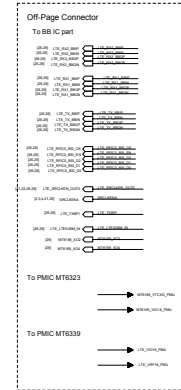
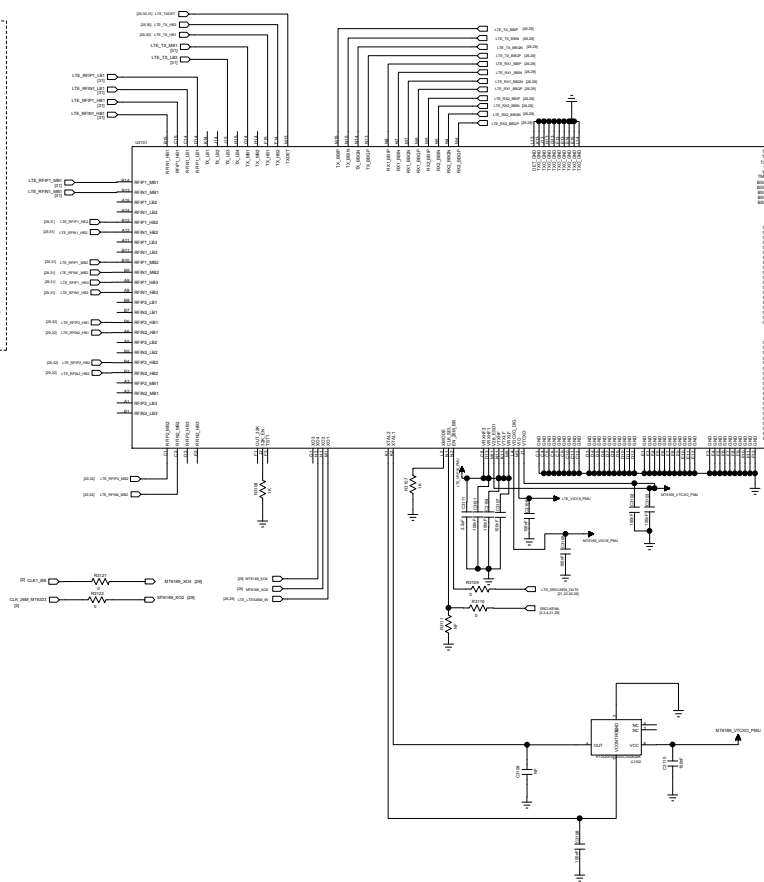
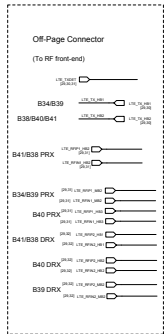




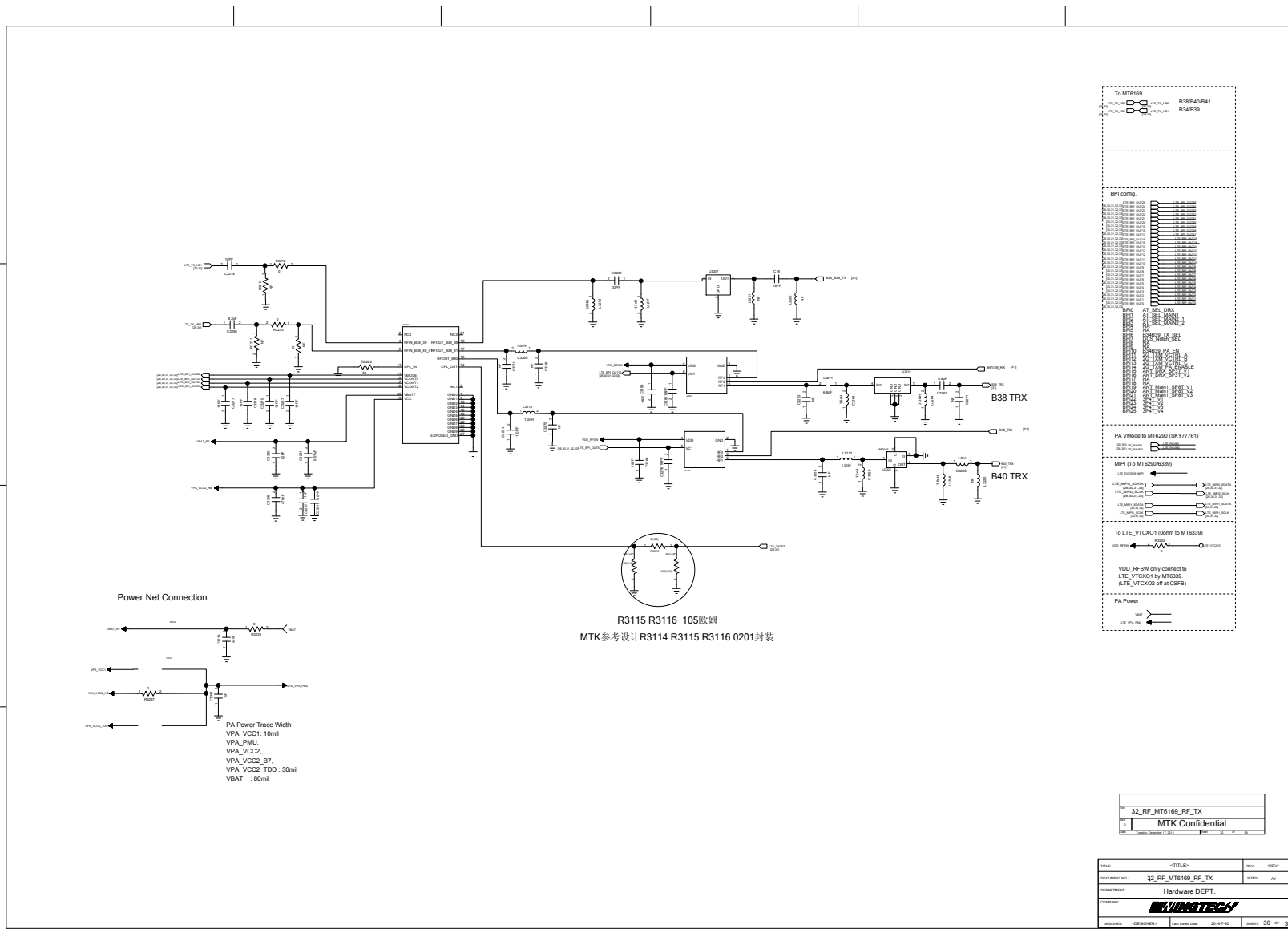




DATE	-TITLE-	REV	-REV-
DOCUMENT NO.	30_RF_MTE165_3D_TRX	REVISED BY	AT
DEPARTMENT	Hardware DEPT.		
DESIGNER			
DRAWING	ASSIGNED	Lotus Model Corp	05/14/7-30
		SHEET	28 OF 33



31 RF MT8169 PIN OUT
Mediatek Confidential



To MTK6169

PA_VCC1 PA_VCC2

BPI config

```

BPI0 PA_VCC1
BPI1 PA_VCC2
BPI2 PA_VCC1
BPI3 PA_VCC2
BPI4 PA_VCC1
BPI5 PA_VCC2
BPI6 PA_VCC1
BPI7 PA_VCC2
BPI8 PA_VCC1
BPI9 PA_VCC2
BPI10 PA_VCC1
BPI11 PA_VCC2
BPI12 PA_VCC1
BPI13 PA_VCC2
BPI14 PA_VCC1
BPI15 PA_VCC2
BPI16 PA_VCC1
BPI17 PA_VCC2
BPI18 PA_VCC1
BPI19 PA_VCC2
BPI20 PA_VCC1
BPI21 PA_VCC2
BPI22 PA_VCC1
BPI23 PA_VCC2
BPI24 PA_VCC1
BPI25 PA_VCC2
BPI26 PA_VCC1
BPI27 PA_VCC2
BPI28 PA_VCC1
BPI29 PA_VCC2
BPI30 PA_VCC1
BPI31 PA_VCC2
BPI32 PA_VCC1
BPI33 PA_VCC2
BPI34 PA_VCC1
BPI35 PA_VCC2
BPI36 PA_VCC1
BPI37 PA_VCC2
BPI38 PA_VCC1
BPI39 PA_VCC2
BPI40 PA_VCC1
BPI41 PA_VCC2
BPI42 PA_VCC1
BPI43 PA_VCC2
BPI44 PA_VCC1
BPI45 PA_VCC2
BPI46 PA_VCC1
BPI47 PA_VCC2
BPI48 PA_VCC1
BPI49 PA_VCC2
BPI50 PA_VCC1
BPI51 PA_VCC2
BPI52 PA_VCC1
BPI53 PA_VCC2
BPI54 PA_VCC1
BPI55 PA_VCC2
BPI56 PA_VCC1
BPI57 PA_VCC2
BPI58 PA_VCC1
BPI59 PA_VCC2
BPI60 PA_VCC1
BPI61 PA_VCC2
BPI62 PA_VCC1
BPI63 PA_VCC2
BPI64 PA_VCC1
BPI65 PA_VCC2
BPI66 PA_VCC1
BPI67 PA_VCC2
BPI68 PA_VCC1
BPI69 PA_VCC2
BPI70 PA_VCC1
BPI71 PA_VCC2
BPI72 PA_VCC1
BPI73 PA_VCC2
BPI74 PA_VCC1
BPI75 PA_VCC2
BPI76 PA_VCC1
BPI77 PA_VCC2
BPI78 PA_VCC1
BPI79 PA_VCC2
BPI80 PA_VCC1
BPI81 PA_VCC2
BPI82 PA_VCC1
BPI83 PA_VCC2
BPI84 PA_VCC1
BPI85 PA_VCC2
BPI86 PA_VCC1
BPI87 PA_VCC2
BPI88 PA_VCC1
BPI89 PA_VCC2
BPI90 PA_VCC1
BPI91 PA_VCC2
BPI92 PA_VCC1
BPI93 PA_VCC2
BPI94 PA_VCC1
BPI95 PA_VCC2
BPI96 PA_VCC1
BPI97 PA_VCC2
BPI98 PA_VCC1
BPI99 PA_VCC2

```

PA Mode to MT6205 (BKV7771)

PA_VCC1 PA_VCC2

MPI (To MT6205B339)

PA_VCC1 PA_VCC2

To LTE_VTCK01 (0A4m to MT6339)

PA_VCC1 PA_VCC2

VDD_RFSW only connect to LTE_VTCK01 by MT6339 (LTE_VTCK01 of 4C15B)

PA_VCC1 PA_VCC2

PA Power

PA_VCC1 PA_VCC2

32	RF_MT6169_RF_TX
1	MTK Confidential

