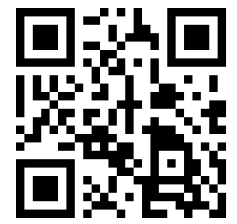
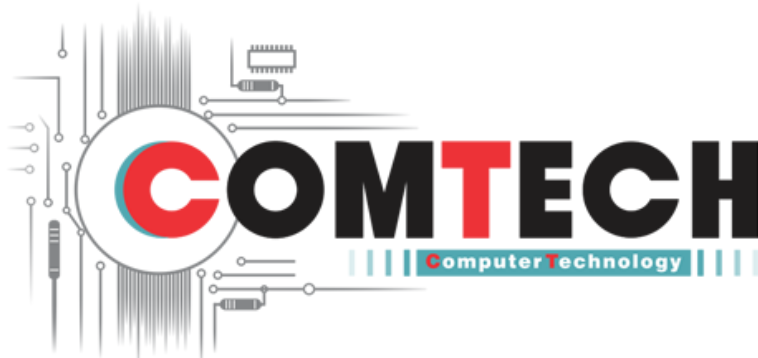
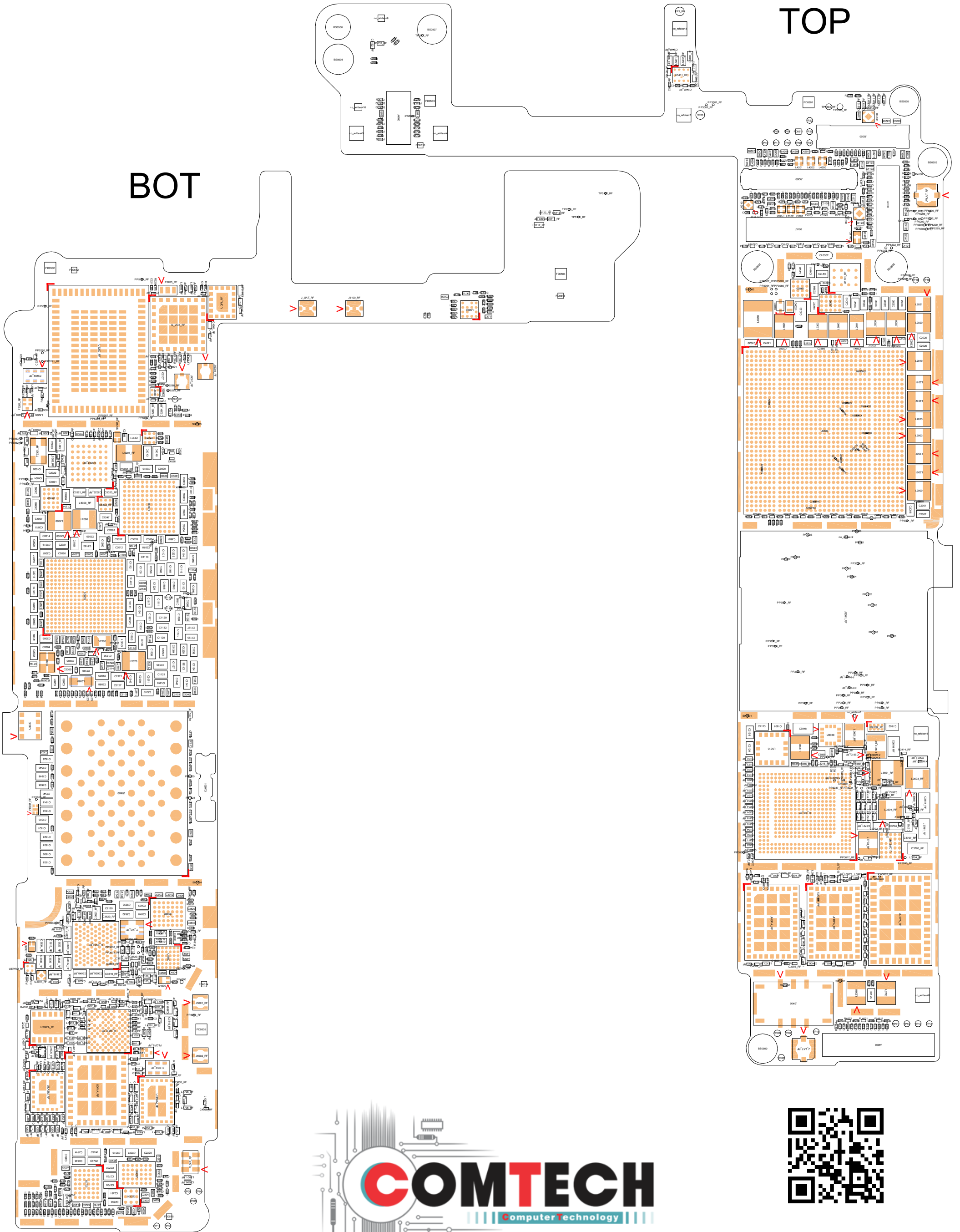


N71位置图



1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	0004536627	PRODUCTION RELEASED		2015-07-21

N71 MLB - PVT OK2FAB

LAST_MODIFICATION= Tue Jul 21 11:39:02 2015


PAGE	<CSA>	CONTENTS	SYNC	DATE
1	1	TABLE OF CONTENTS		
2	3	SYSTEM:BOM TABLES		
3	4	SYSTEM:N71 SPECIFIC		
4	6	SYSTEM:MECHANICAL		
5	7	SOC:JTAG,USB,XTAL		
6	8	SOC:PCIE		
7	9	SOC:CAMERA & DISPLAY		
8	10	SOC:SERIAL & GPIO		
9	11	SOC:OWL		
10	12	SOC:POWER (1/3)		
11	13	SOC:POWER (2/3)		
12	15	SOC:POWER (3/3)		
13	20	NAND		
14	21	SYSTEM POWER:PMU (1/3)		
15	22	SYSTEM POWER:PMU (2/3)		
16	23	SYSTEM POWER:PMU (3/3)		
17	24	SYSTEM POWER:CHARGER		
18	30	SYSTEM POWER:BATTERY CONN		
19	31	SENSORS:MOTION SENSORS		
20	32	CAMERA:FOREHEAD FLEX B2B		
21	33	CAMERA:REAR CAMERA B2B		
22	35	CAMERA:STROBE DRIVER		
23	36	AUDIO:CALTRA CODEC (1/2)		
24	37	AUDIO:CALTRA CODEC (2/2)		
25	38	AUDIO:SPEAKER DRIVER		
26	40	AUDIO:ARC DRIVER		
27	41	DISPLAY:POWER		
28	42	TOUCH:ORB & MESA B2B		
29	45	DISPLAY:KEPLER B2B		
30	46	I/O:TRISTAR 2		

PAGE	<CSA>	CONTENTS	SYNC	DATE
31	47	I/O:DOCK FLEX B2B		
32	49	I/O:BUTTON FLEX B2B		
33		BASEBAND:RADIO SYMBOL		
34		page1		
35		ELNA & UAT ANT FEED		
36		FE: ANT CONNECTORS AND UAT TUNER		
37		WLAN LAT 2.4GHZ BAW BPF		
38		DEBUG CONN & TEST POINTS		
		CELLULAR BASEBAND: POWER1		
39		CELLULAR BASEBAND: POWER2		
40		CELLULAR BASEBAND: CONTROL AND INTERFACES		
41		CELLULAR BASEBAND: GPIOs		
43		CELLULAR PMU: CONTROL AND CLOCKS		
44		CELLULAR PMU: SWITCHERS AND LDOS		
		CELLULAR PMU: ET MODULATOR		
46		CELLULAR TRANSCEIVER: POWER		
47		CELLULAR TRANSCEIVER: PRX PORTS		
48		CELLULAR TRANSCEIVER: DRX/GPS PORTS		
49		CELLULAR TRANSCEIVER: TX PORTS		
50		CELLULAR FRONT END: LB PAD		
51		CELLULAR FRONT END: MB PAD		
52		CELLULAR FRONT END: HB PAD		
53		CELLULAR FRONT END: 2G PA		
54		CELLULAR FRONT END: LB ASM		
55		CELLULAR FRONT END: MB-HB ASM		
56		CELLULAR FRONT END: DIVERSITY		
57		SIM		
58		WIFI/BT: WIFI/BT MODULE		
59		STOCKHOLM		

TABLE

- SCH 051-1902
- BRD 820-5507
- MCO 056-01060
- BOM 639-00263 (BETTER, DB30)
- BOM 639-00265 (ULTRA, DB30)
- BOM 639-00266 (SUPREME, DB30)
- BOM 639-01056 (BETTER, B30)
- BOM 639-01057 (ULTRA, B30)
- BOM 639-01058 (SUPREME, B30)
- BOM 639-01098 (BETTER, DB30C)
- BOM 639-01100 (ULTRA, DB30C)
- BOM 639-01099 (SUPREME, DB30C)
- BOM 939-01627 (BETTER, DARWIN)

TABLE OF CONTENTS

DRAWING TITLE		SCHEM, SINGLE, BRD, N71	
 Apple Inc.	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		PAGE	1 OF 49
		SHEET	1 OF 59



SCHEMATIC & PCB BOM CALLOUTS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Rows include SCH_SINGLE_BRD_N71, PCBF_SINGLE_BRD_N71, and various EEEE CODE FOR 639-00263, 639-00265, 639-01057, 639-01058, 639-01098, 639-01100, 639-01099, 939-01627.

S3E NAND BOM OPTIONS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), CRITICAL, BOM OPTION. Rows include NAND_1YNM_16GBX_838_64G_T_SLGA70, NAND_1YNM_64GBX_838_MLB_64G_H_SLGA70, NAND_1YNM_128GBX_838_TLC_128G_H_SLGA70.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Rows include 335S00074, 335S00078, 335S00064, 335S00065.

CARBON/ACCEL BOM OPTIONS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Rows include IC_ACCEL_3-AXIS_DIG_BMA282_LGA14, RES_MF_20 OHM_5K_1/32W_01005, CAP_CER_XSR_2.2UF_204_6.3V_0201, IC_CARBON_MPU_6700-12_LGA16.

ALTERNATE BOM OPTIONS

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Rows include 138S00032, 138S00049, 155S00660, 138S00005, 138S00048, 138S0702, 118S0764, 138S00006, 152S2052, 155S0773, 377S0168, 155S00067, 155S00012, 138S0706, 138S0945, 155S00095, 138S0648, 132S0400, 155S0960, 138S00024, 335S00066, 155S00653.

NOT ALL REFERENCE DESIGNATORS LISTED. USED ~116 TIMES IN DESIGN. USED ~116 TIMES IN DESIGN. USED ~7 TIMES IN DESIGN. USED ~63 TIMES IN DESIGN. USED ~63 TIMES IN DESIGN. USED ~3 TIMES IN DESIGN. USED ~19 TIMES IN DESIGN. USED ~61 TIMES IN DESIGN. USED ~9 TIMES IN DESIGN. USED ~8 TIMES IN DESIGN. USED ~11 TIMES IN DESIGN. USED ~17 TIMES IN DESIGN. USED ~17 TIMES IN DESIGN. USED ~12 TIMES IN DESIGN. USED ~2 TIMES IN DESIGN. USED ~9 TIMES IN DESIGN. USED ~7 TIMES IN DESIGN. USED ~4 TIMES IN DESIGN.

POWER INDUCTOR ALTERNATES

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Rows include 152S00120, 152S00118.

ACTIVE DIODE ALTERNATE

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Row includes 376S00106.

SHIELD PART NUMBERS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Rows include SHIELD_EMI_UPPER_FRONT_WTOP_N71, SHIELD_EMI_LOWER_FRONT_CLOSED_NOMI_N71, SHIELD_EMI_SA_OPEN_N71, SHIELD_EMI_UPPER_BACK_WTOP_N71, SHIELD_EMI_LOWER_BACK_WTOP_N71.

SOC/PMU SUB BOMS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Rows include SUBBOM_SINGLE_BRD_MAU1_N71, IC_PMU_ARMADA_A0_G255A1_07P-AL_WGCEP380, RES_MF_100 OHM_1K_1/32W_01005, CAP_CER_NPO/COG_100PF_5%_16V_01005, PROD_FUSED_H_DRAM, RES_MF_0 OHM_1/32W_01005.

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Rows include IC_PMU_ARMADA_G255A1_07P-AL_WGCEP380, RES_MF_3.0KOHM_1K_1/32W_01005, CAP_CER_NPO/COG_100PF_5%_16V_01005, M DEV FUSED_M_DRAM, RES_MF_330 OHM_1K_1/32W_01005.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Row includes 685-00070.

SOC ALTERNATES

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Rows include 339S00113, 339S00114.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Rows include 339S00125, 339S00126, 339S00127, 339S00128, 339S00129.

INDUCTOR SUB BOMS

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Rows include SUBBOM_SINGLE_BRD_CYNTEC_N71, IND_PWR_SHLD_1.0OH_3.6A_0.060 OHM_2014, IND_PWR_SHLD_1.0OH_3.6A_0.060 OHM_2016, IND_PWR_SHLD_0.47OH_3.6A_0.048 OHM_2012.

Table with 5 columns: PART#, QTY, DESCRIPTION, REFERENCE DESIGNATOR(S), BOM OPTION. Rows include IND_PWR_SHLD_1.0OH_3.6A_0.060 OHM_2014, IND_PWR_SHLD_1.0OH_3.6A_0.060 OHM_2016, IND_PWR_SHLD_0.47OH_3.6A_0.048 OHM_2012.

Table with 5 columns: PART NUMBER, ALTERNATE FOR PART NUMBER, BOM OPTION, REF DES, COMMENTS. Row includes 685-00080.

D

D

C

C

B

B

A

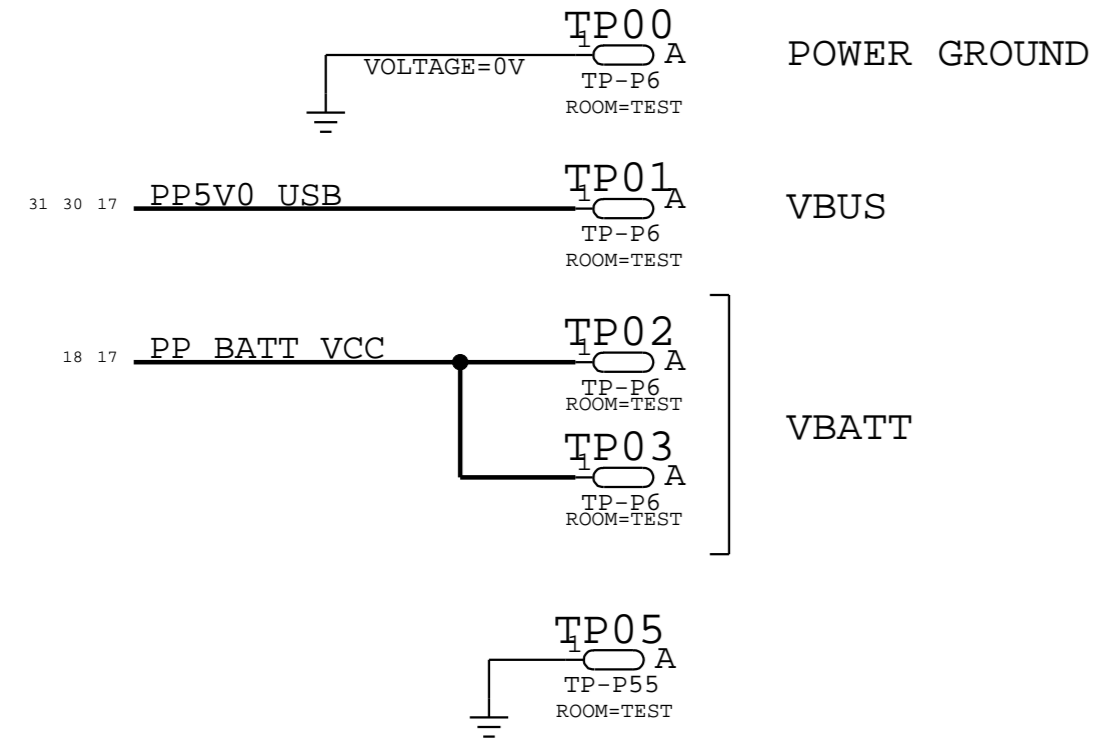
A



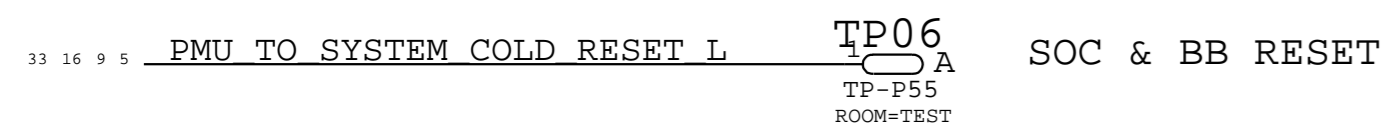
Page title: SYSTEM:BOM TABLES. Apple Inc. logo. Drawing number: 051-1902. Revision: A.0.0. Page: 3 OF 49. Sheet: 2 OF 59. NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED

TESTPOINTS

POWER



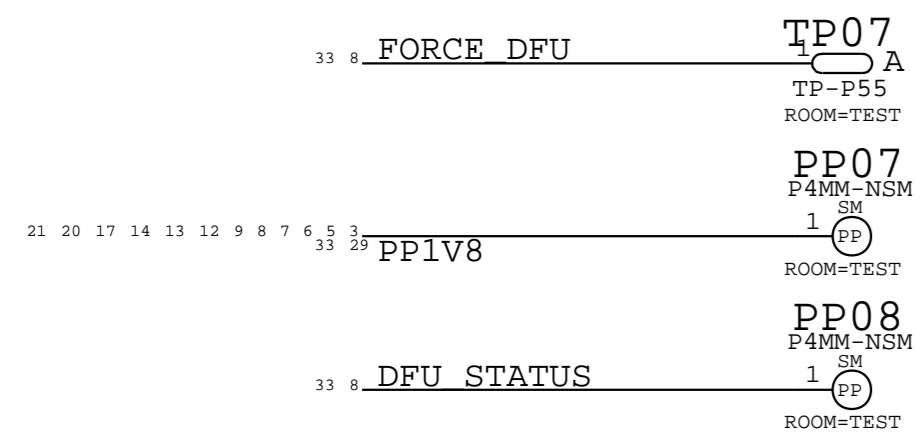
RESET



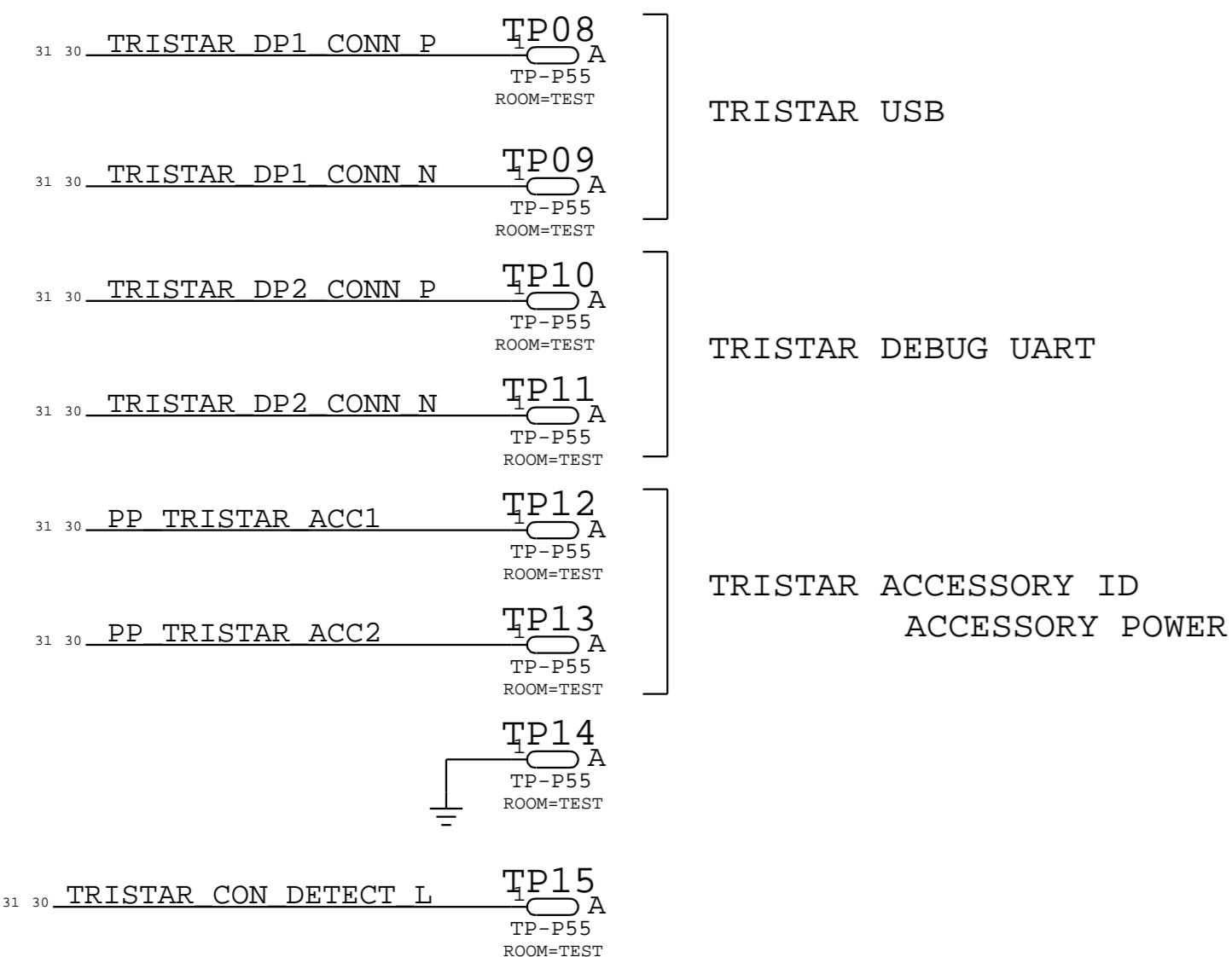
DFU

FORCE DFU PROCEDURE:

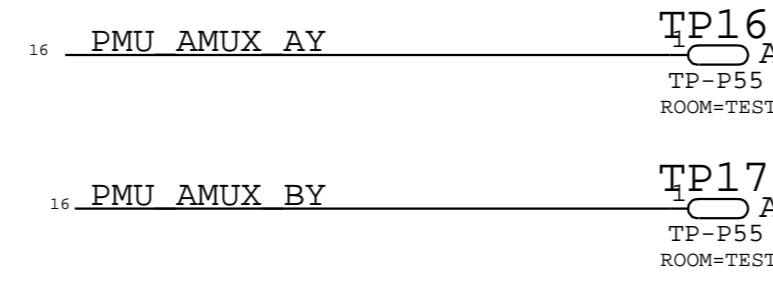
1. FROM OFF MODE SHORT TP07 TO PP07
2. PLUG IN E75 CABLE TO FORCE DFU



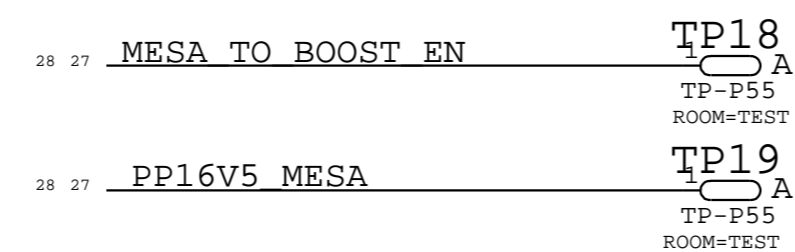
E75



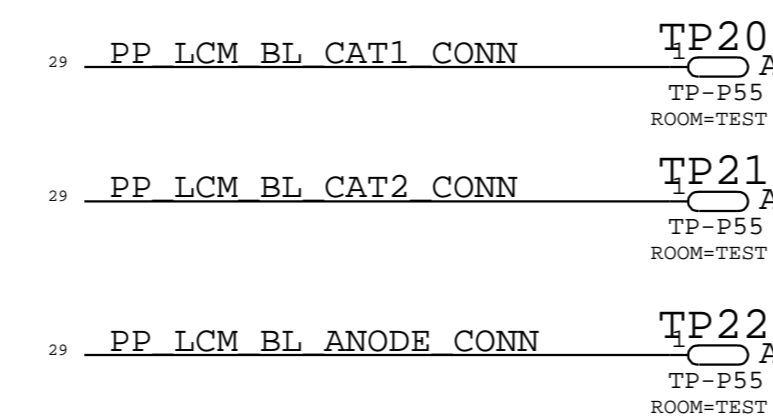
AMUX



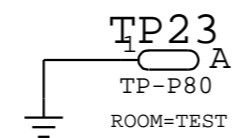
MOJAVE



LCM



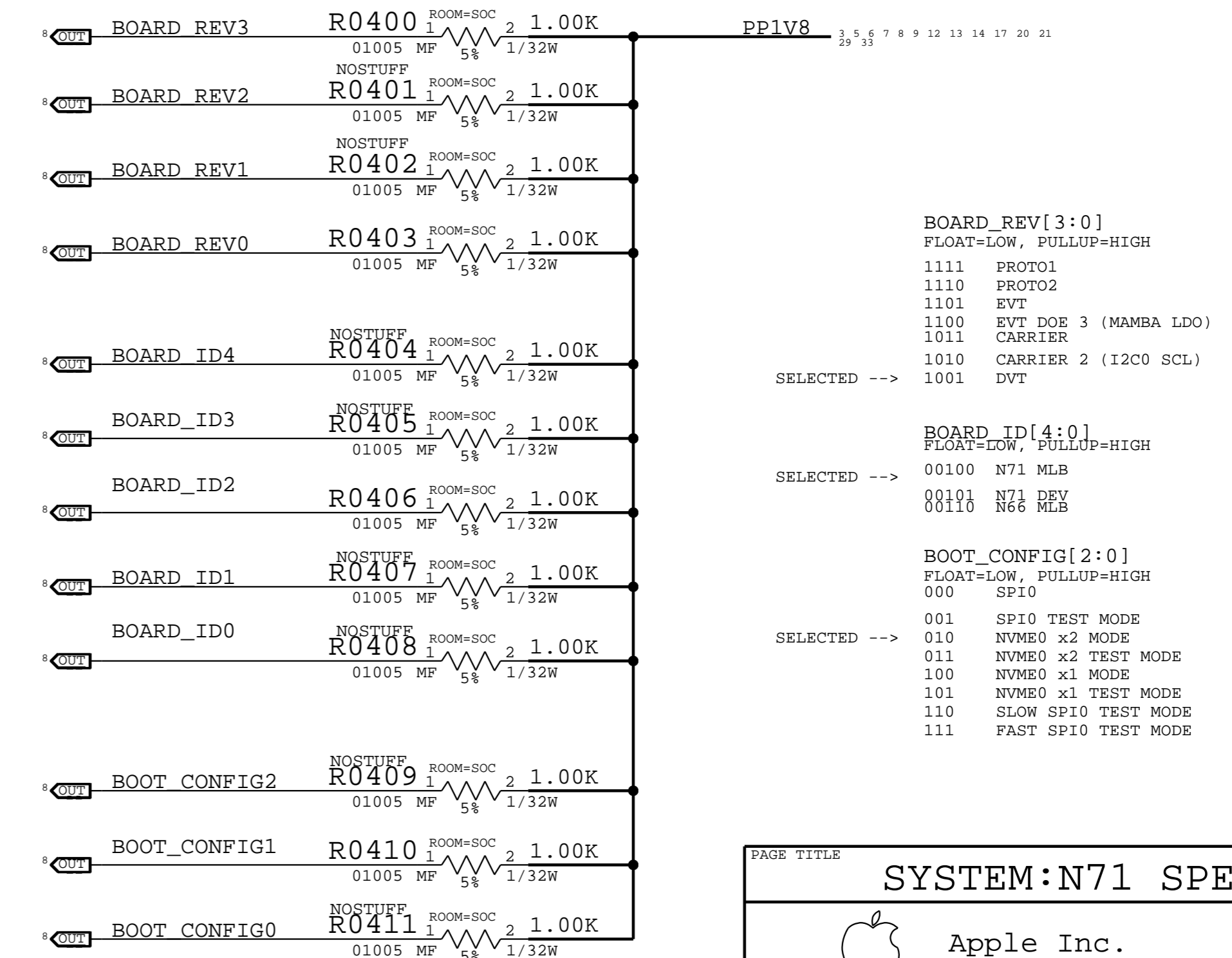
SUPER SCREW



N71 I2C DEVICE MAP

I2C BUS	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
I2C0	ANTIGUA PMU	1110100X	0X74	0XE8
	CHESTNUT	0100111X	0X27	0X4E
	MUON	1100010X	0X62	0XC4
I2C1	TIGRIS	1110101X	0X75	0XE9
	ARC DRIVER	1000001X	0X41	0X82
	SPEAKER AMP	1000000X	0X40	0X80
	TRISTAR	0011010X	0X1A	0X34
I2C2	ALS	0101001X	0X29	0X52
	DISP EEPROM	1010001X	0X51	0XA2
OWL	UNUSED	N/A	N/A	N/A
ISP I2C0	REAR CAM	TBD	TBD	TBD
	LED DRIVER	1100011X	0X63	0XC6
ISP I2C1	FRONT CAM	0010000X	0X10	0X20
TOUCH I2C	MESON	1000000X	0x40	0x80
	MAMBA	1100000X	0x60	0xC0
	DOPPLER	1011000X	0x58	0xB0
SEP I2C	SEP EEPROM	1010001X	0x51	0xA2

BOOTSTRAPPING: BOARD REV BOARD ID BOOT CONFIG

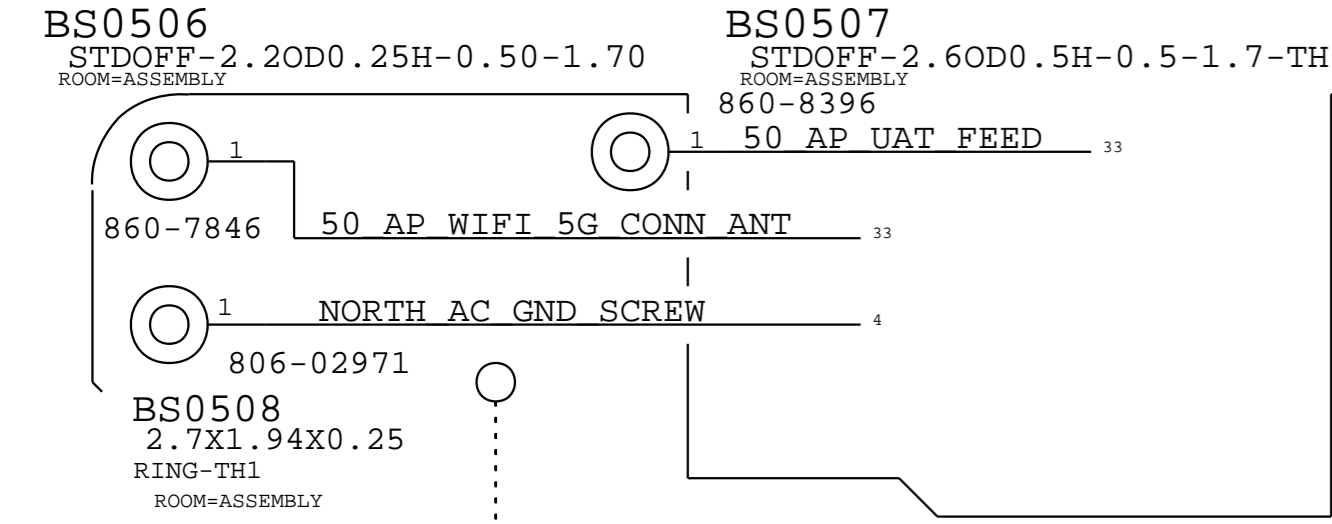


RESISTOR STUFF = HIGH '1'
RESISTOR NOSTUFF = LOW '0'

PAGE TITLE		DRAWING NUMBER	SIZE
SYSTEM:N71 SPECIFIC		051-1902	D
Apple Inc.		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
		PAGE	4 OF 49
		SHEET	3 OF 59

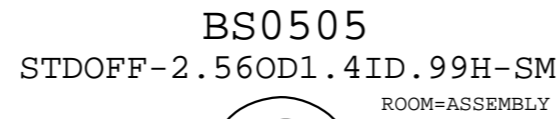


PENINSULA STANDOFFS



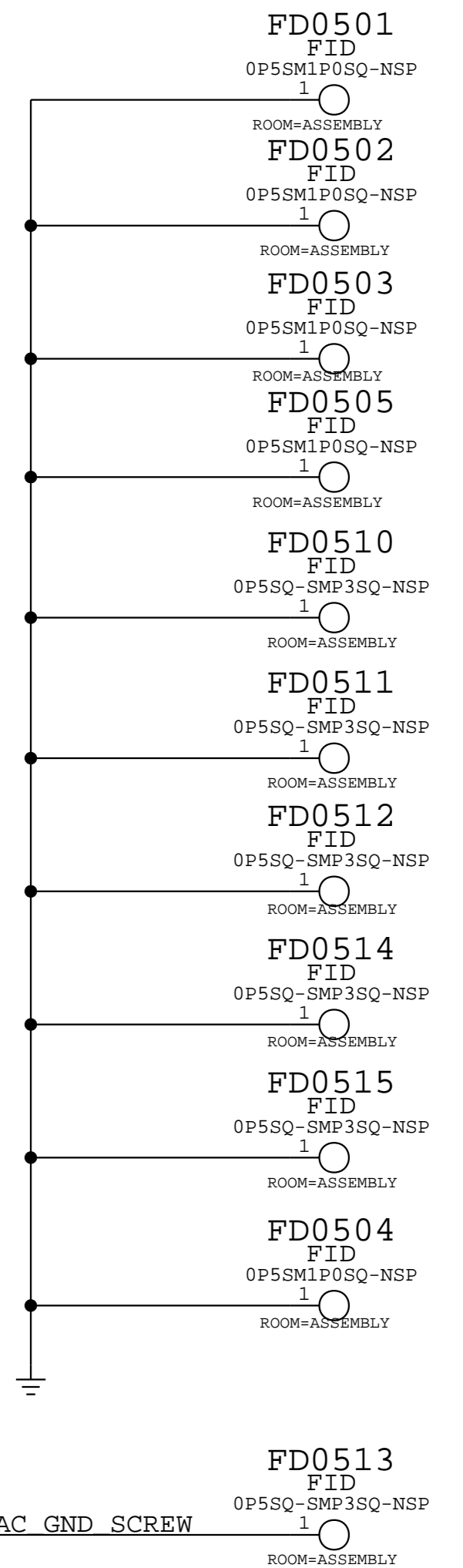
TOP-SIDE

STOCKHOLM FEED

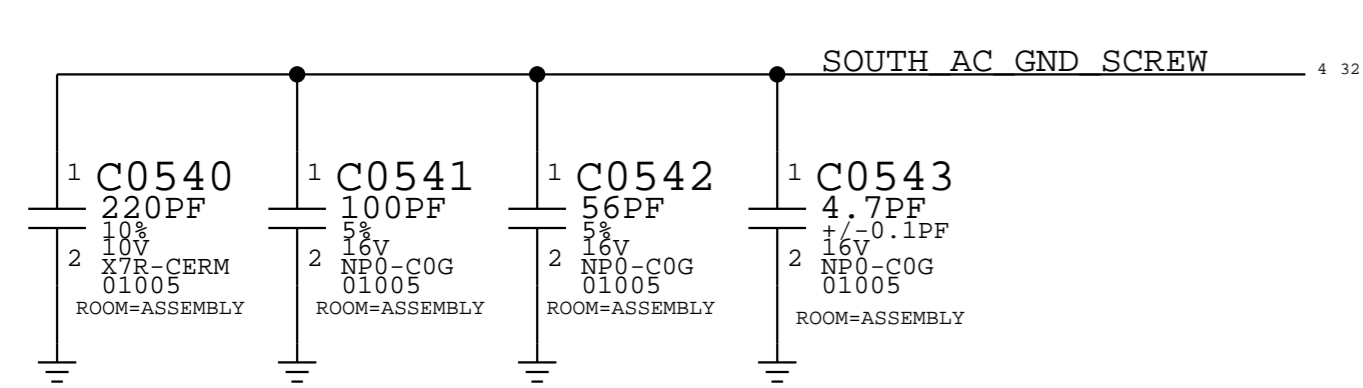


BOTTOM-SIDE

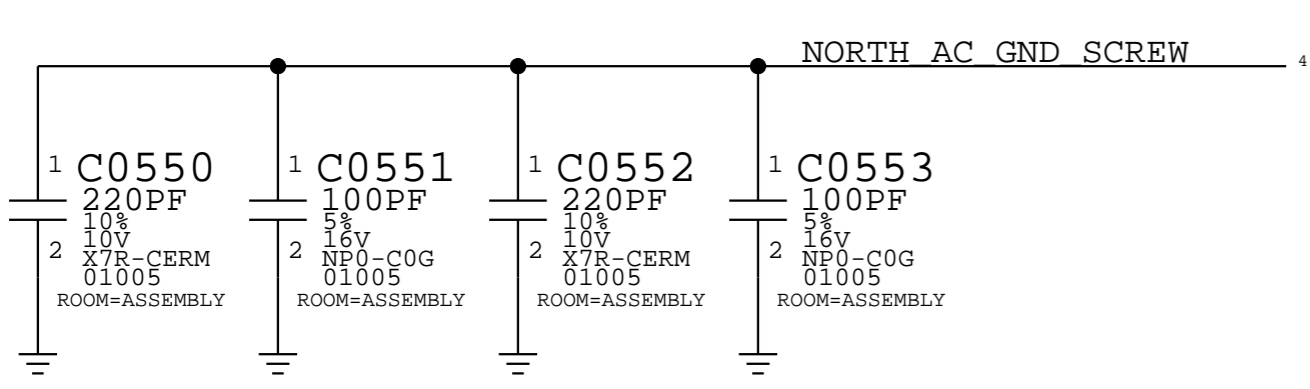
FIDUCIALS



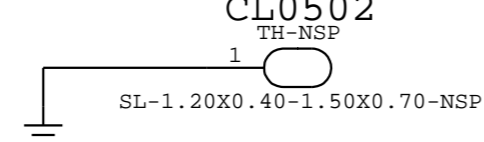
SOUTH DC CURRENT BLOCKING CAPS



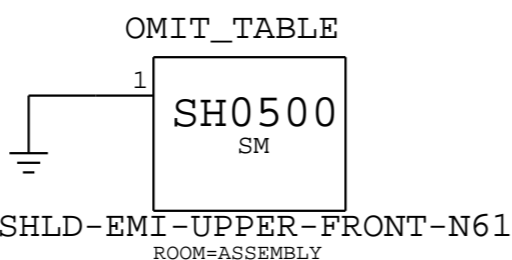
NORTH DC CURRENT BLOCKING CAPS



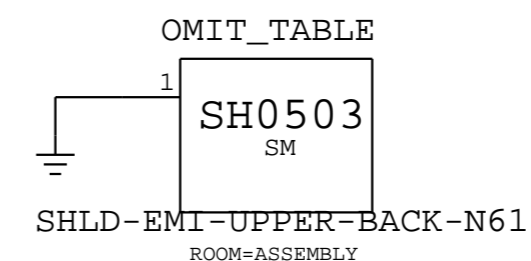
PLATED SLOTTED THRU-HOLE



UPPER SHIELD

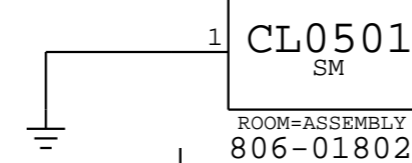


UPPER SHIELD

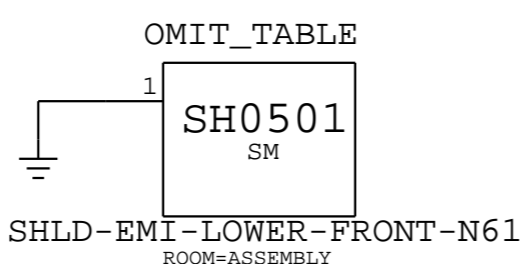


DUAL RF COAX CLIP

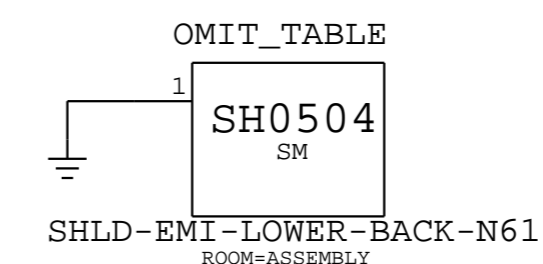
CLIP-RETENTION-COAX-DOUBLE



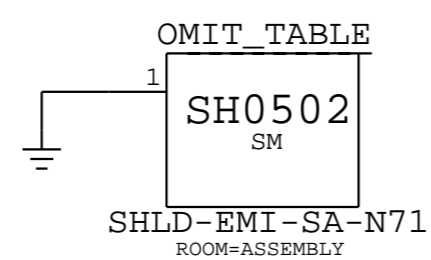
LOWER SHIELD



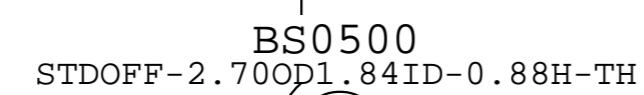
LOWER SHIELD



SA SHIELD



SOUTH TUBE STANDOFF

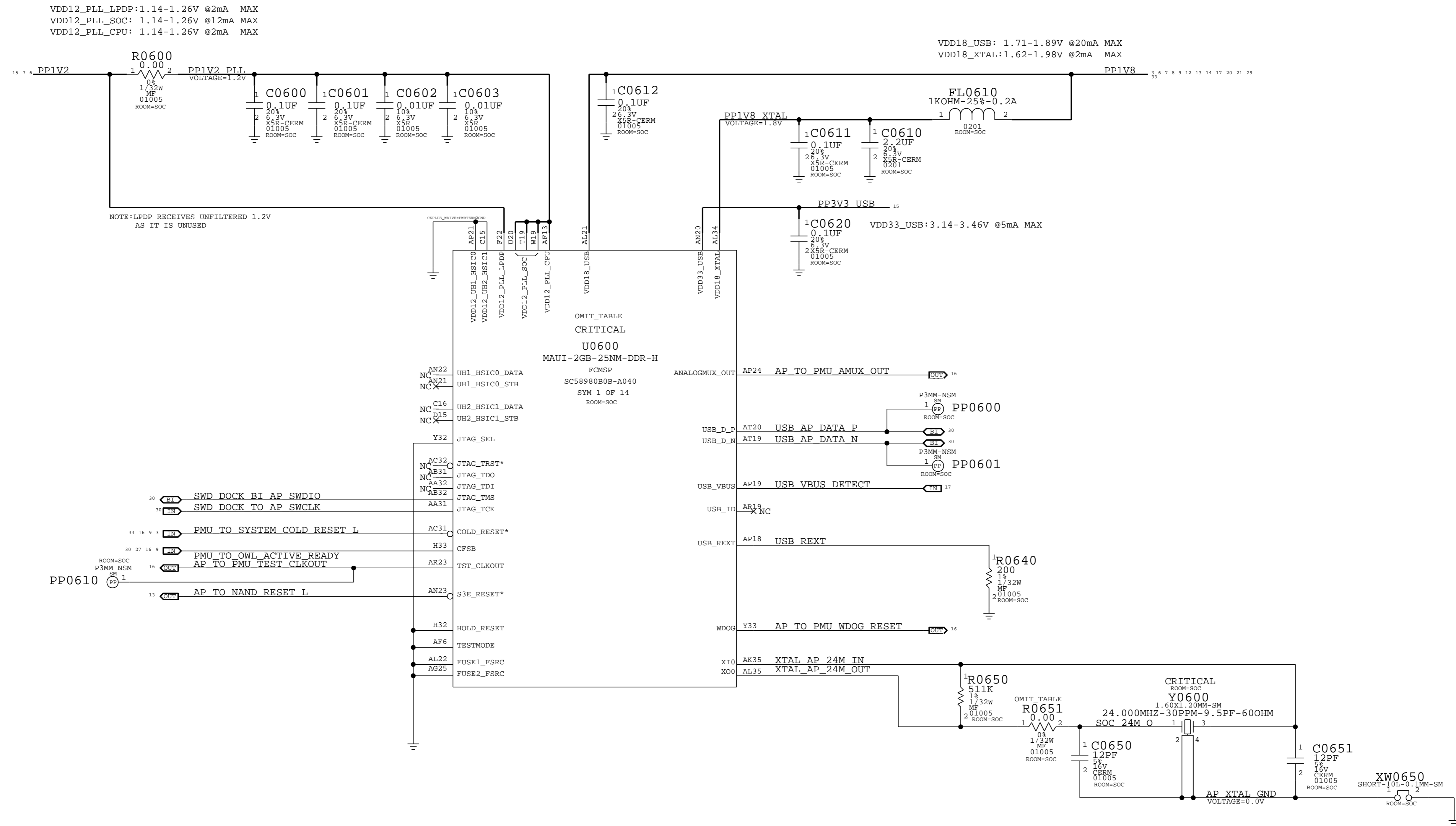


TODO:UPDATE REF DES



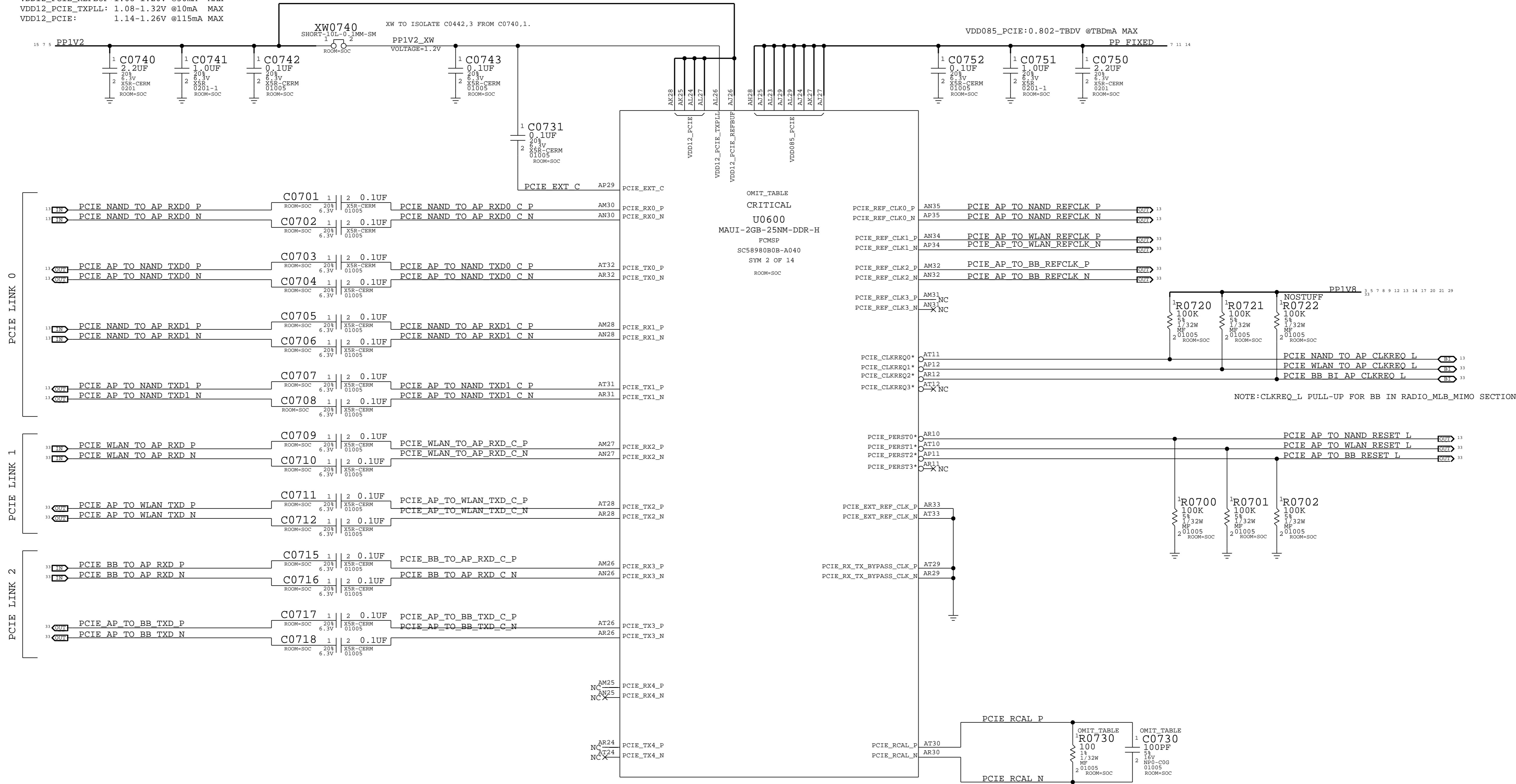
PAGE TITLE		
SYSTEM:MECHANICAL		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED	BRANCH	
	PAGE	5 OF 49
	SHEET	4 OF 59

MAUI - USB, JTAG, XTAL



MAUI - PCIE INTERFACES

VDD12_PCIE_REFBUF:1.08-1.26V @50mA MAX
 VDD12_PCIE_TXPLL: 1.08-1.32V @10mA MAX
 VDD12_PCIE: 1.14-1.26V @115mA MAX



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SOC:PCIE			
	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
NOTICE OF PROPRIETARY PROPERTY:			
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:			
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE			
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			
PAGE	7 OF 49		6 OF 59
SHEET			

MAUI - CAMERA & DISPLAY INTERFACES

8 7 6 5 4 3 2 1

D

D

C

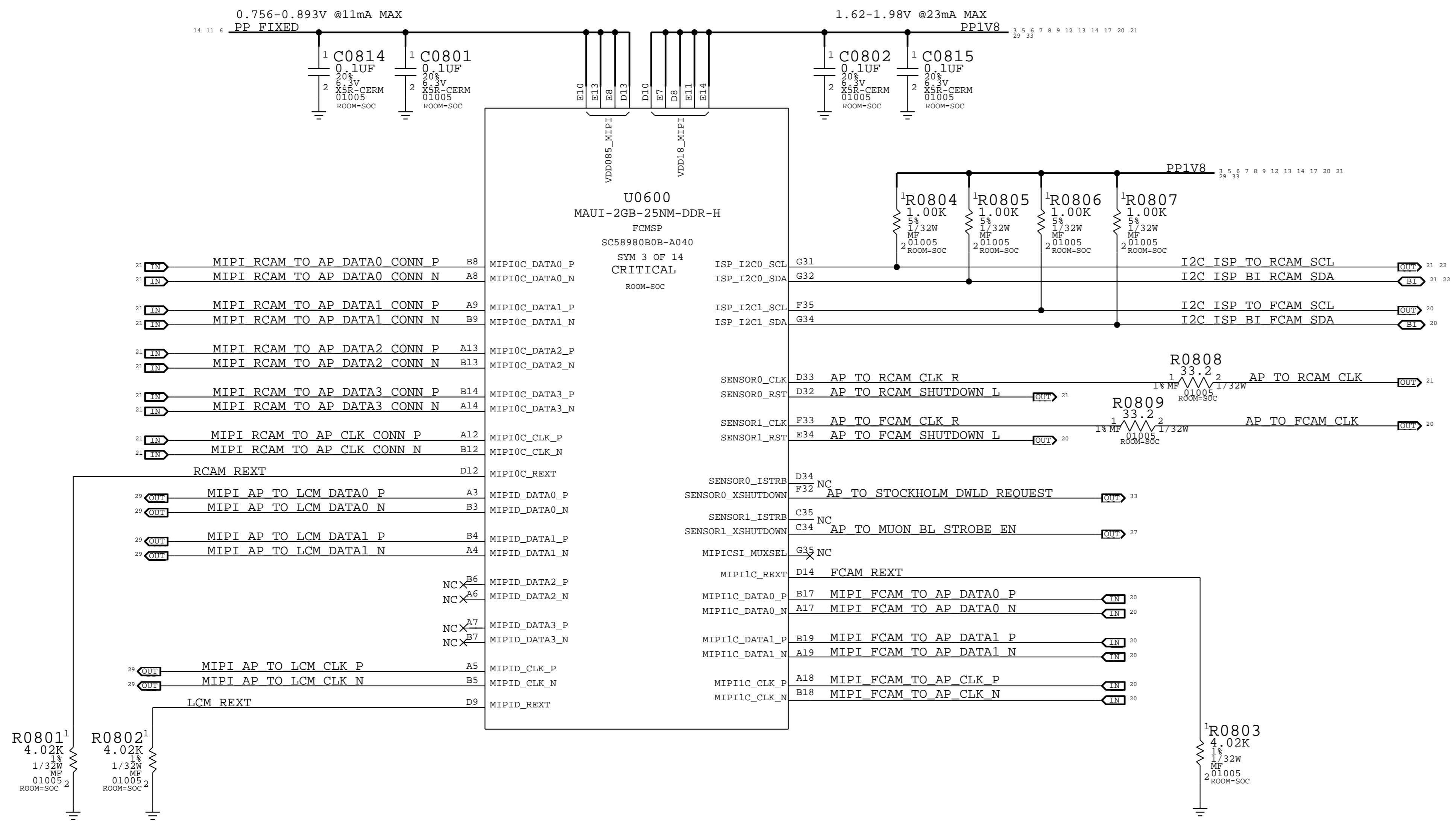
C

B

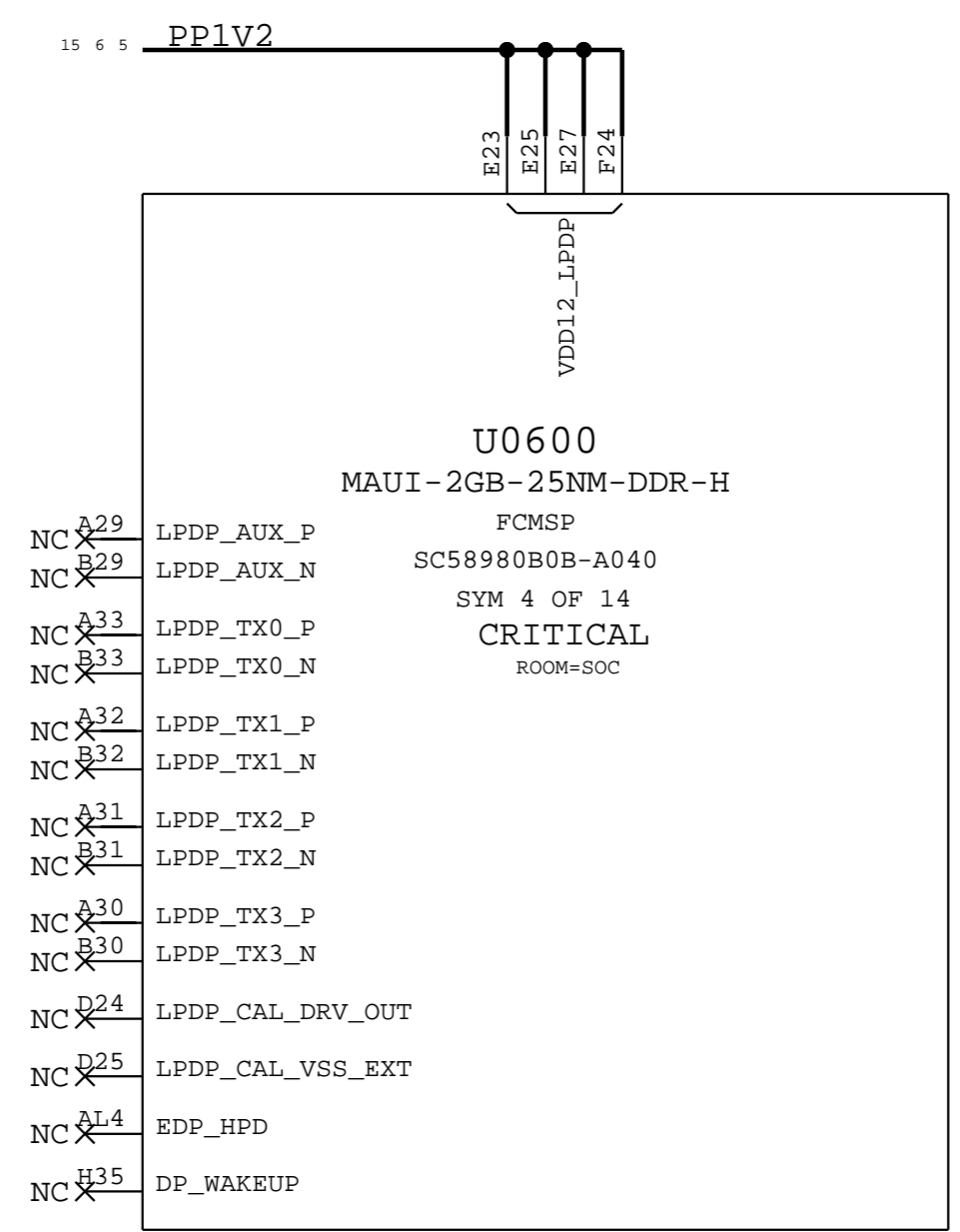
B

A

A



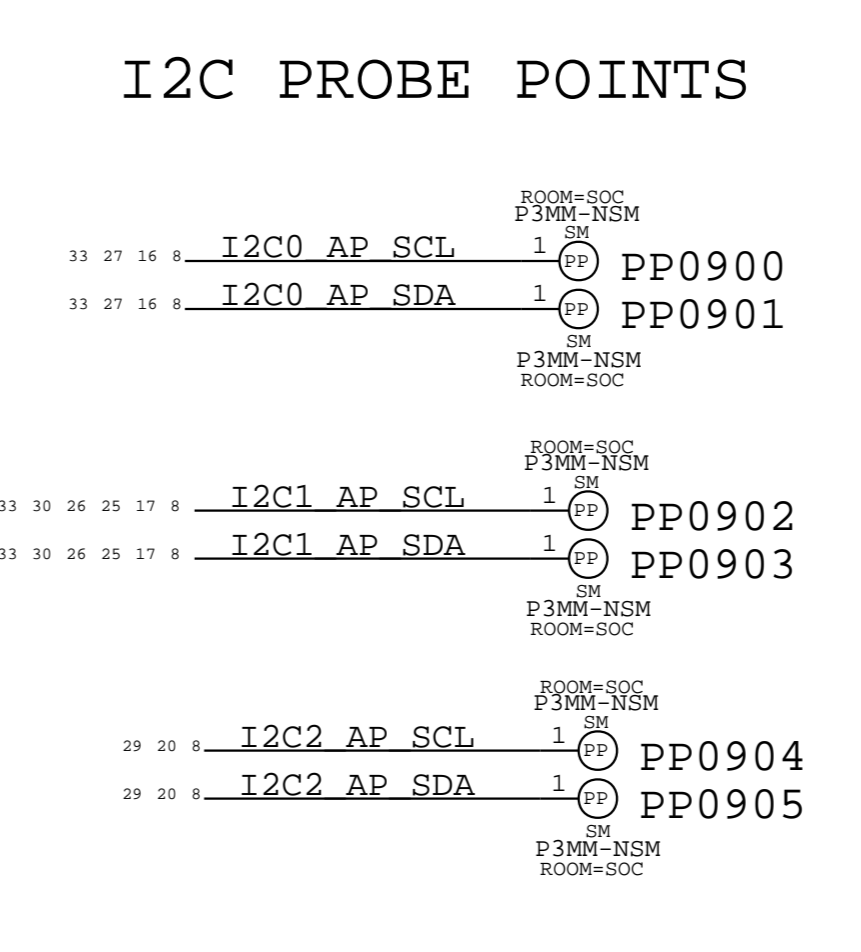
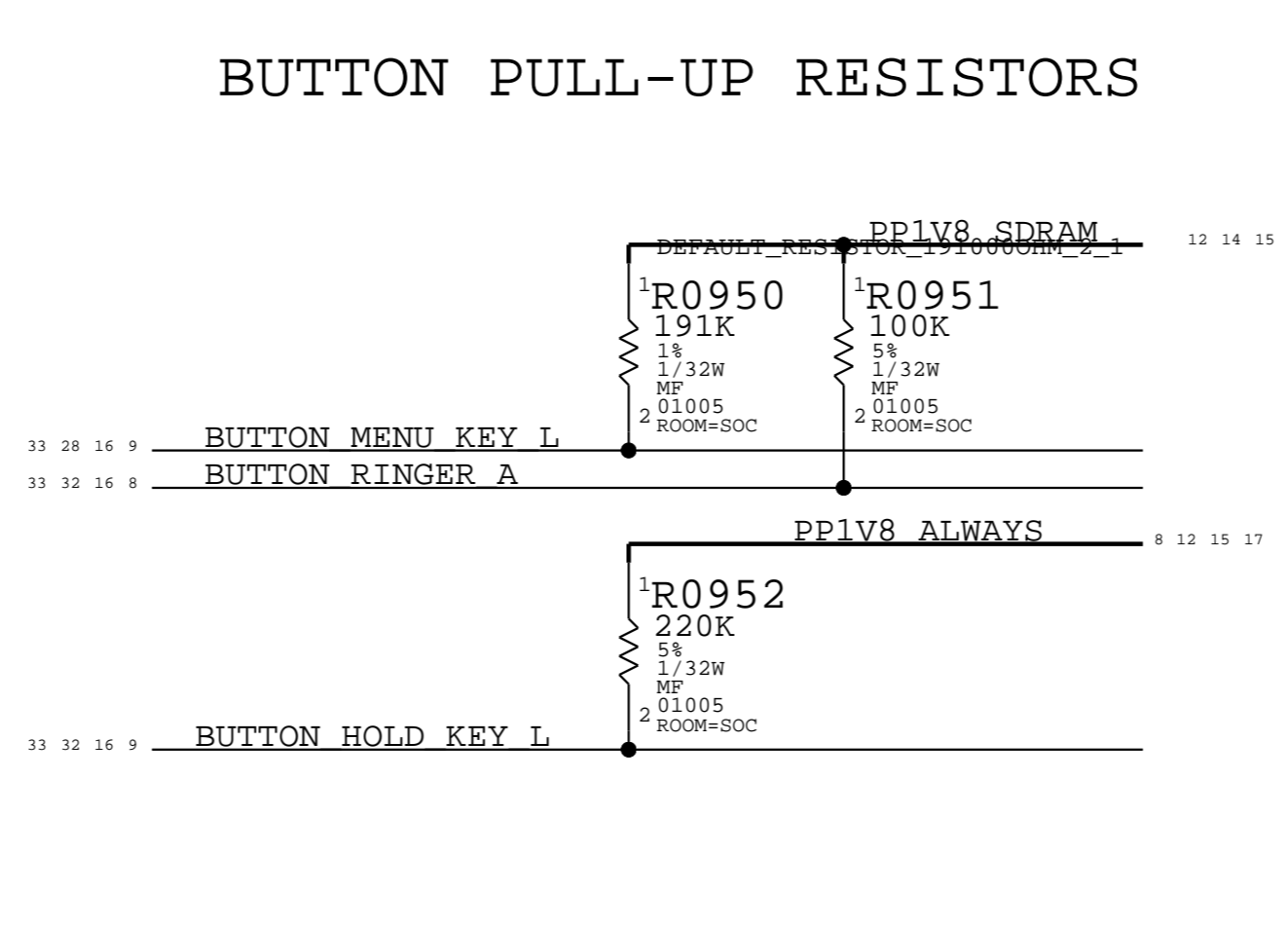
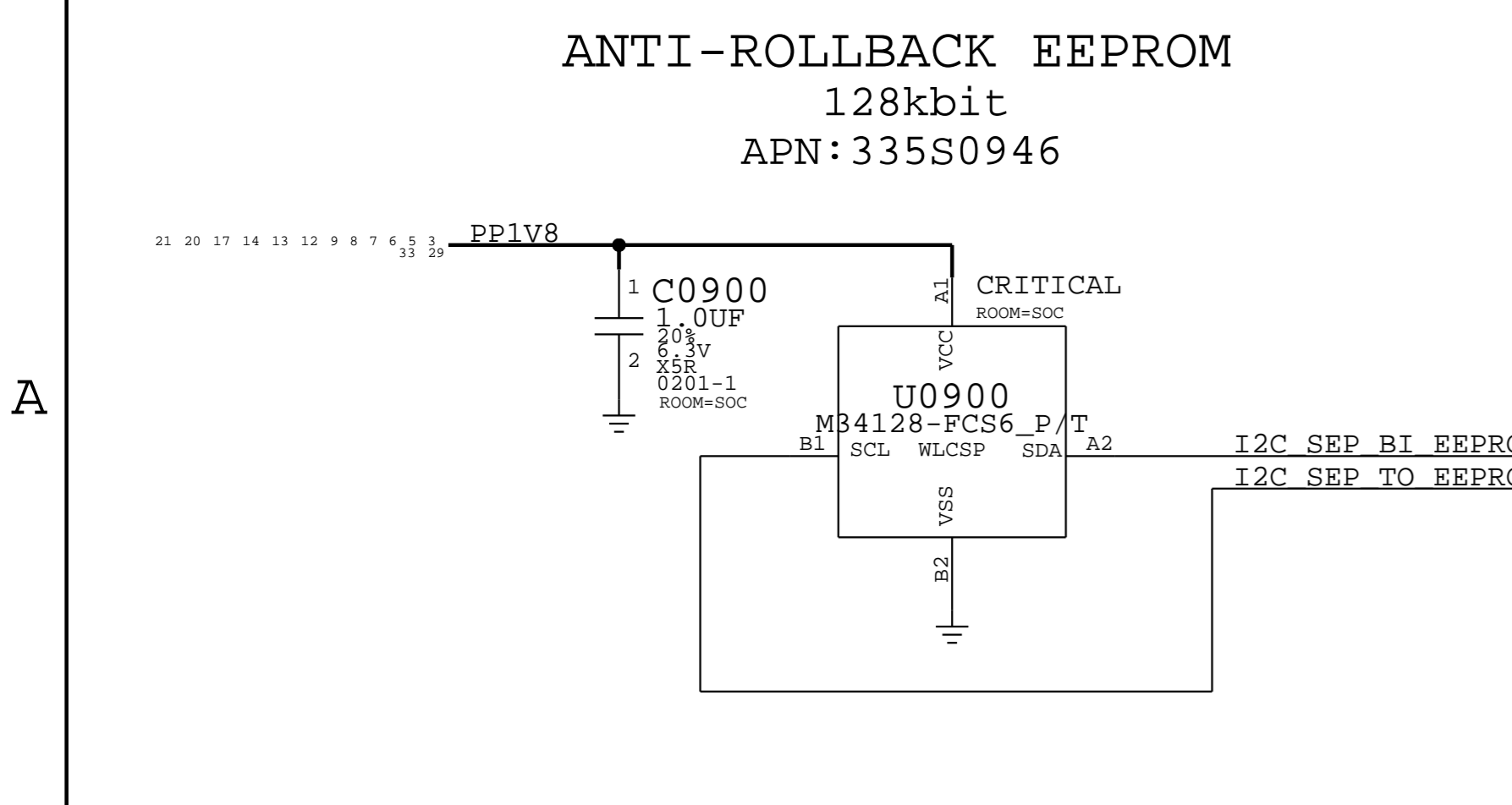
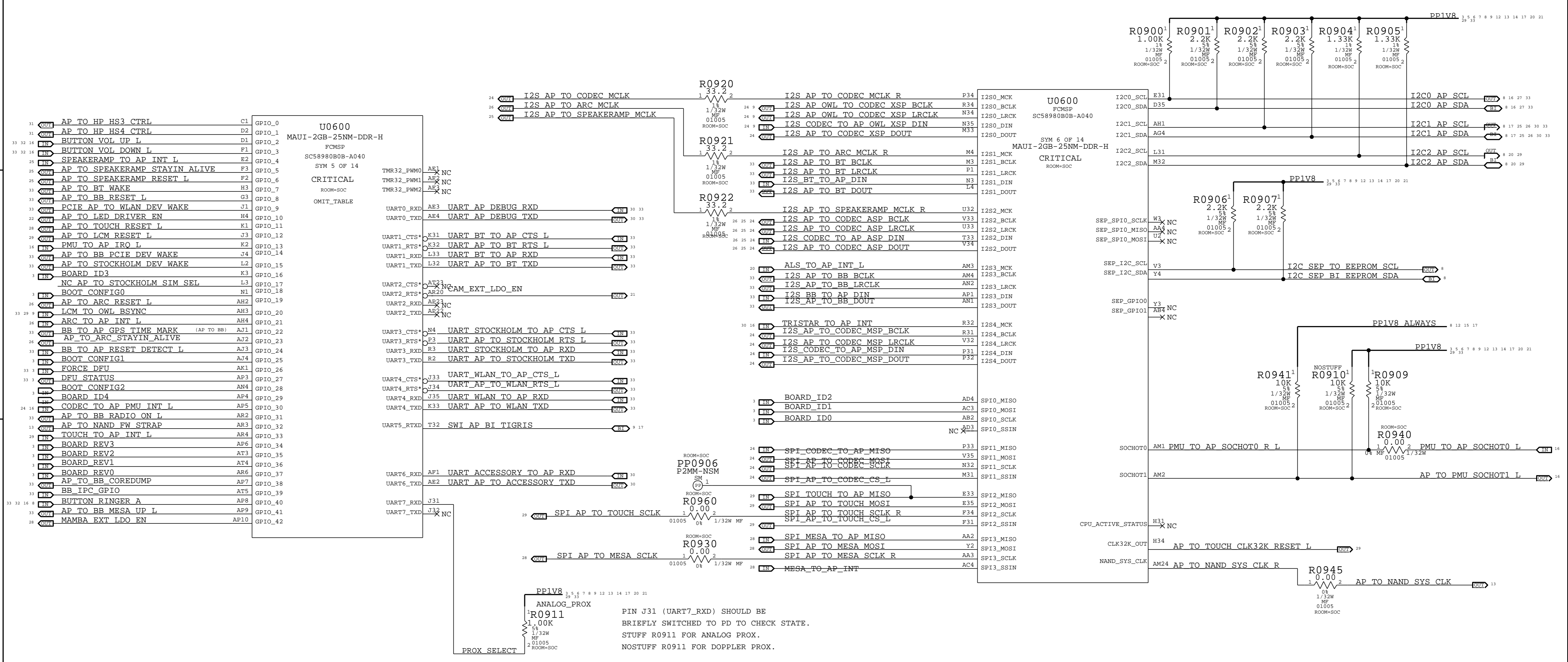
NOTE: VDDI2_LPDP SHOULD BE POWERED
EVEN WHEN LPDP IS NOT USED



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SOC: CAMERA & DISPLAY			
DRAWING NUMBER		SIZE	
Apple Inc.		D	
REVISION		A.0.0	
NOTICE OF PROPRIETARY PROPERTY:			
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:			
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE			
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			
BRANCH		PAGE	
		8 OF 49	
SHEET		7 OF 59	

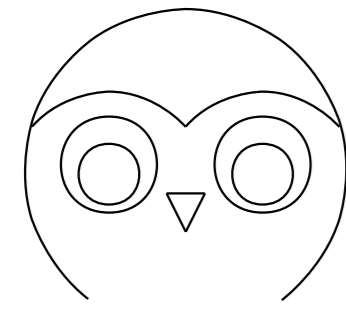
8 7 6 5 4 3 2 1

MAUI - GPIO & SERIAL INTERFACES

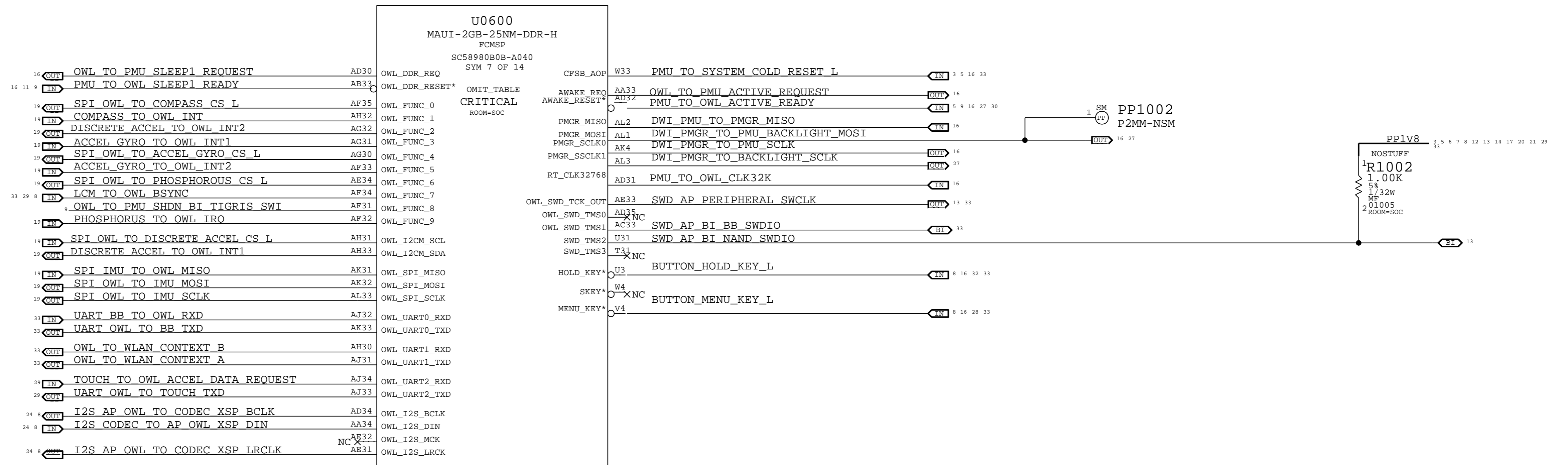
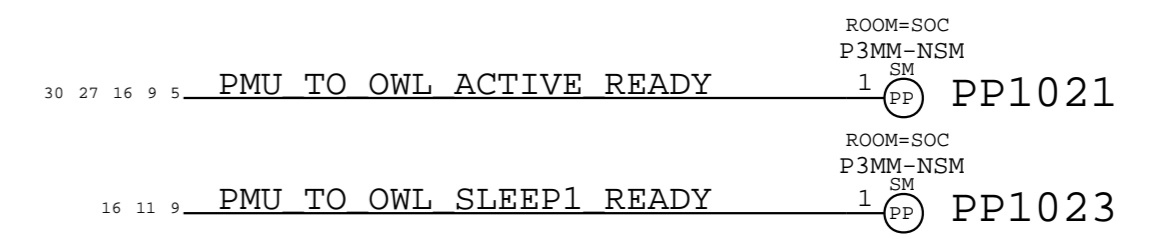


SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SOC: SERIAL & GPIO			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
		BRANCH	
		PAGE	9 OF 49
		SHEET	8 OF 59
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

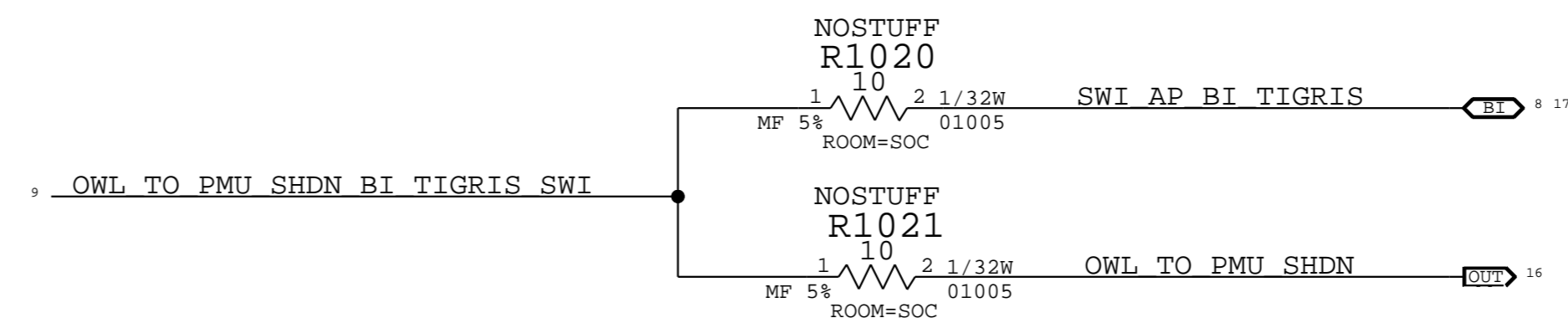
MAUI - OWL



POWER STATE CONTROL PROBE POINTS

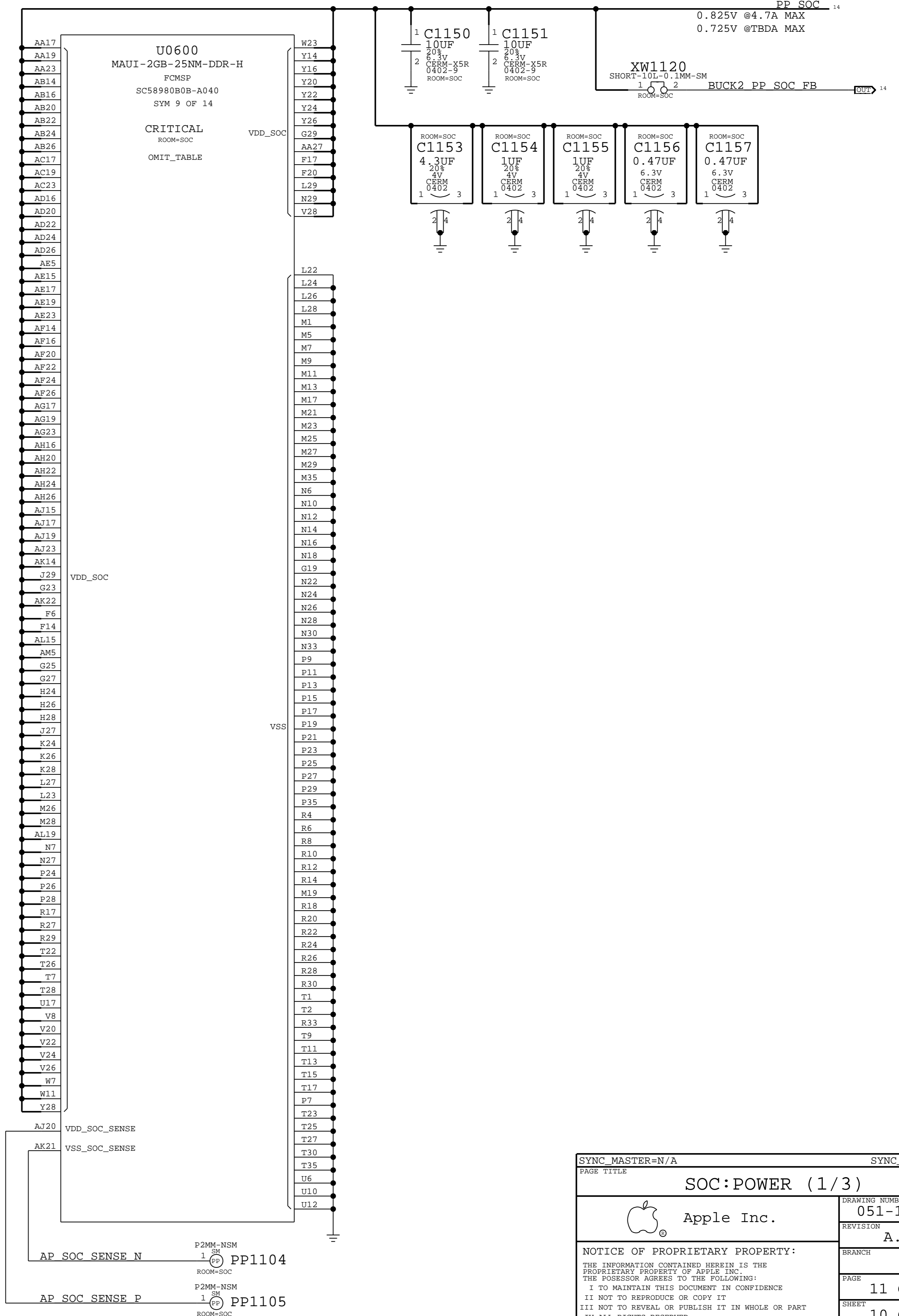
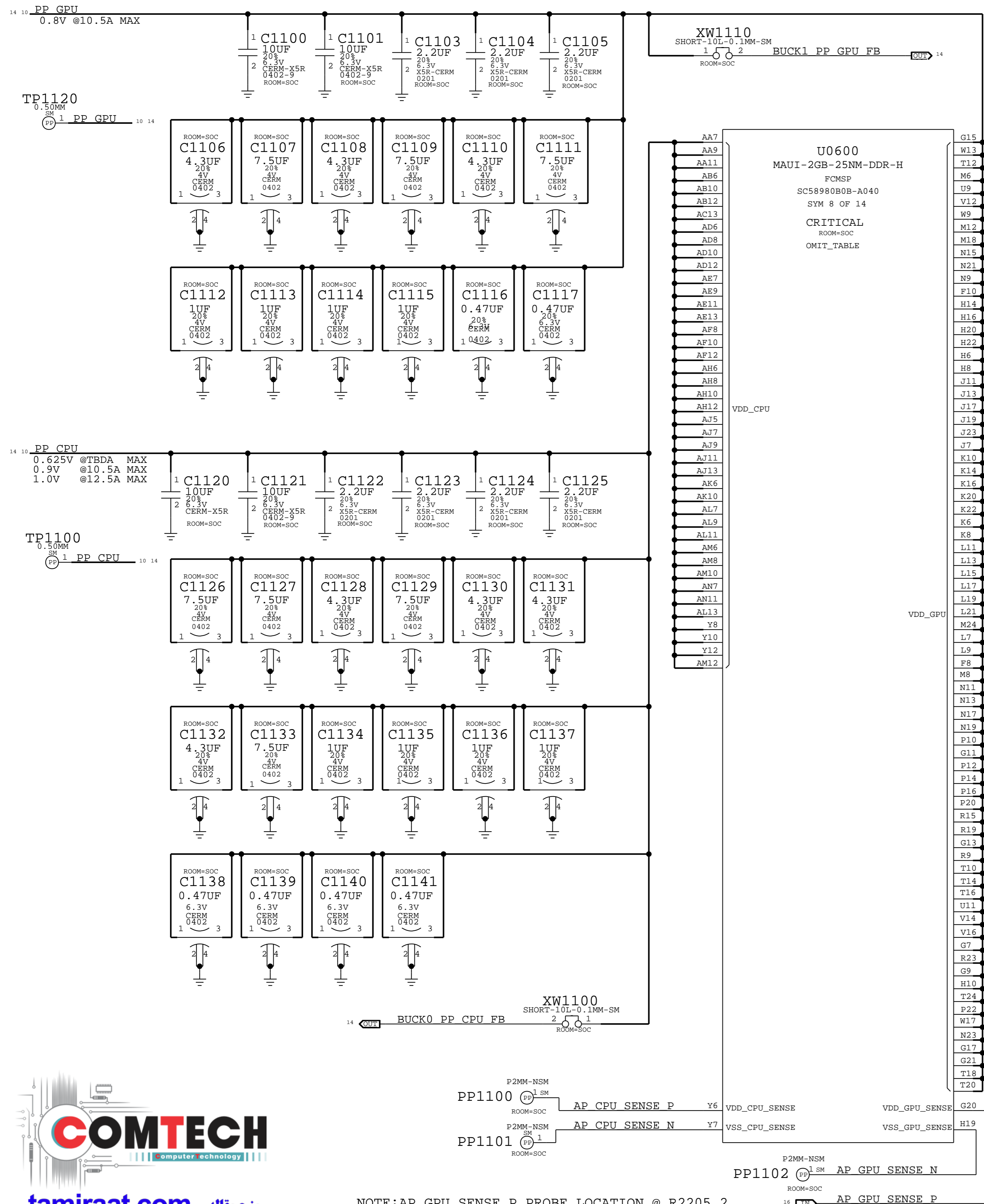


OWL SYSTEM SHUTDOWN OPTION



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SOC:OWL			
	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
		PAGE	10 OF 49
		SHEET	9 OF 59

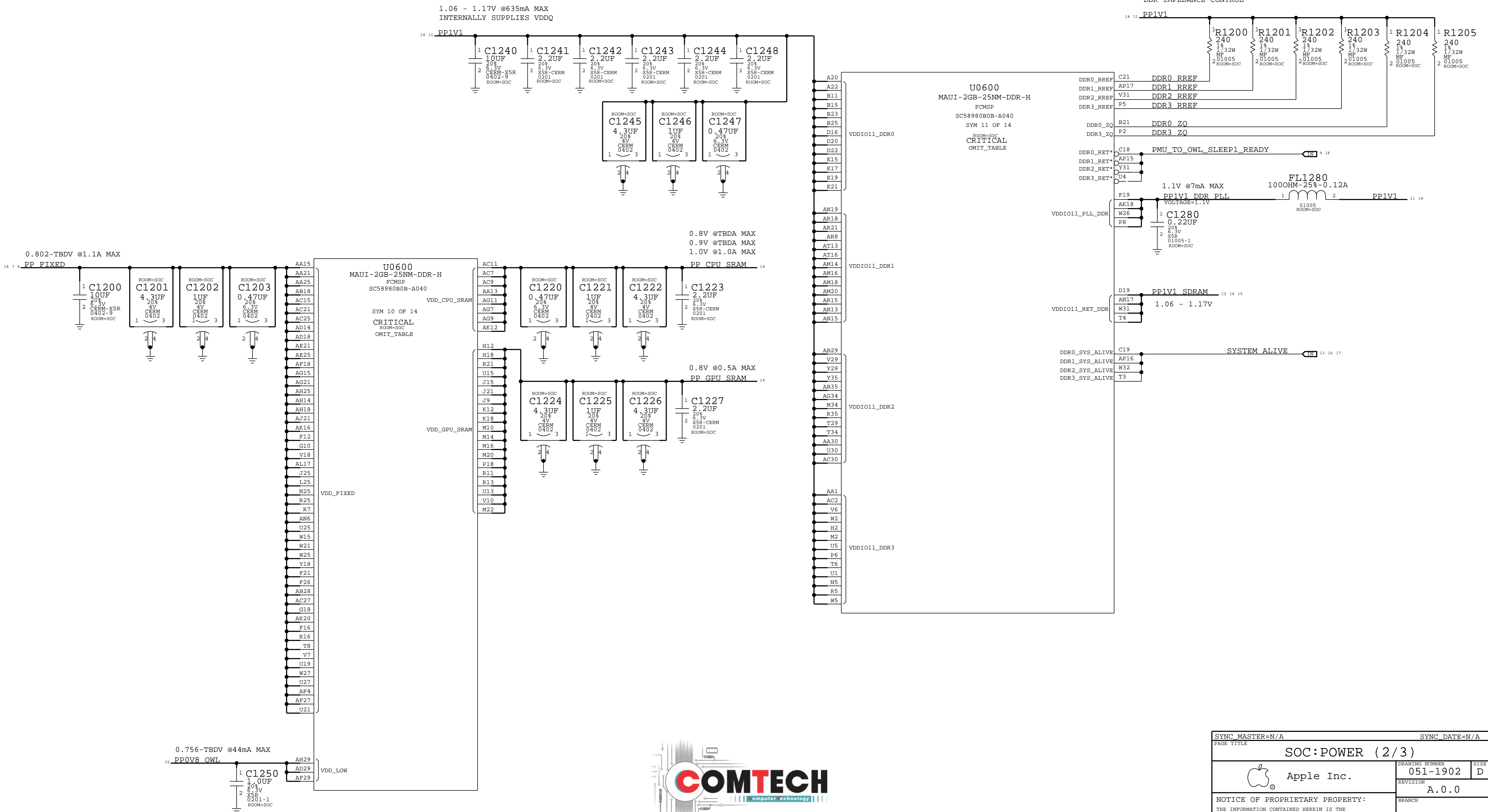
MAUI - CPU, GPU & SOC RAILS



NOTE: AP_GPU_SENSE_P PROBE LOCATION @ R2205.2

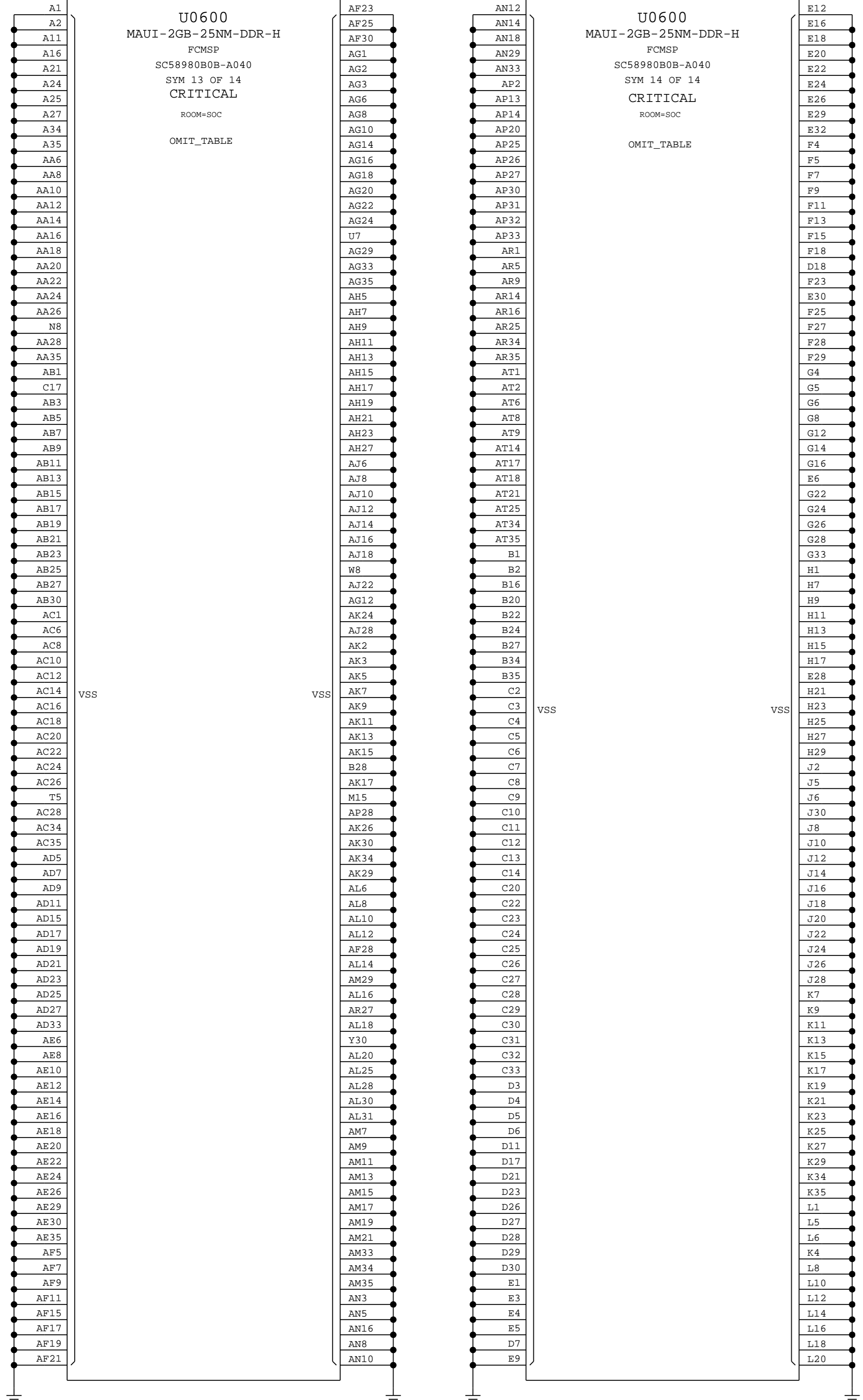
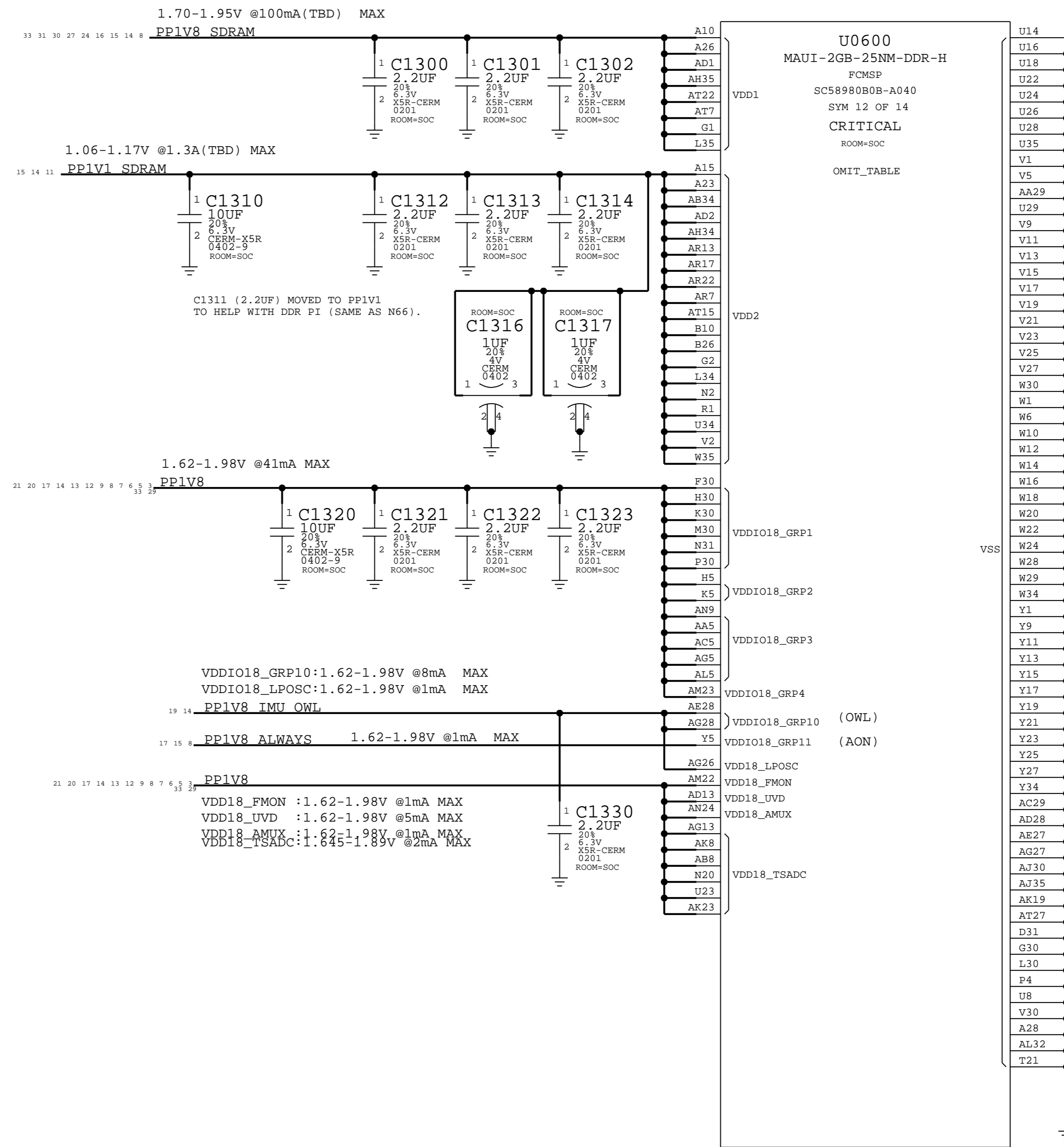
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SOC:POWER (1/3)			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
		BRANCH	
		PAGE	11 OF 49
		SHEET	10 OF 59
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE I NOT TO REPRODUCE OR COPY IT I NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART I ALL RIGHTS RESERVED			

MAUI - POWER SUPPLIES



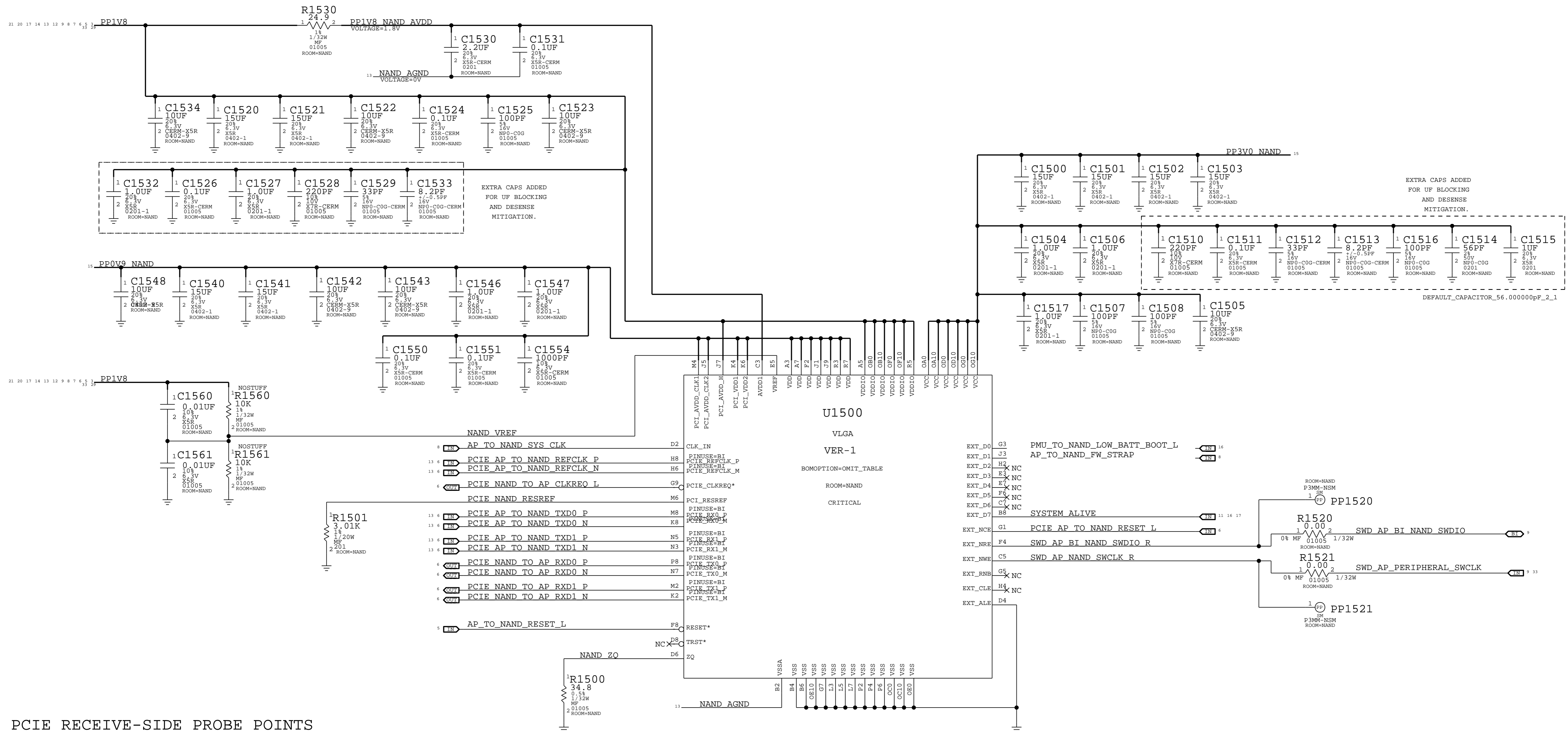
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SOC:POWER (2/3)			
	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
BRANCH			
PAGE		12 OF 49	
SHEET		11 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

MAUI - POWER SUPPLIES



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE SOC: POWER (3/3)			
Apple Inc.		DRAWING NUMBER 051-1902	SIZE D
		REVISION A.0.0	
BRANCH			
PAGE 13 OF 49		SHEET 12 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

S3E NAND



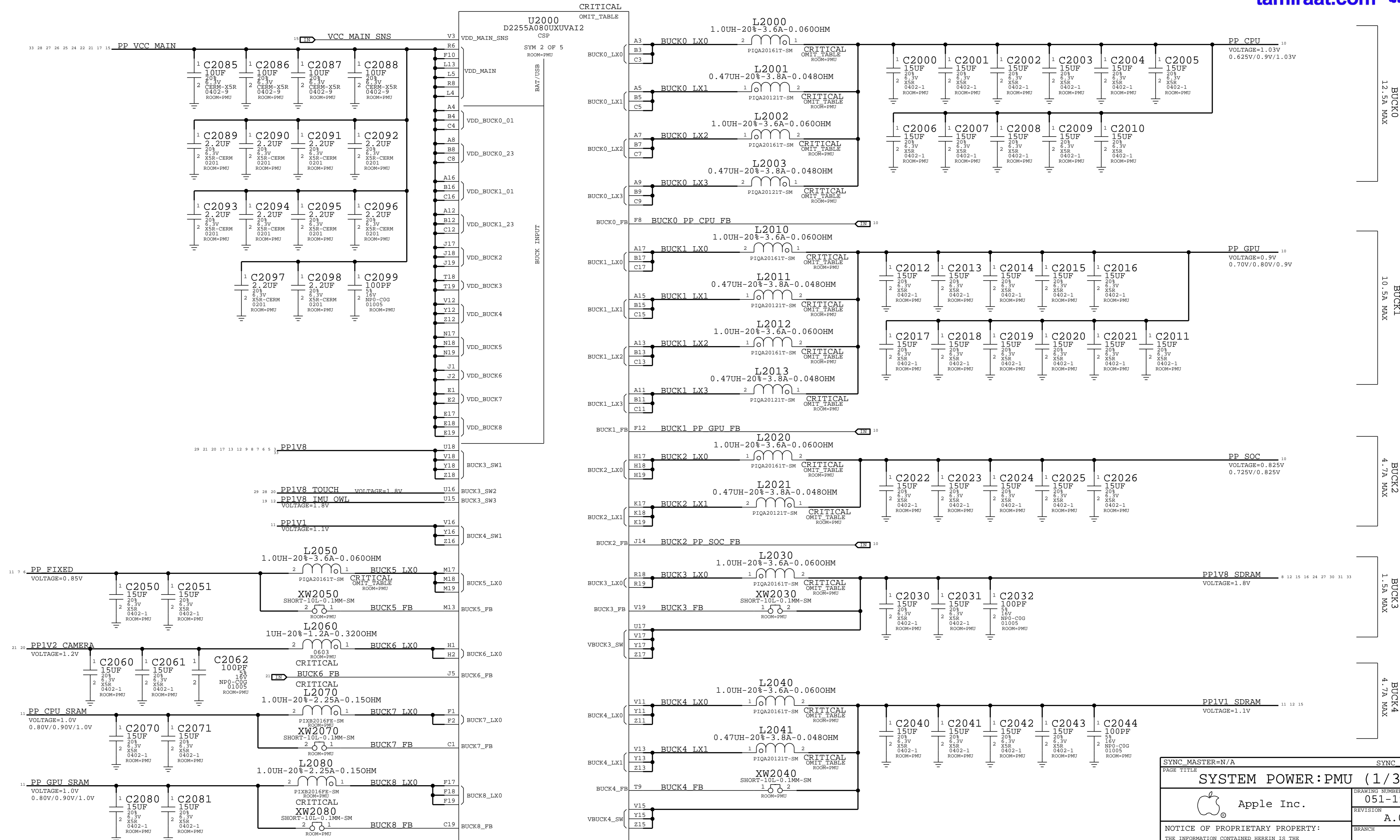
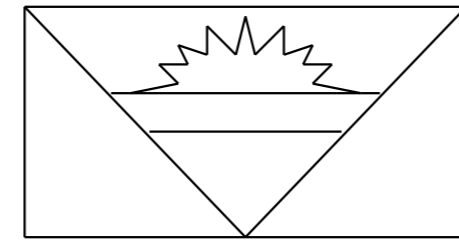
PCIE RECEIVE-SIDE PROBE POINTS

Signal	Probe Point
PCIE AP TO NAND REFCLK P	PP1500
PCIE AP TO NAND REFCLK N	PP1501
PCIE AP TO NAND TXD0 P	PP1502
PCIE AP TO NAND TXD0 N	PP1503
PCIE AP TO NAND TXD1 P	PP1504
PCIE AP TO NAND TXD1 N	PP1505



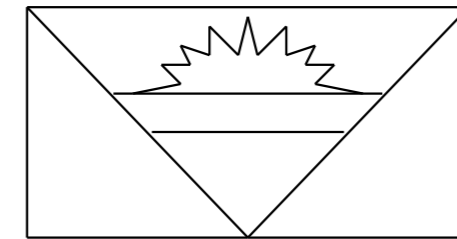
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
NAND			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
		BRANCH	
		PAGE	15 OF 49
		SHEET	13 OF 59
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

ANTIGUA PMU - Buck Supplies



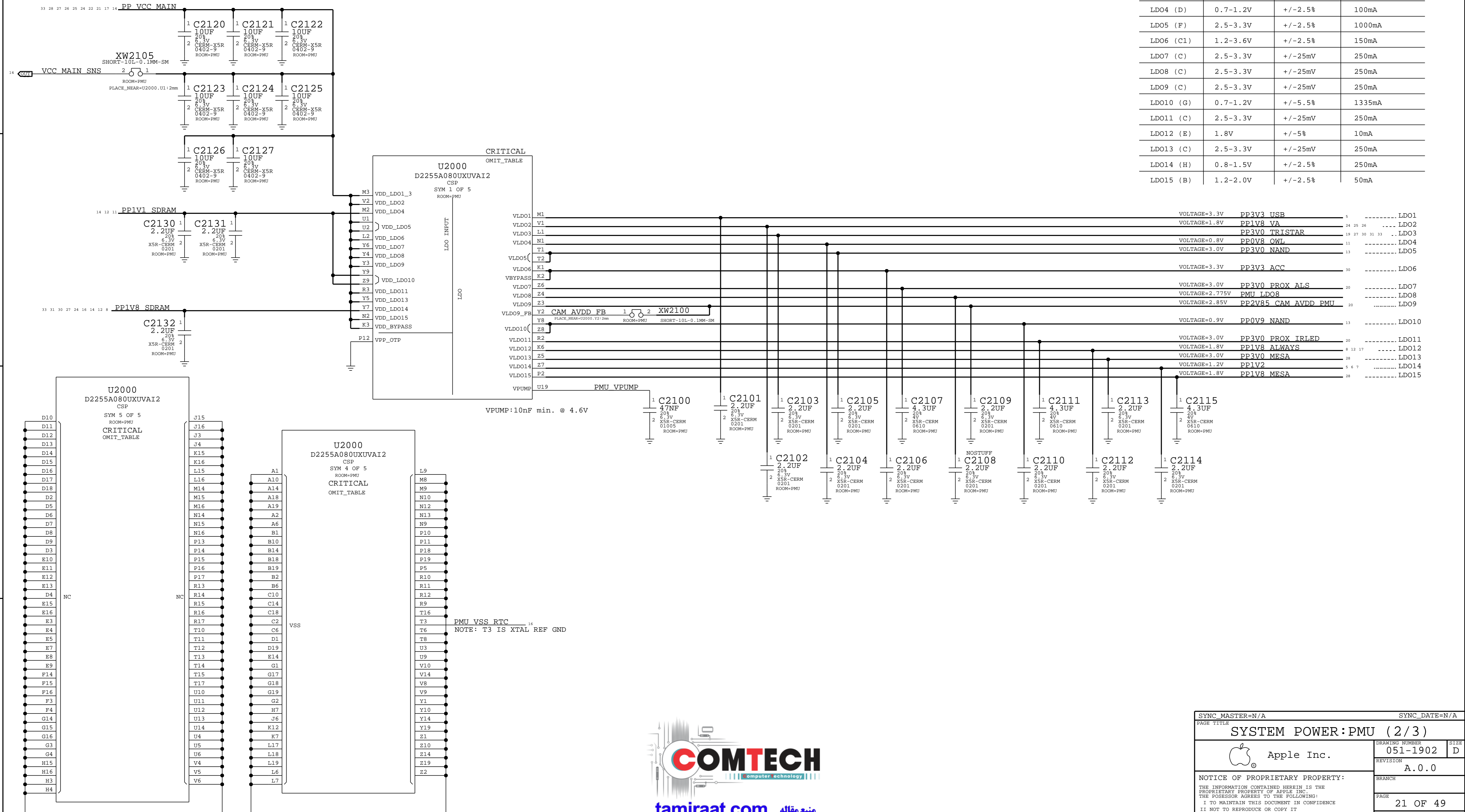
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SYSTEM POWER:PMU (1/3)			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE	
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		20 OF 49	
II NOT TO REPRODUCE OR COPY IT		SHEET	
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART		14 OF 59	
IV ALL RIGHTS RESERVED			

ANTIGUA PMU - LDOs

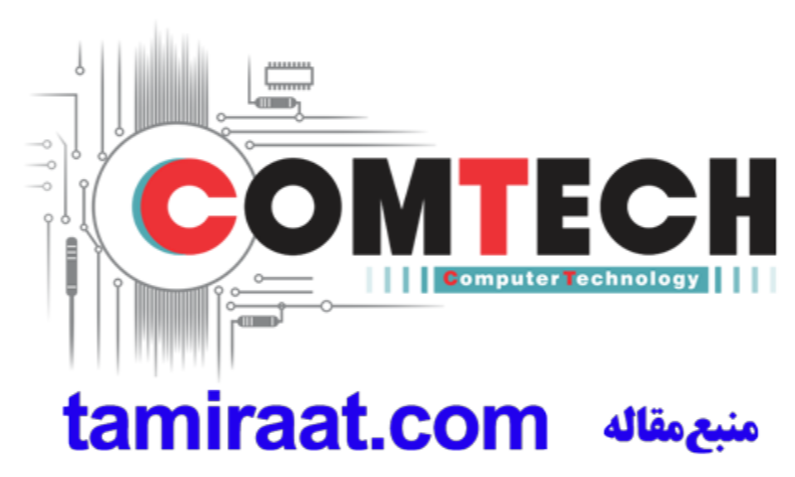


ANTIGUA LDO SPECS

LDO#	ADJ. RANGE	ACCURACY	MAX. CURRENT
LDO1 (A)	2.5-3.3V	+/-1.4%	50mA
LDO2 (B)	1.2-2.0V	+/-2.5%	50mA
LDO3 (A)	2.5-3.3V	+/-1.4%	50mA
LDO4 (D)	0.7-1.2V	+/-2.5%	100mA
LDO5 (F)	2.5-3.3V	+/-2.5%	1000mA
LDO6 (C1)	1.2-3.6V	+/-2.5%	150mA
LDO7 (C)	2.5-3.3V	+/-25mV	250mA
LDO8 (C)	2.5-3.3V	+/-25mV	250mA
LDO9 (C)	2.5-3.3V	+/-25mV	250mA
LDO10 (G)	0.7-1.2V	+/-5.5%	1335mA
LDO11 (C)	2.5-3.3V	+/-25mV	250mA
LDO12 (E)	1.8V	+/-5%	10mA
LDO13 (C)	2.5-3.3V	+/-25mV	250mA
LDO14 (H)	0.8-1.5V	+/-2.5%	250mA
LDO15 (B)	1.2-2.0V	+/-2.5%	50mA

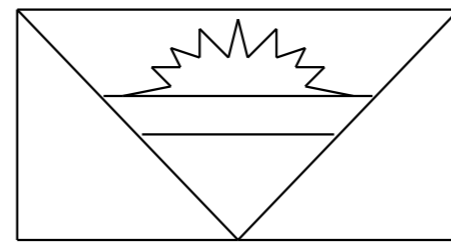


VOLTAGE	COMPONENT	Pin	LDO
VOLTAGE=3.3V	PP3V3 USB	5	LDO1
VOLTAGE=1.8V	PP1V8 VA	24 25 26	LDO2
	PP3V0 TRISTAR	19 27 30 31 33	LDO3
VOLTAGE=0.8V	PP0V8 OWL	11	LDO4
VOLTAGE=3.0V	PP3V0 NAND	13	LDO5
VOLTAGE=3.3V	PP3V3 ACC	30	LDO6
VOLTAGE=3.0V	PP3V0 PROX ALS	20	LDO7
VOLTAGE=2.775V	PMU LDO8		LDO8
VOLTAGE=2.85V	PP2V85 CAM AVDD PMU	20	LDO9
VOLTAGE=0.9V	PP0V9 NAND	13	LDO10
VOLTAGE=3.0V	PP3V0 PROX IRLED	20	LDO11
VOLTAGE=1.8V	PP1V8 ALWAYS	8 12 17	LDO12
VOLTAGE=3.0V	PP3V0 MESA	28	LDO13
VOLTAGE=1.2V	PP1V2	5 6 7	LDO14
VOLTAGE=1.8V	PP1V8 MESA	28	LDO15



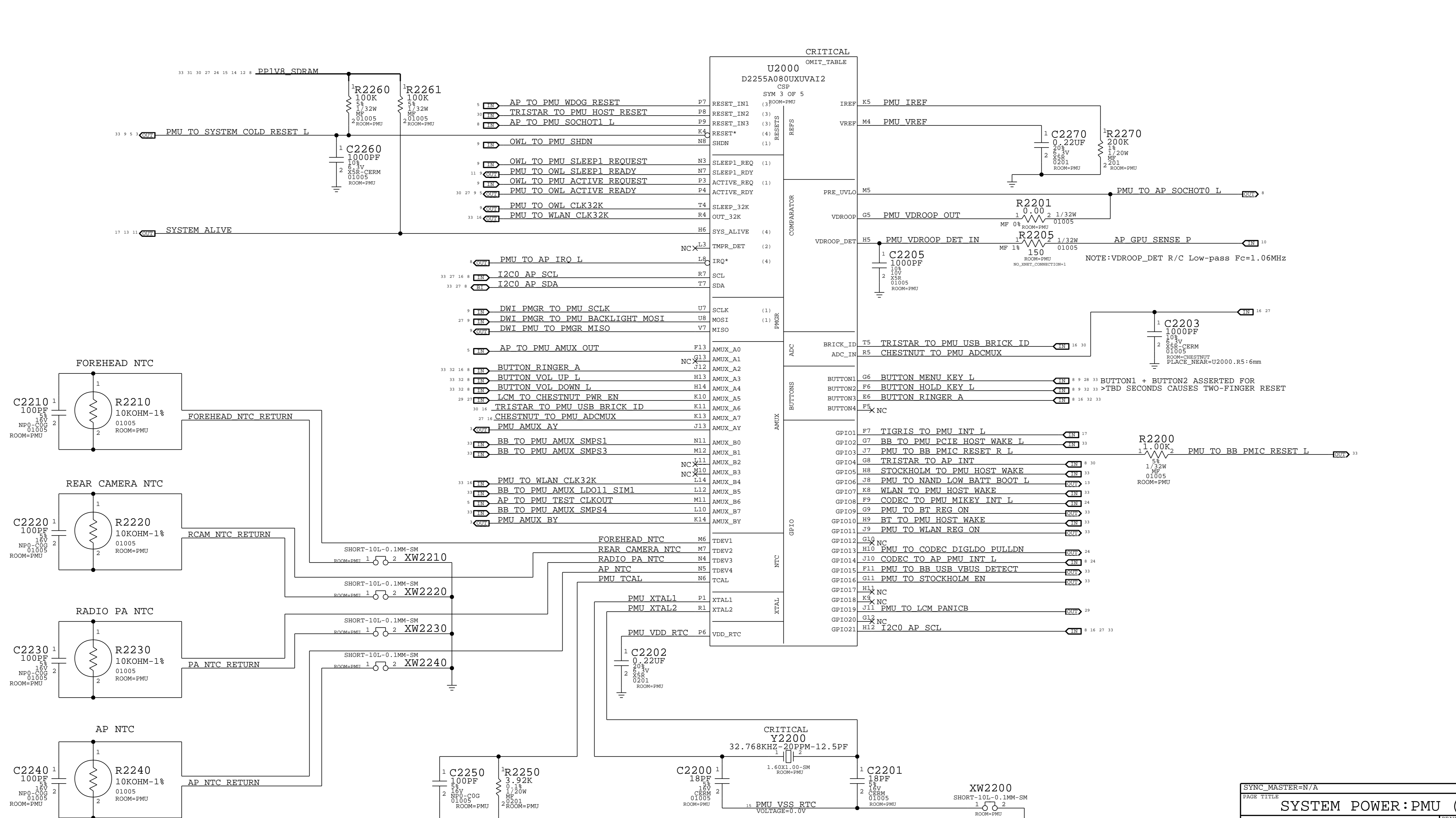
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SYSTEM POWER:PMU (2/3)			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
NOTICE OF PROPRIETARY PROPERTY:			
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:			
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE			
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			
PAGE		21 OF 49	
SHEET		15 OF 59	

ANTIGUA PMU - GPIOs, NTCs



CONTROL PIN NOTES:

- NOTE (1): INPUT PULL-DOWN 100-300k
- NOTE (2): INPUT PULL-DOWN 1M
- NOTE (3): INPUT PULL-UP OR DOWN 100k-300k
- NOTE (4): OUTPUT OPEN-DRAIN, REQUIRES PULL-UP

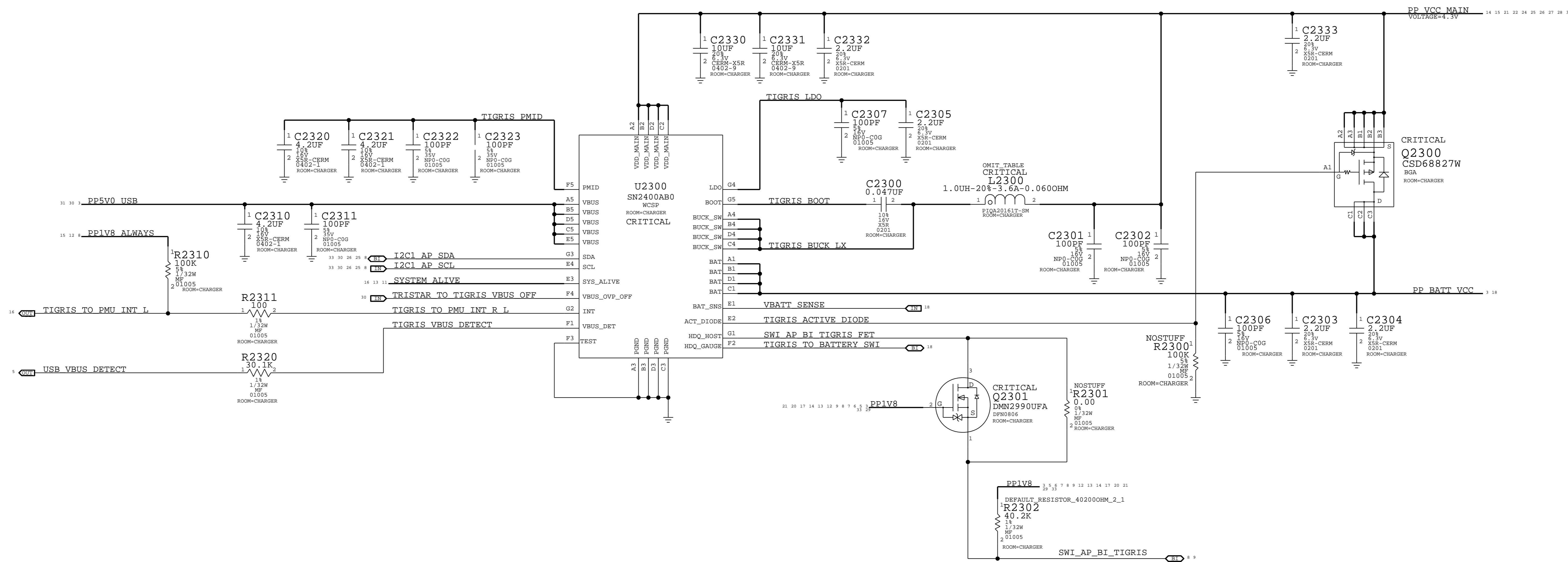


NOTE: 100PF CAPS ARE THE SAMPLING CAPS FOR PMU ADC

SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
SYSTEM POWER:PMU (3/3)			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
NOTICE OF PROPRIETARY PROPERTY:			
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:			
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE			
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			
		PAGE	
		22 OF 49	
		SHEET	
		16 OF 59	

TIGRIS CHARGER

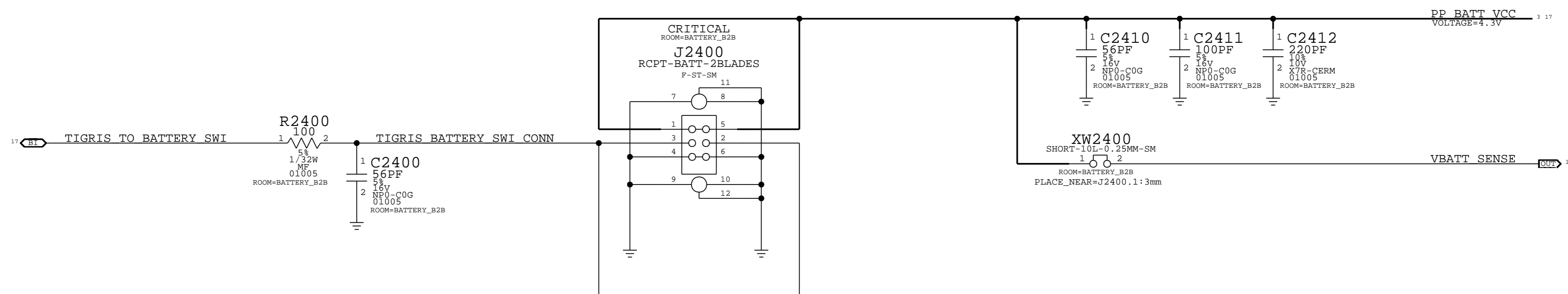
APN: 343S00033



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE SYSTEM POWER:CHARGER			
DRAWING NUMBER 051-1902		SIZE D	
REVISION A.0.0		BRANCH	
PAGE 23 OF 49		SHEET 17 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

BATTERY CONNECTOR

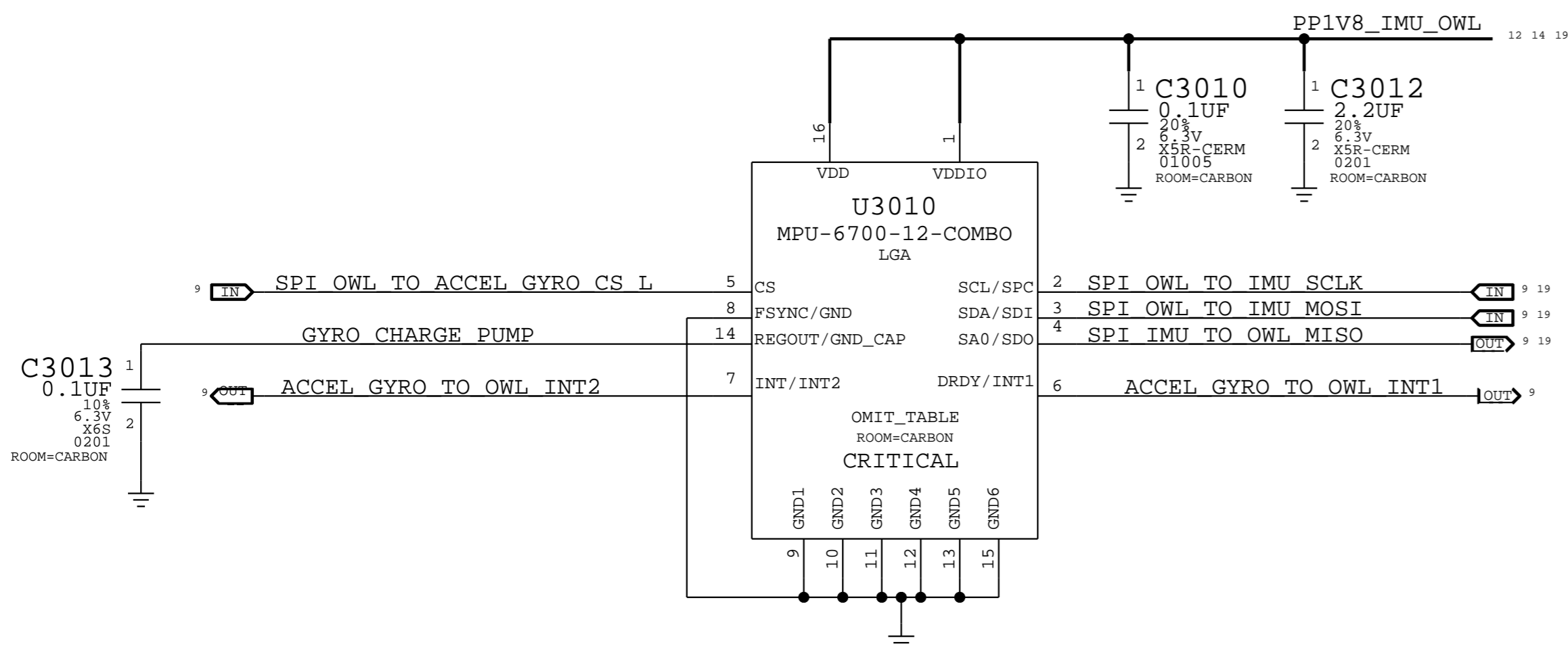
THIS ONE ON MLB ----> 516S00104 (RCPT)
516S00105 (PLUG)



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE SYSTEM POWER: BATTERY CONN			
DRAWING NUMBER 051-1902		SIZE D	
REVISION A.0.0		BRANCH	
PAGE 24 OF 49		SHEET 18 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

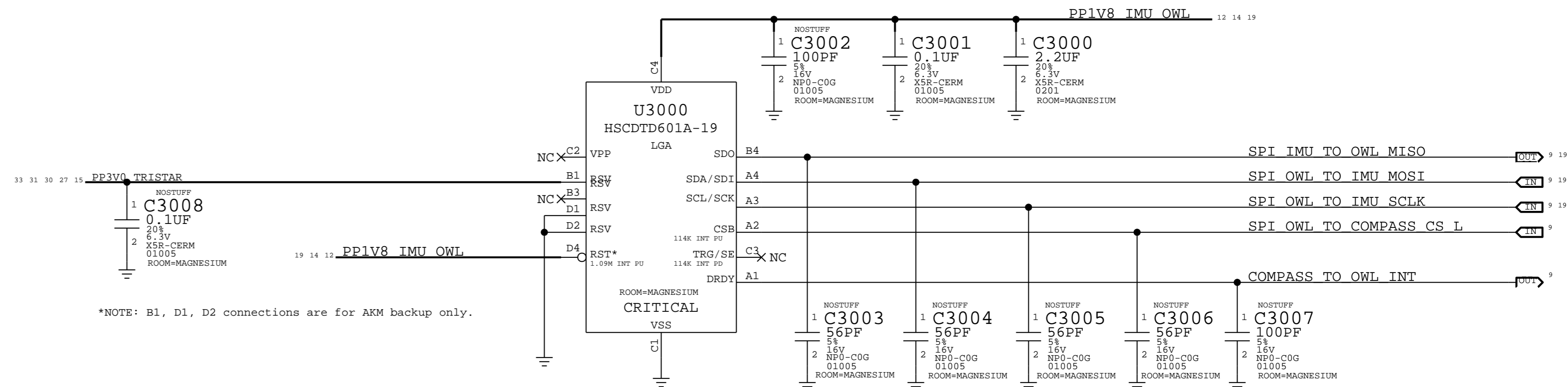
CARBON - ACCEL & GYRO

INVENSENSE (APN: 338S00017, 338S00087)



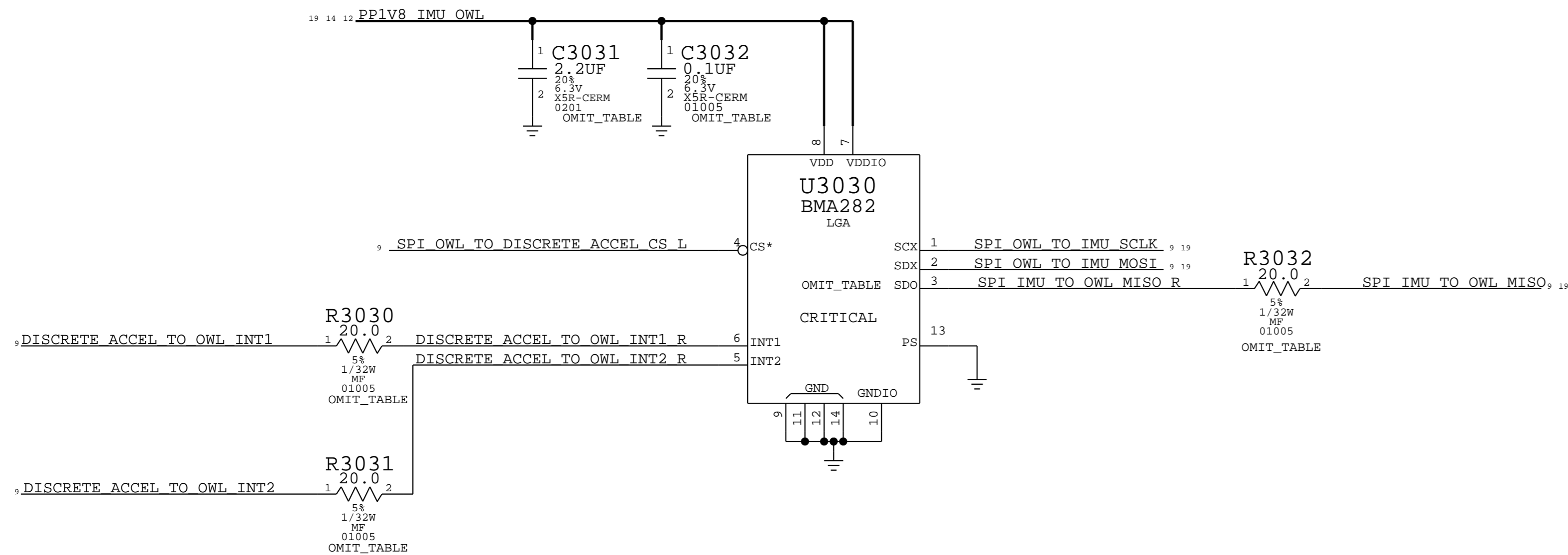
MAGNESIUM - COMPASS

ALPS (APN:338S00084)



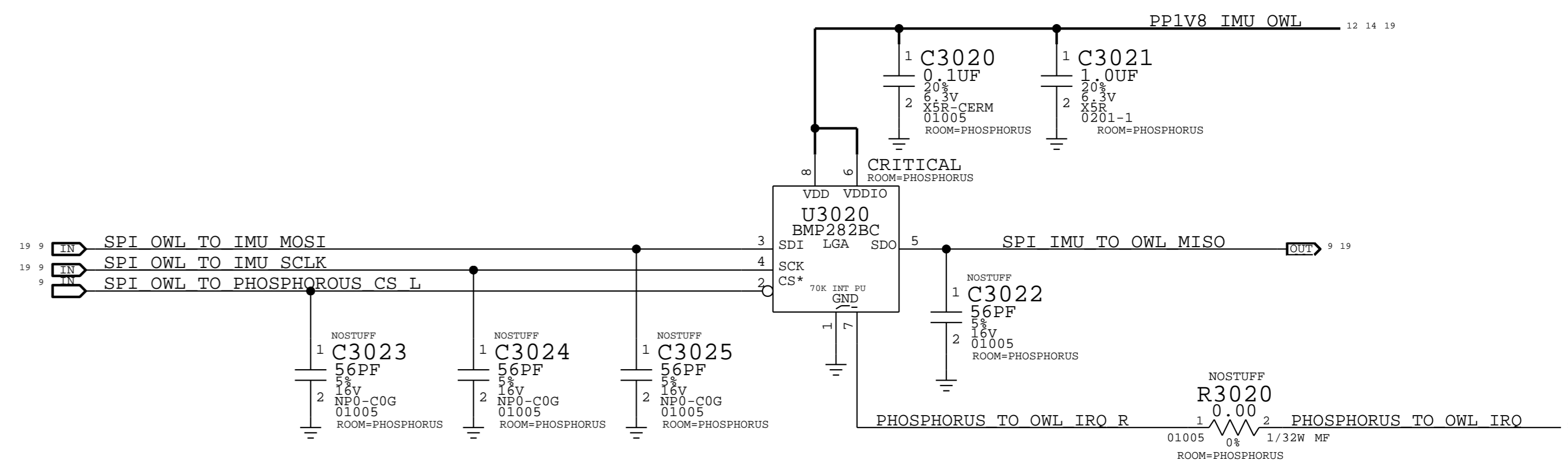
DISCRETE ACCEL

BOSCH (APN: 338S1163)



PHOSPHOROUS

BOSCH (APN:338S00044)



R3020 SHOULD BE STUFFED FOR ST PHOSPHOROUS ONLY.
 FOR BOSCH PHOSPHOROUS, PINS 1 AND 7 ARE SHORTED INTERNALLY,
 SO NO NEED FOR 0-OHM TO GROUND OPTION ON PIN 7.



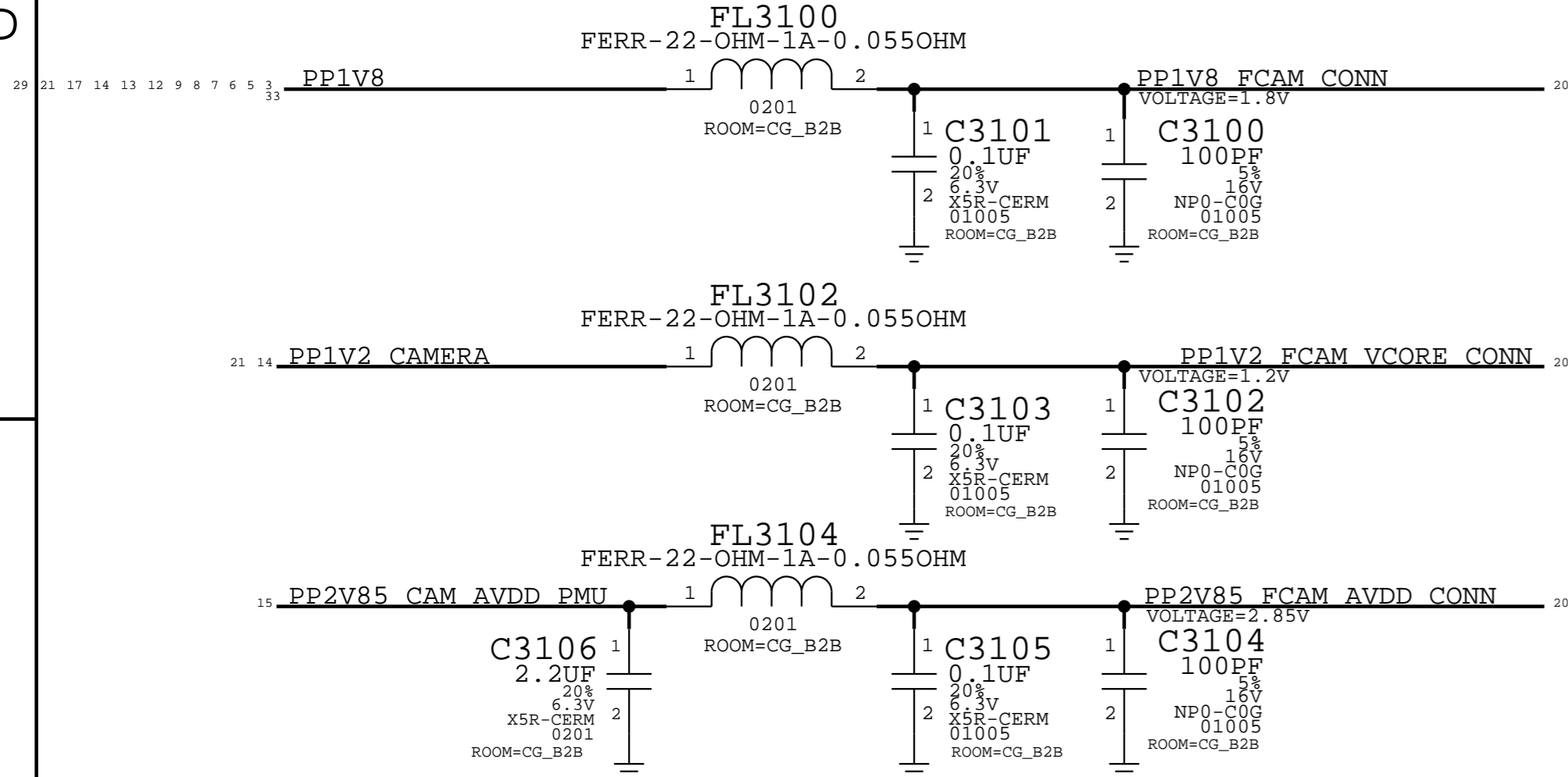
PAGE TITLE		DRAWING NUMBER		SIZE
SENSORS:MOTION SENSORS		051-1902		D
Apple Inc.		REVISION		A.0.0
NOTICE OF PROPRIETARY PROPERTY:		BRANCH		
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE		30 OF 49
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		SHEET		19 OF 59
II NOT TO REPRODUCE OR COPY IT				
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART				
IV ALL RIGHTS RESERVED				

FOREHEAD FLEX (FCAM)

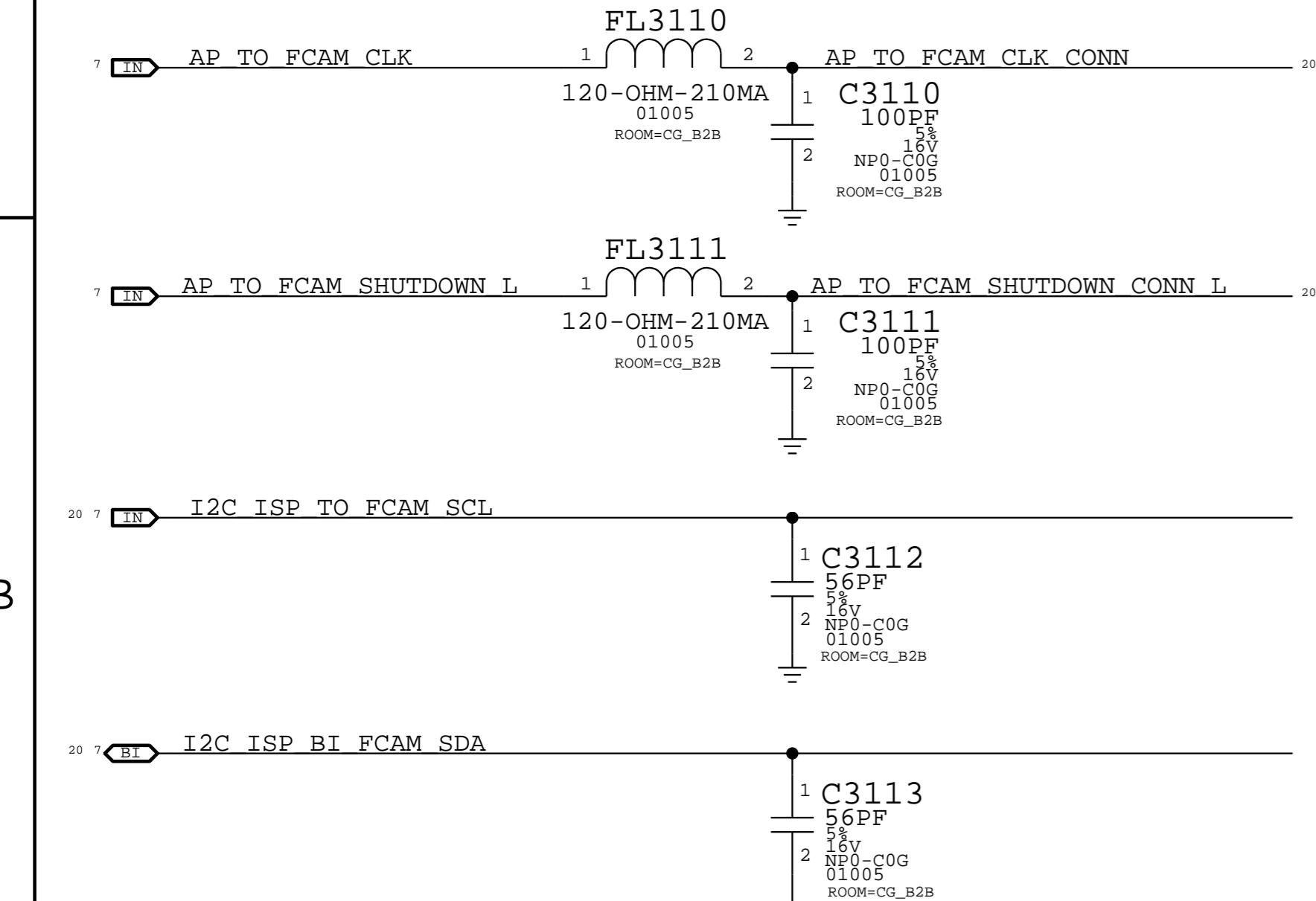
PROX & ALS POWER

FOREHEAD CONNECTOR

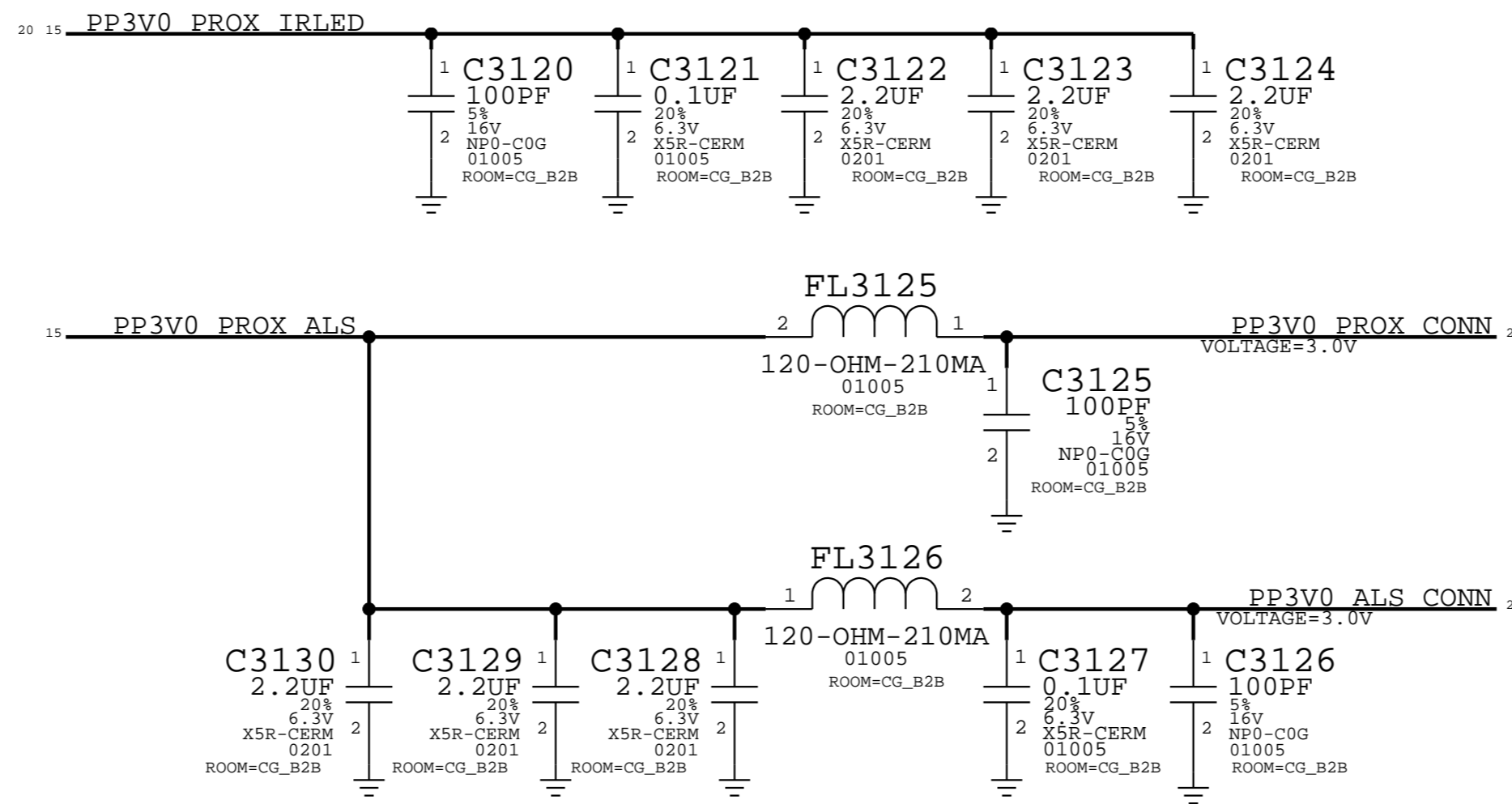
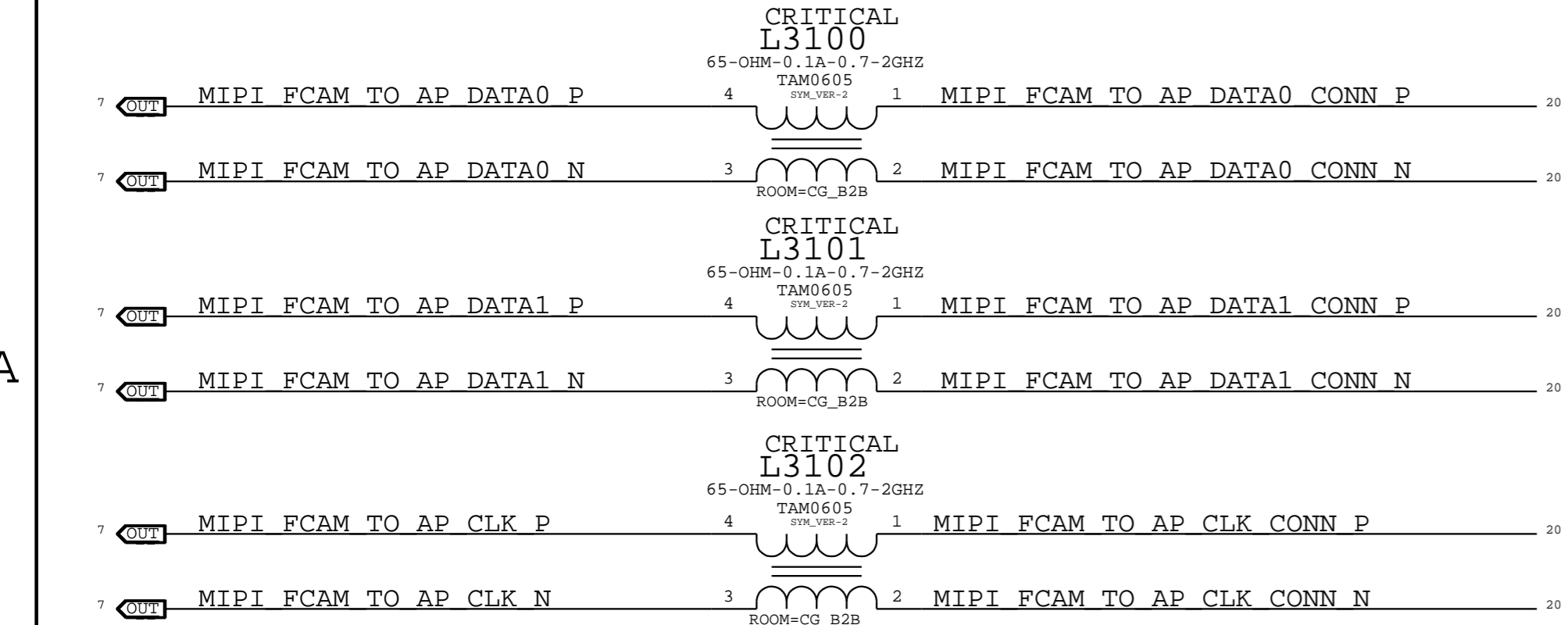
CAMERA POWER



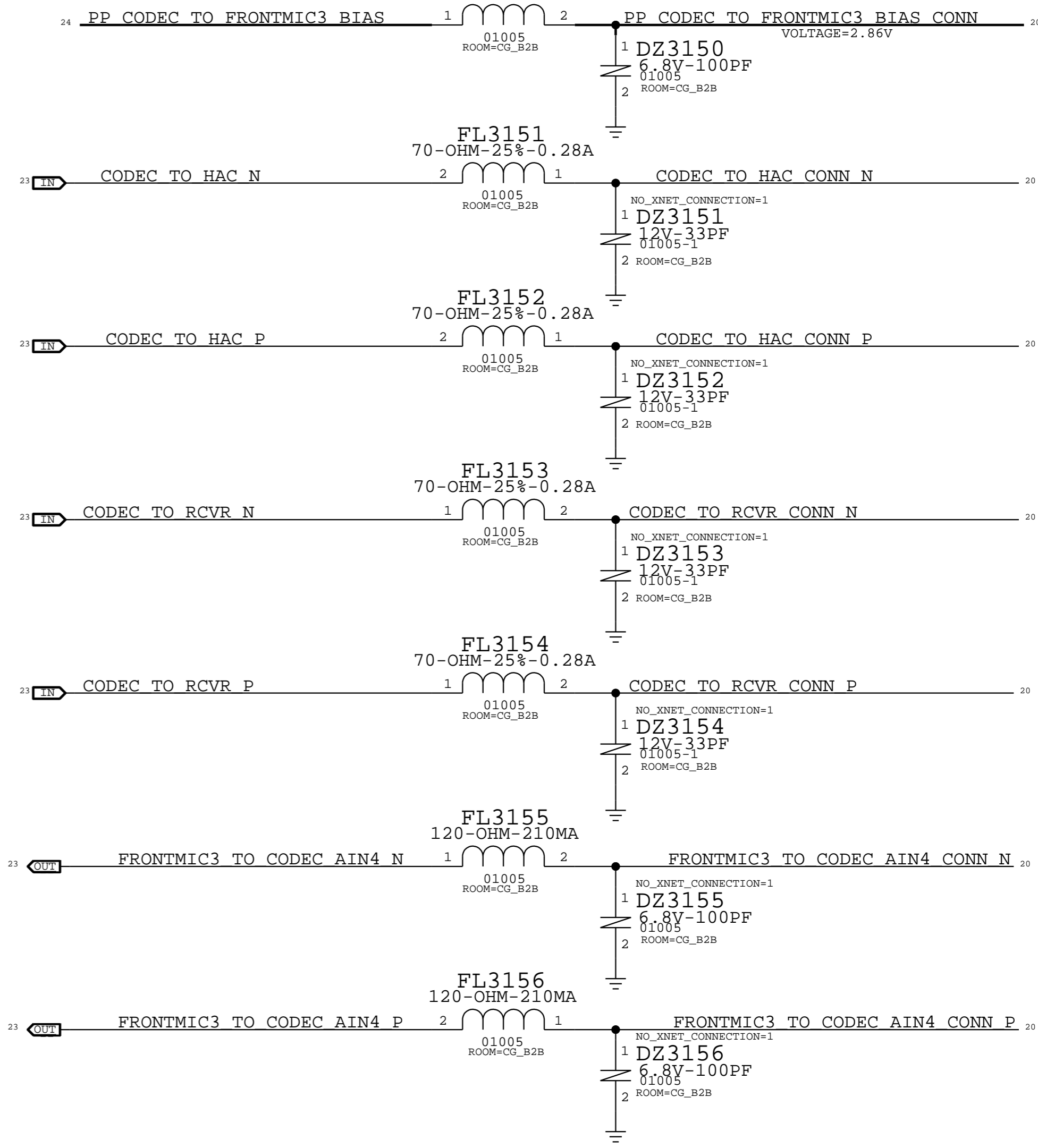
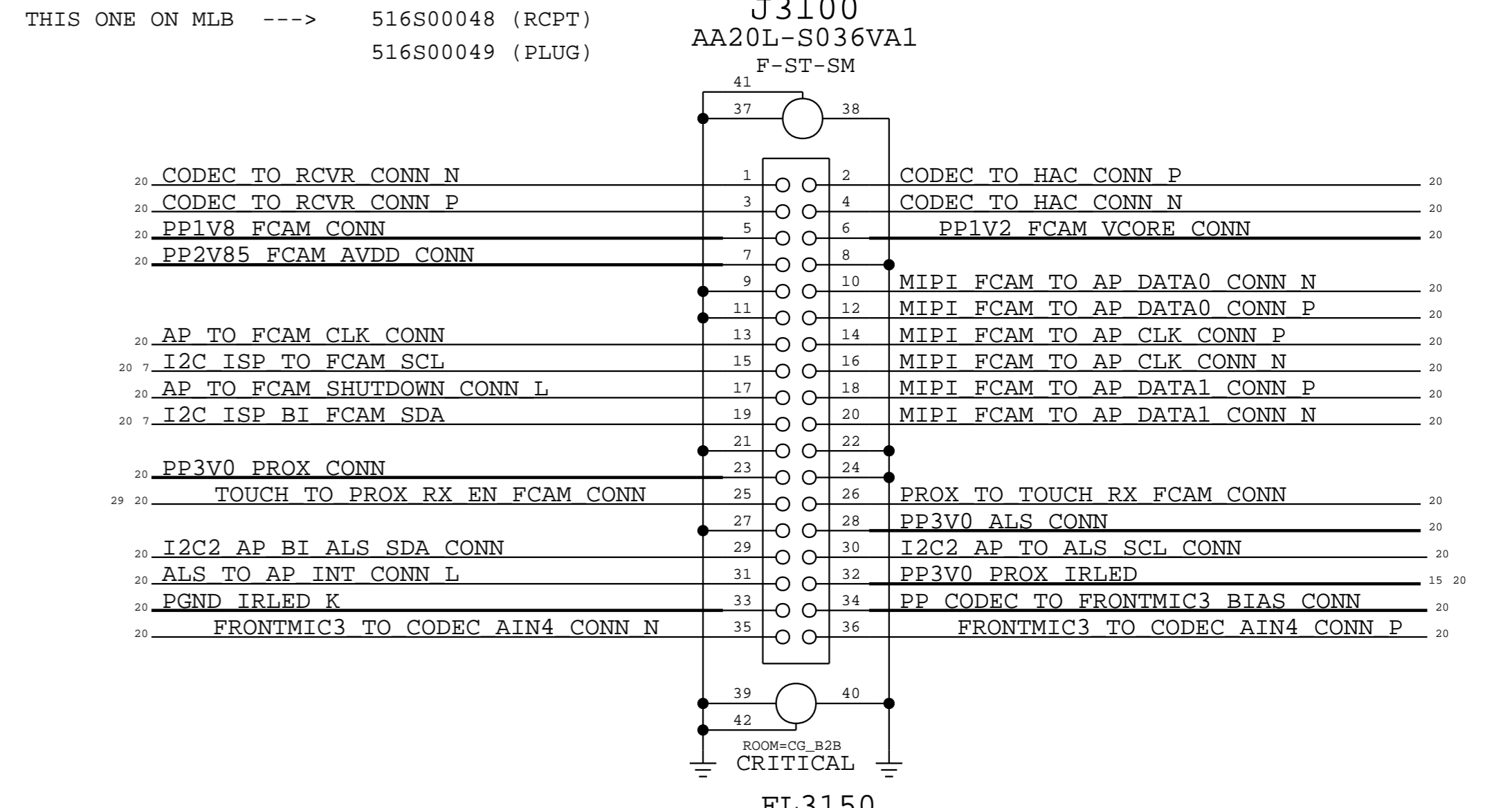
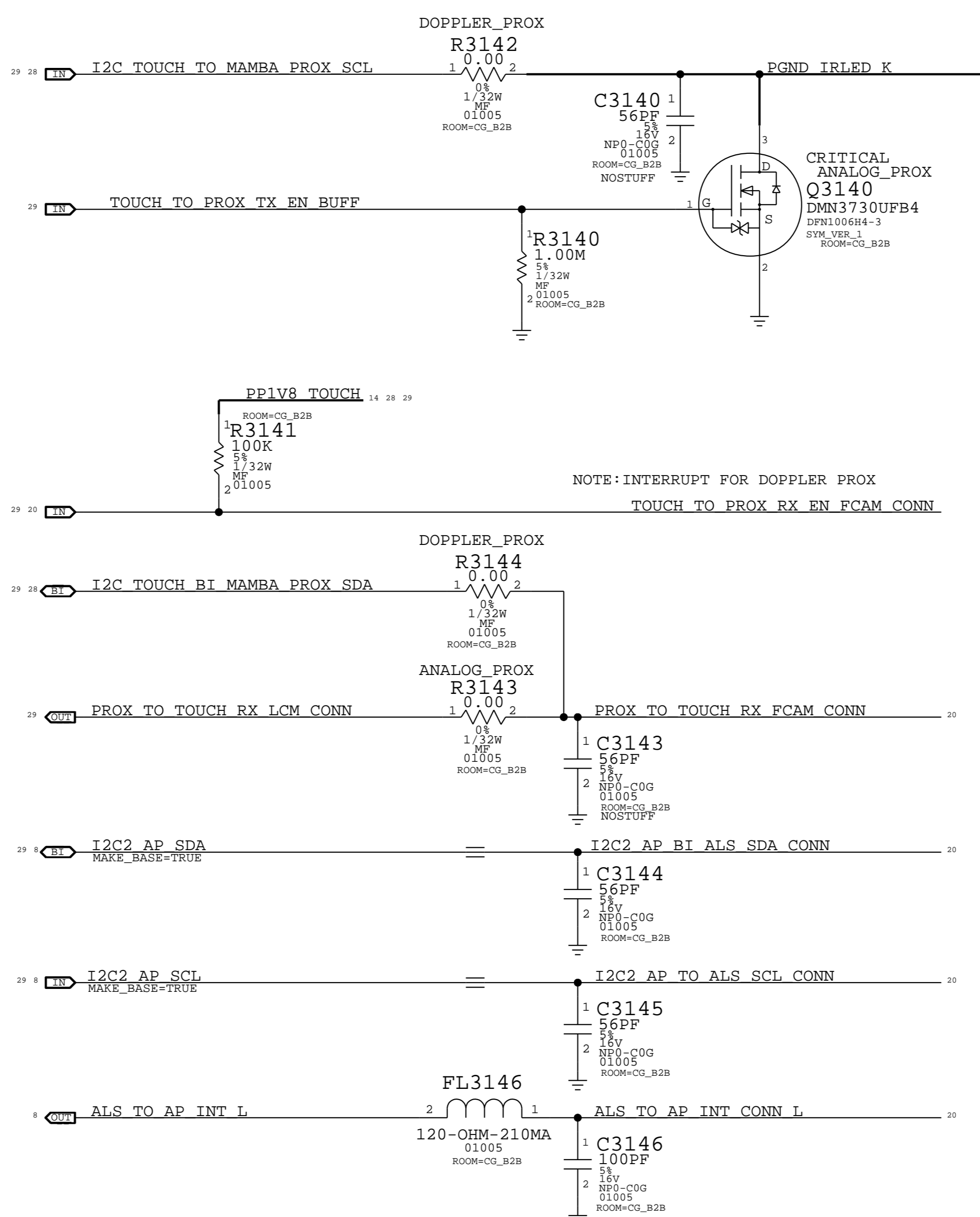
CAMERA I/O



CAMERA MIPI



PROX & ALS INTERFACE



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
CAMERA: FOREHEAD FLEX B2B			
DRAWING NUMBER		SIZE	
051-1902		D	
REVISION		A.0.0	
BRANCH		PAGE	
		31 OF 49	
SHEET		20 OF 59	

NOTICE OF PROPRIETARY PROPERTY:
 THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:
 I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE
 II NOT TO REPRODUCE OR COPY IT
 III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART
 IV ALL RIGHTS RESERVED

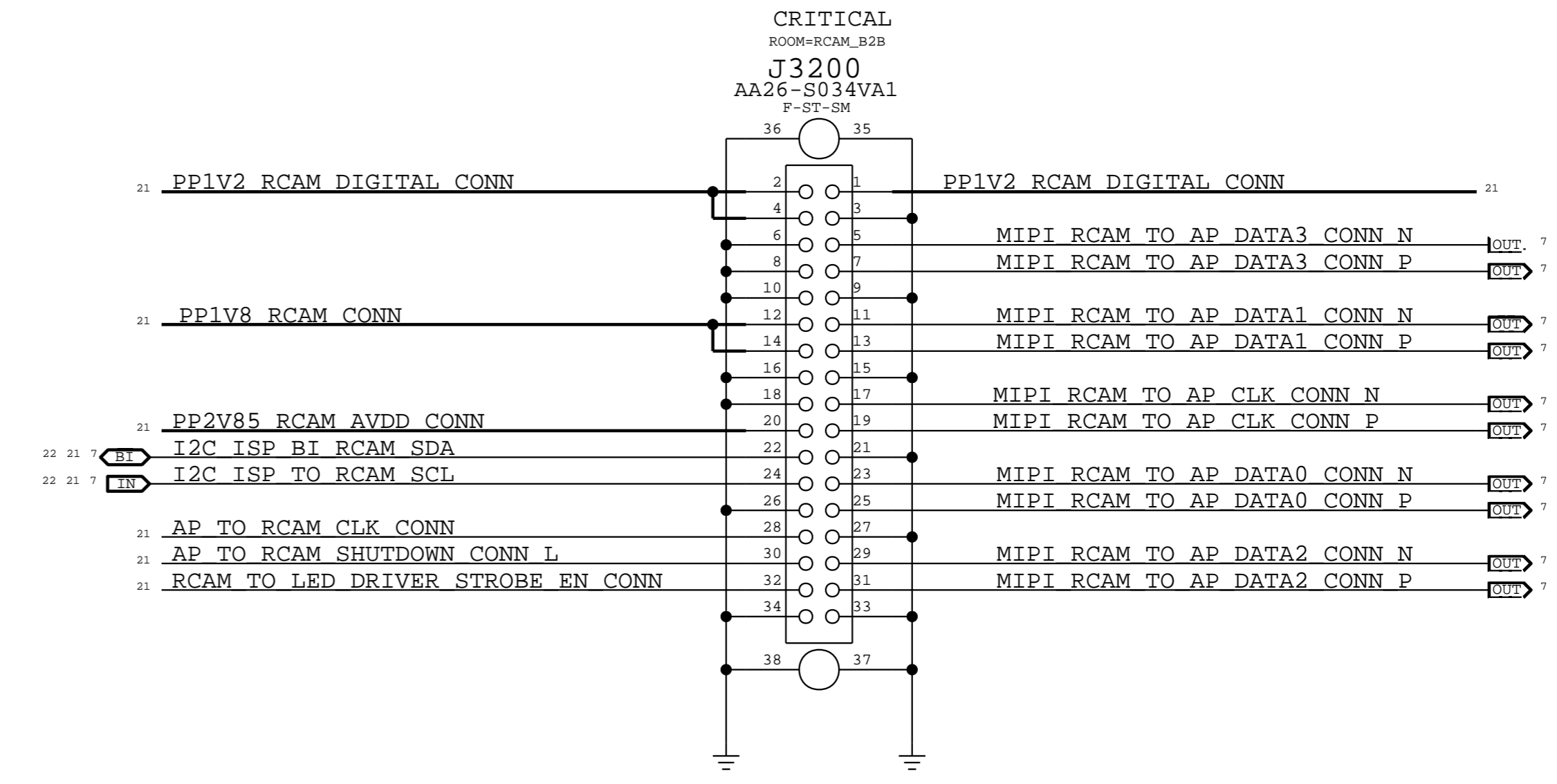
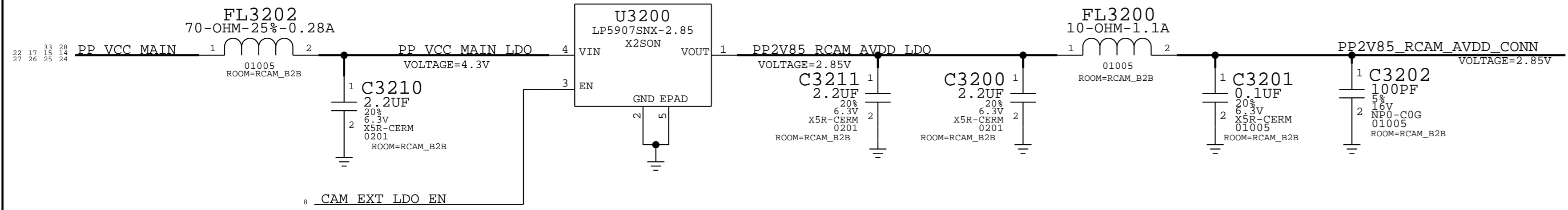
REAR CAMERA FLEX

RCAM CONNECTOR

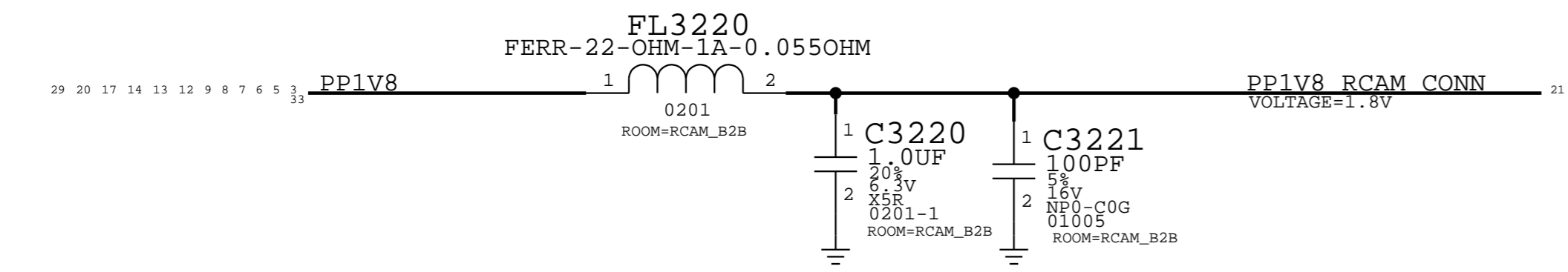
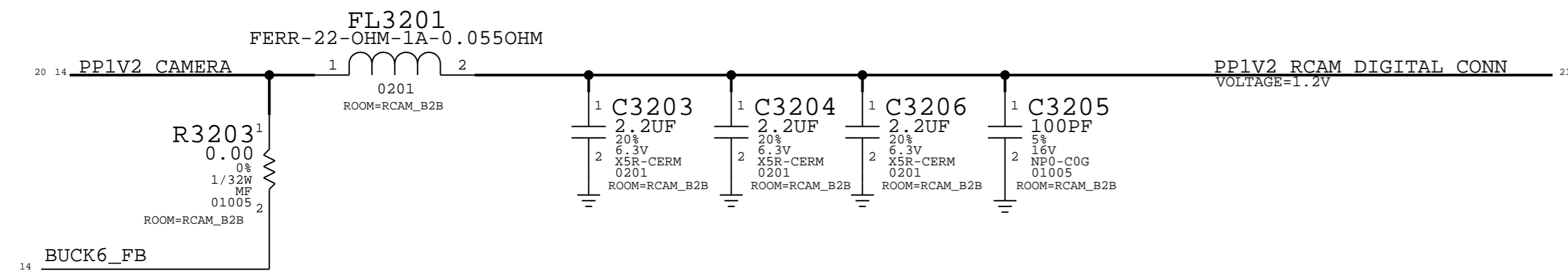
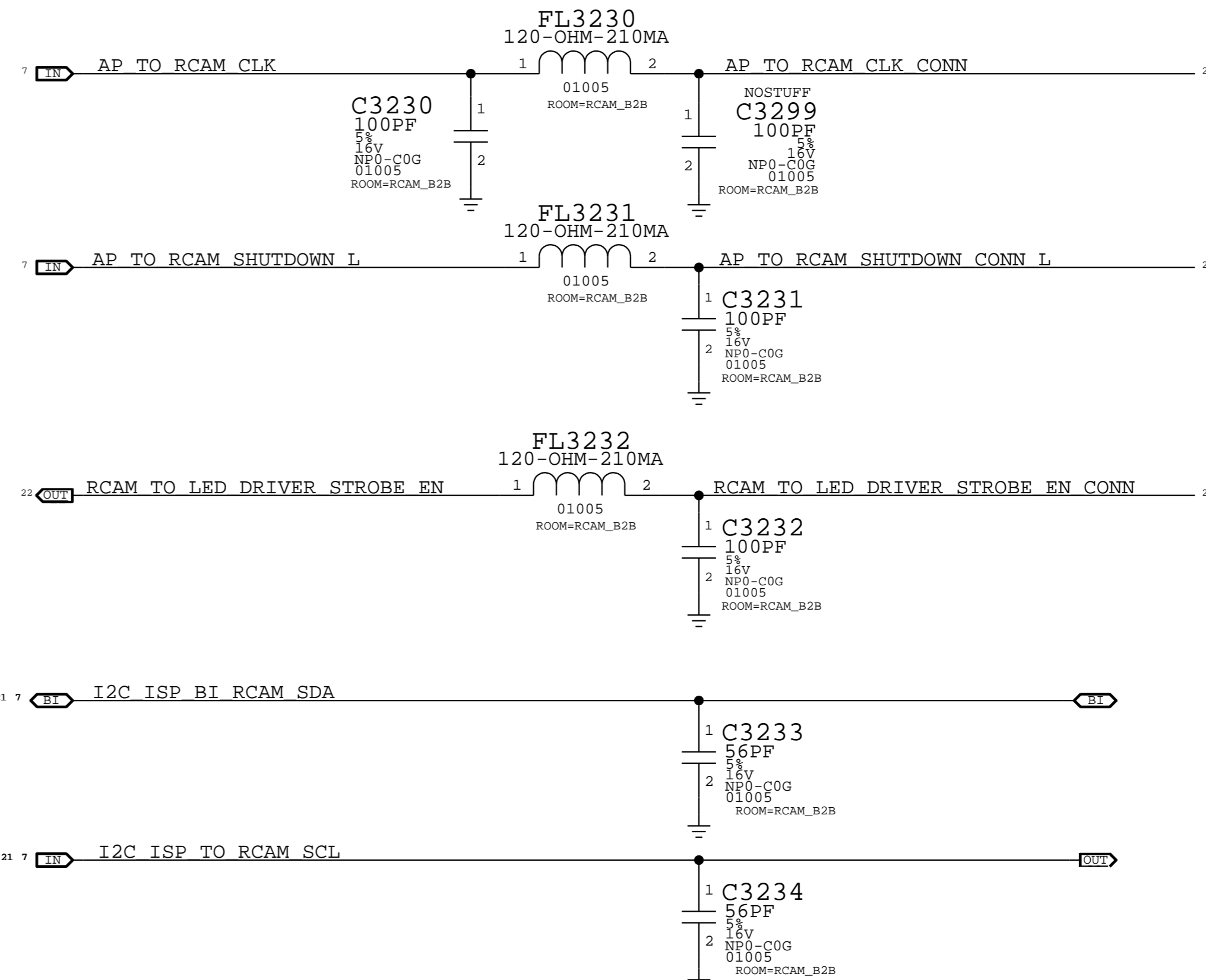
THIS ONE ON MLB ----> 516S00043 (RCPT)
516S00042 (PLUG)

CAMERA POWER

NOTE: OUTPUT IMPEDANCE MUST BE >0.01-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.



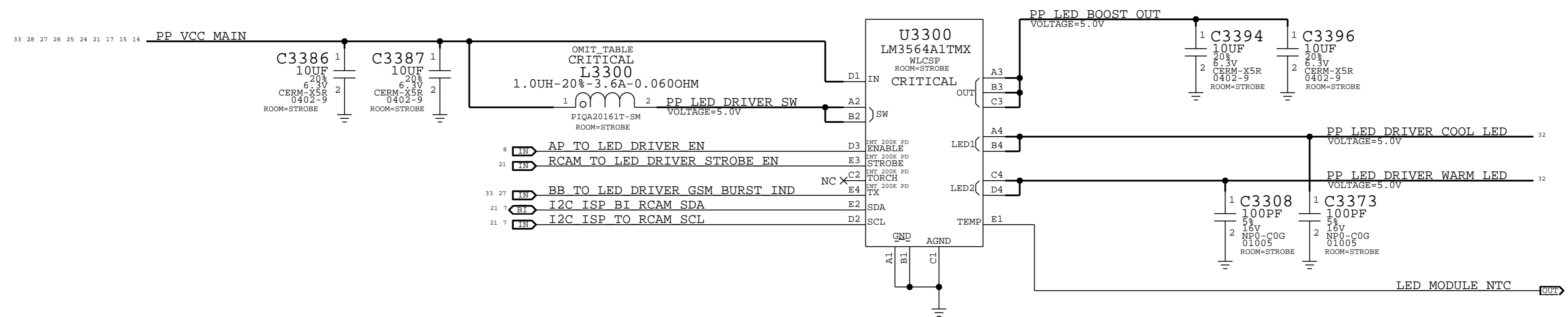
DIGITAL I/O



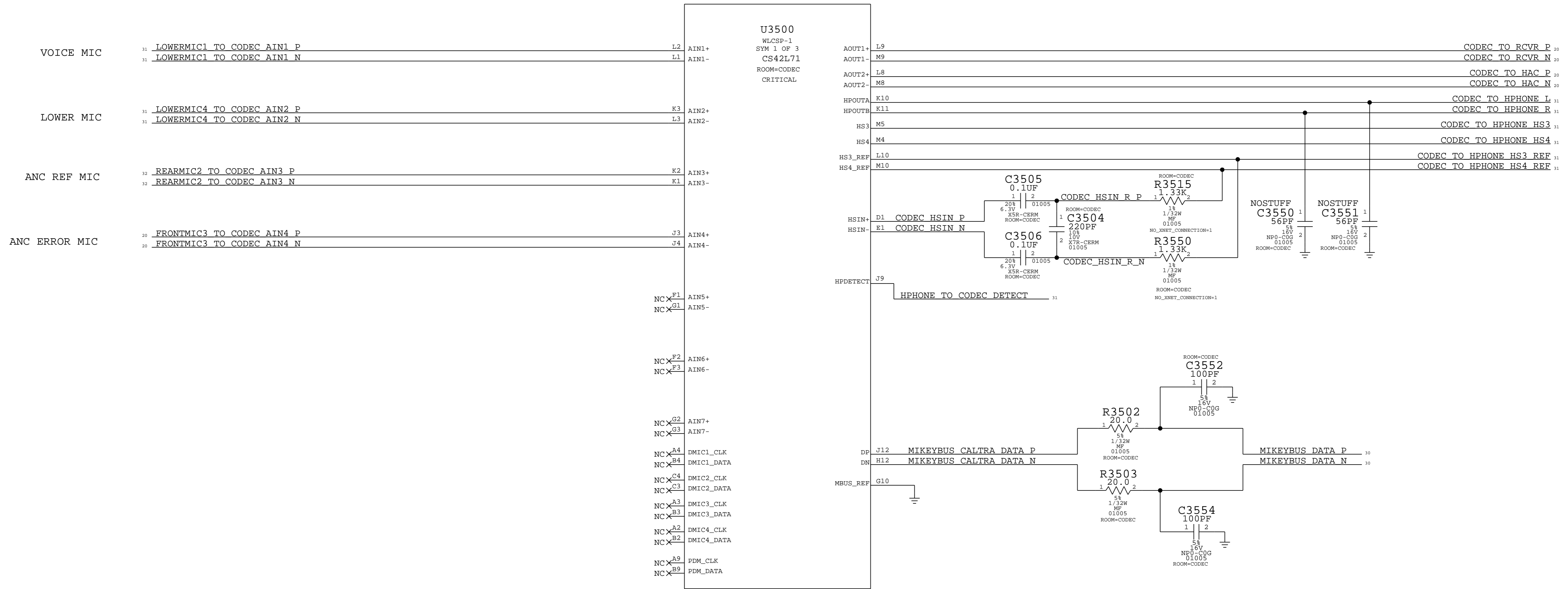
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE CAMERA:REAR CAMERA B2B			
DRAWING NUMBER 051-1902		SIZE D	
REVISION A.0.0		BRANCH	
PAGE 32 OF 49		SHEET 21 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

DUAL LED STROBE DRIVER

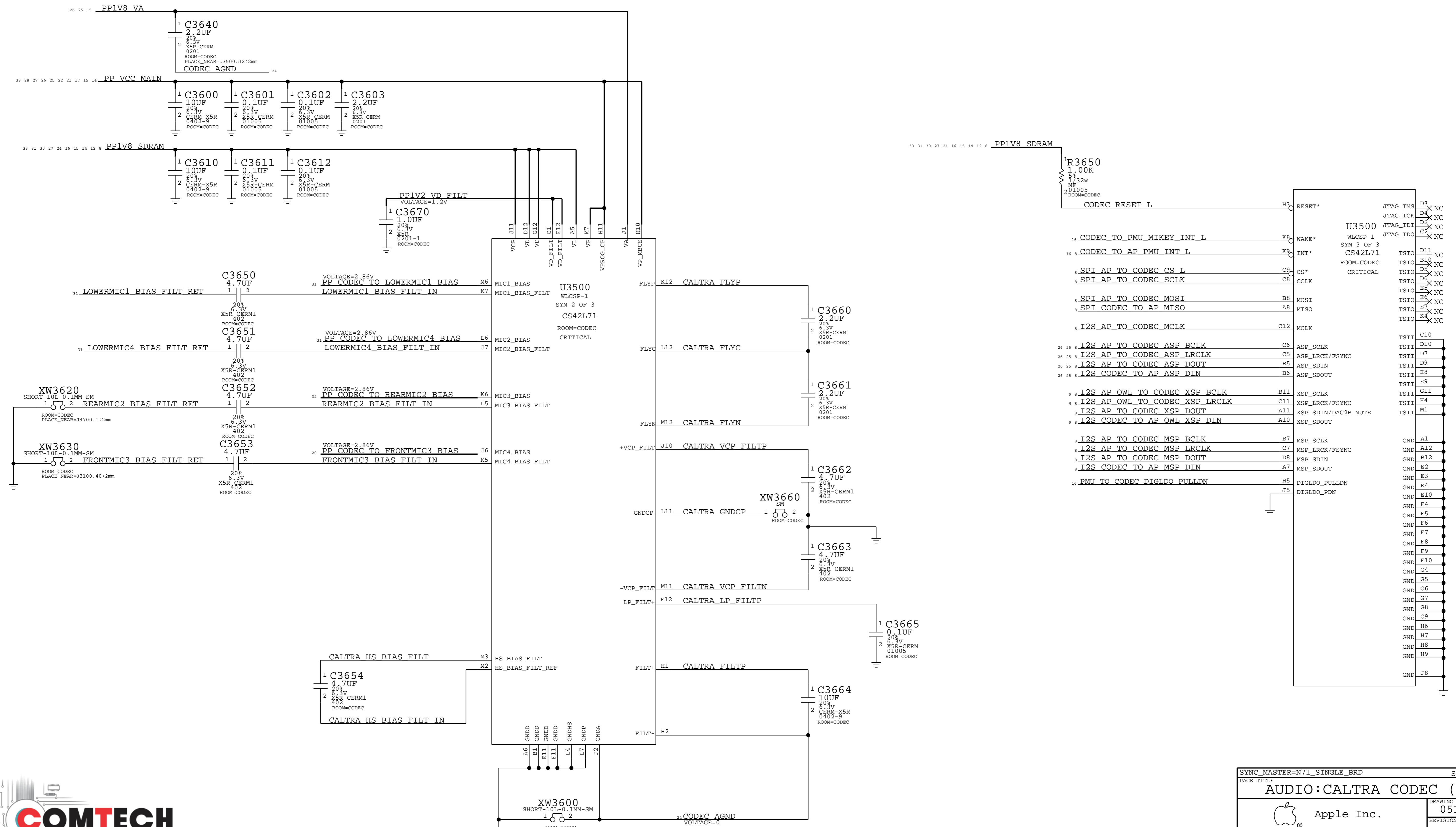
APN: 353S3899



CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)



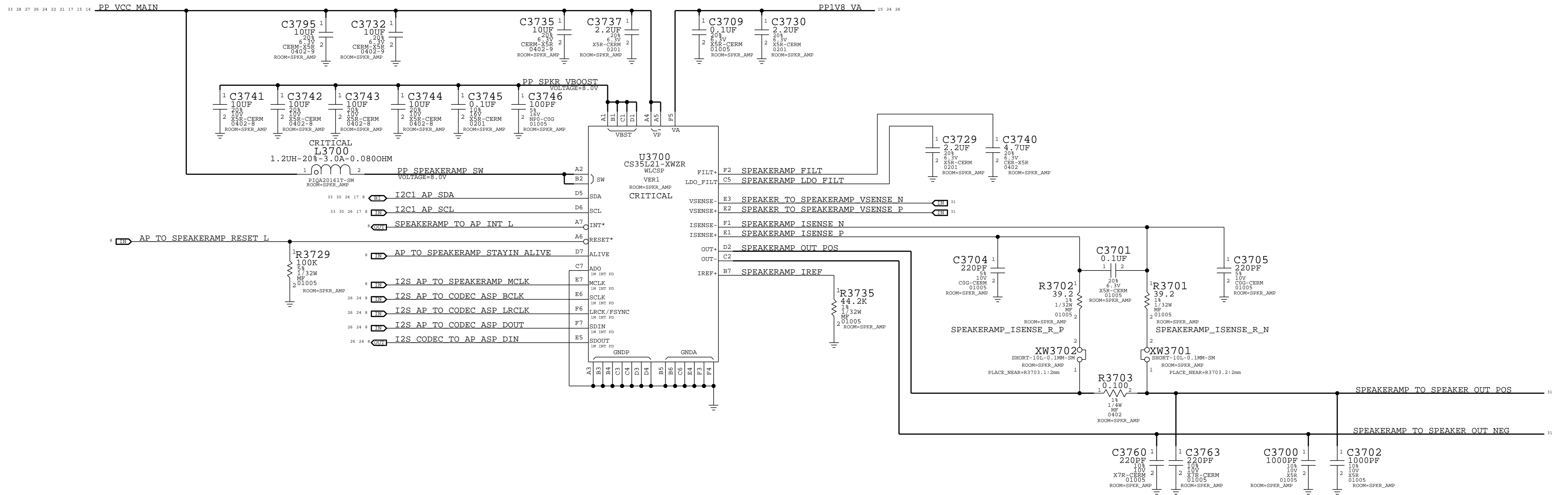
CALTRA AUDIO CODEC (POWER & I/O)




SYNC_MASTER=N71_SINGLE_BRD		SYNC_DATE=05/29/2014	
PAGE TITLE			
AUDIO: CALTRA CODEC (2/2)			
Apple Inc.	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE	36 OF 49
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		SHEET	24 OF 59
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			

SPEAKER AMPLIFIER

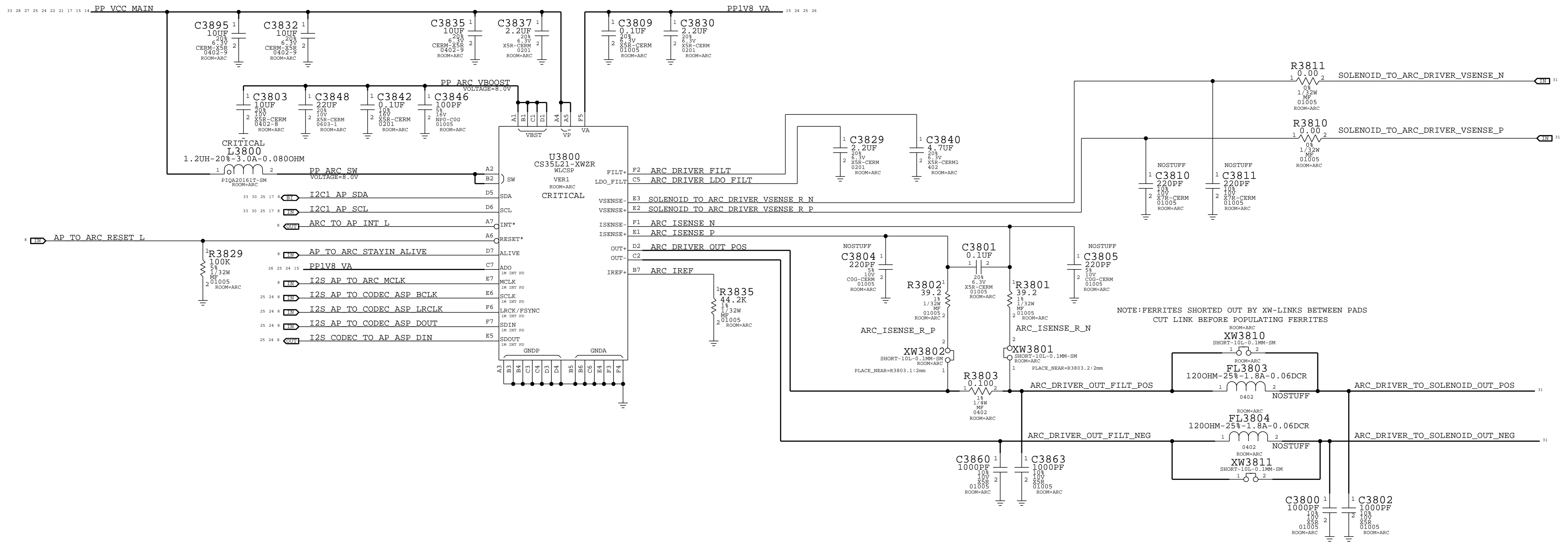
APN: 338S1285



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
AUDIO: SPEAKER DRIVER			
 Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
		PAGE	37 OF 49
		SHEET	25 OF 59

ARC DRIVER

APN: 338S1285

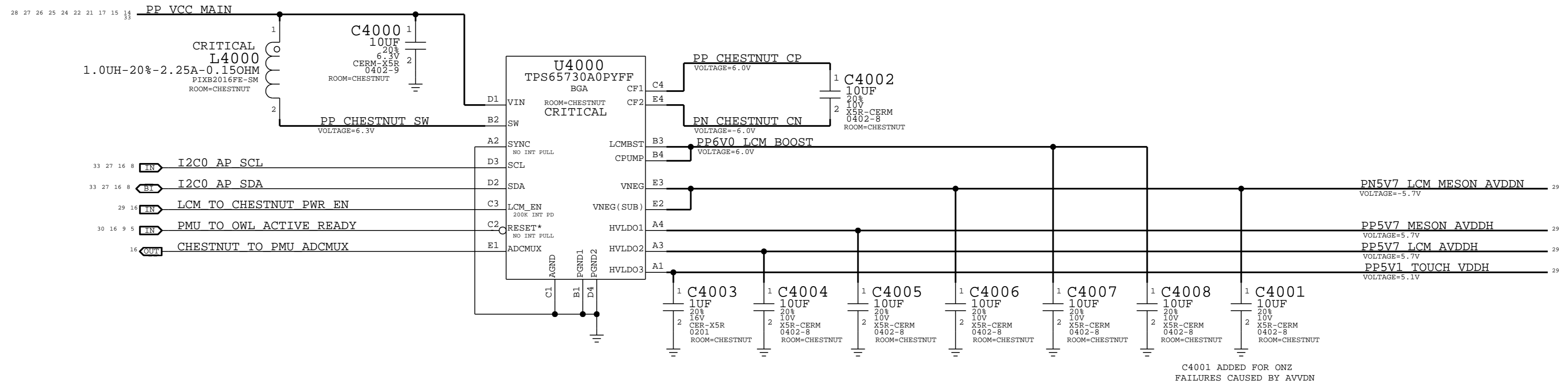


PAGE TITLE		AUDIO:ARC DRIVER	
Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
		PAGE	38 OF 49
		SHEET	26 OF 59

DISPLAY & TOUCH - POWER SUPPLIES

CHESTNUT DISPLAY PMU

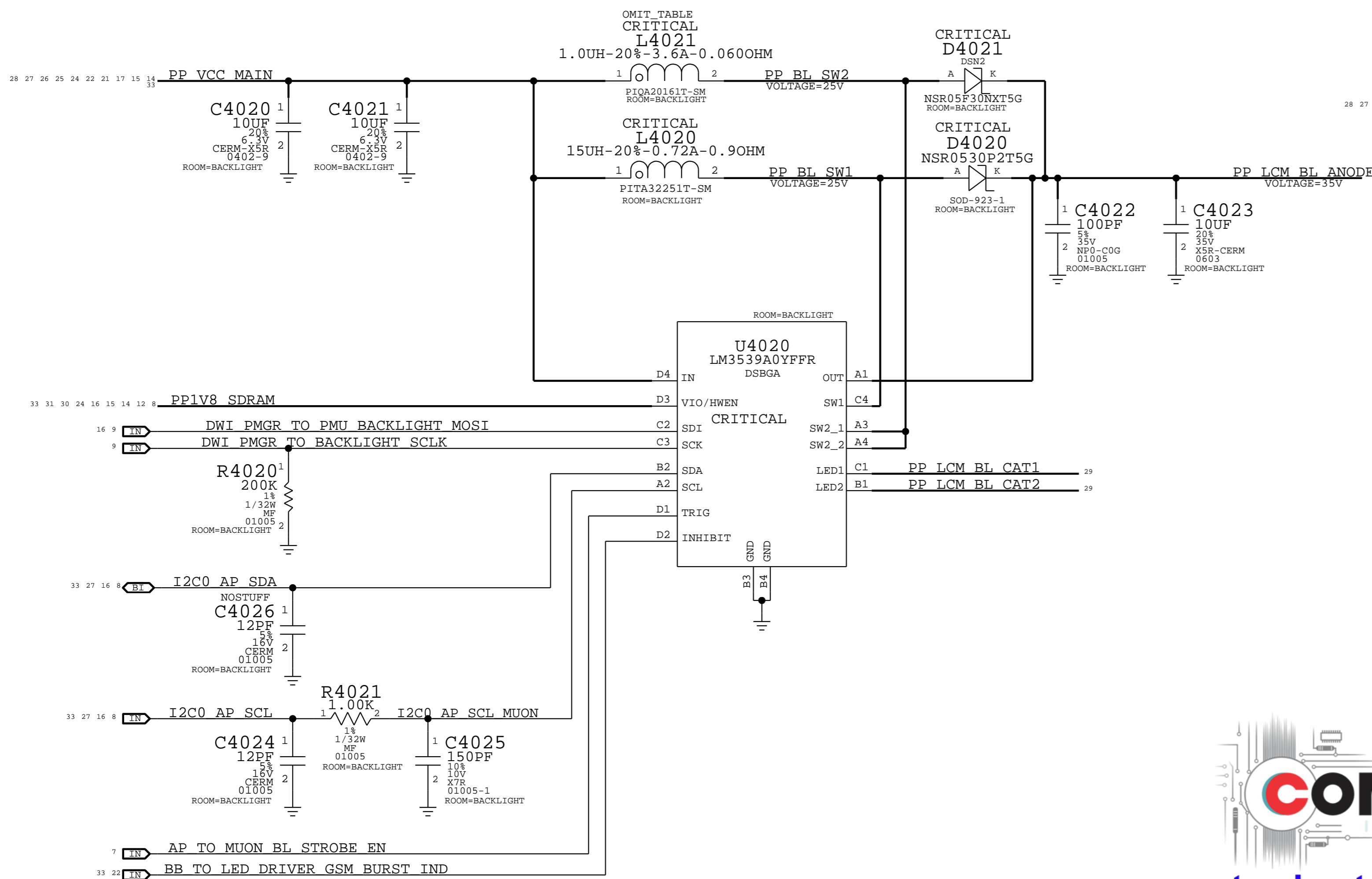
APN: 338S1172



C4001 ADDED FOR ONZ FAILURES CAUSED BY AVVDN

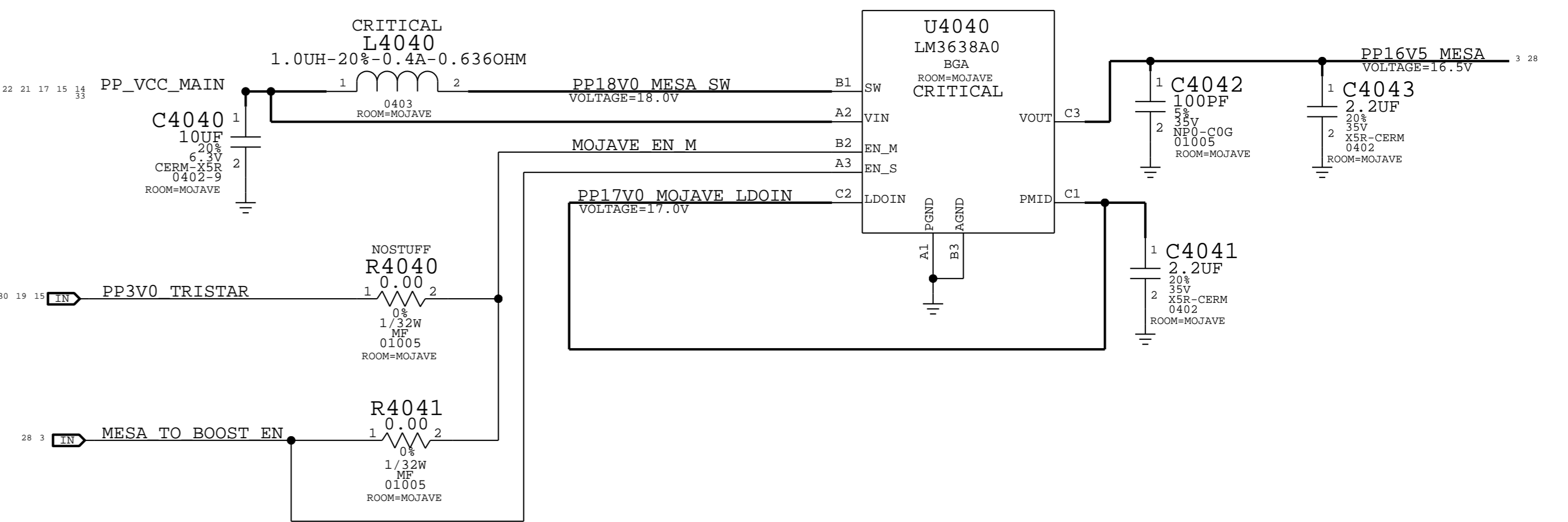
LED BACKLIGHT DRIVER

APN: 353S00407



MOJAVE MESA BOOST

APN: 353S00671



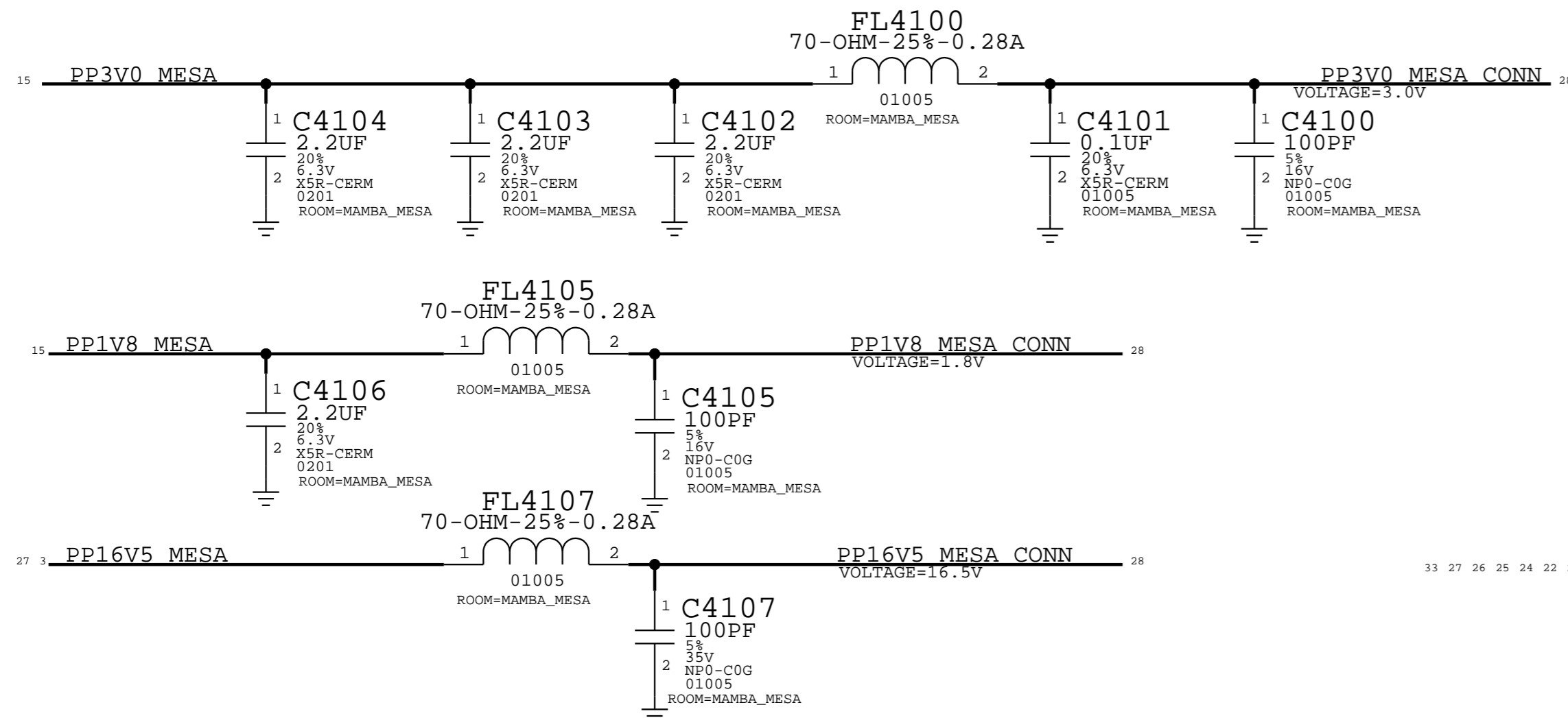
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
DISPLAY: POWER			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
		BRANCH	
		PAGE	40 OF 49
		SHEET	27 OF 59
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

MAMBA & MESA (M&M) FLEX

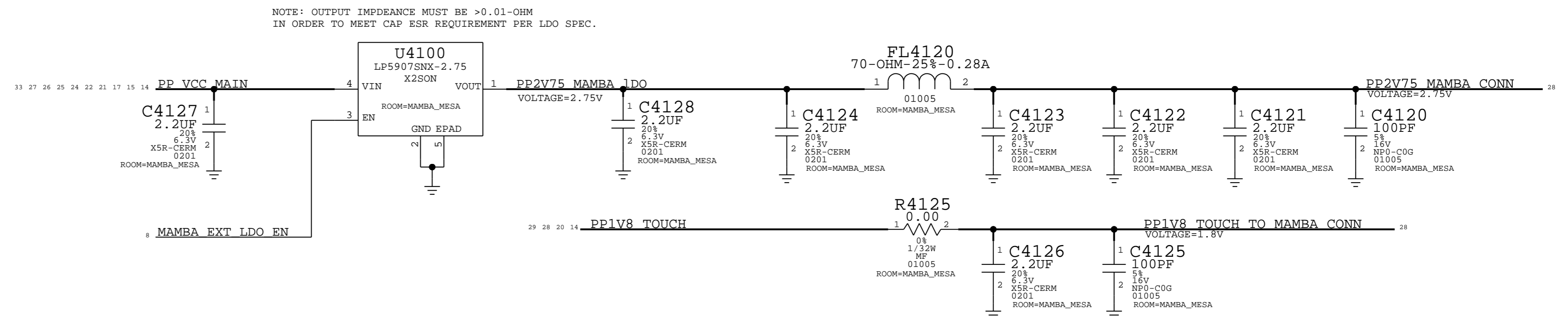
MAMBA & MESA CONNECTOR



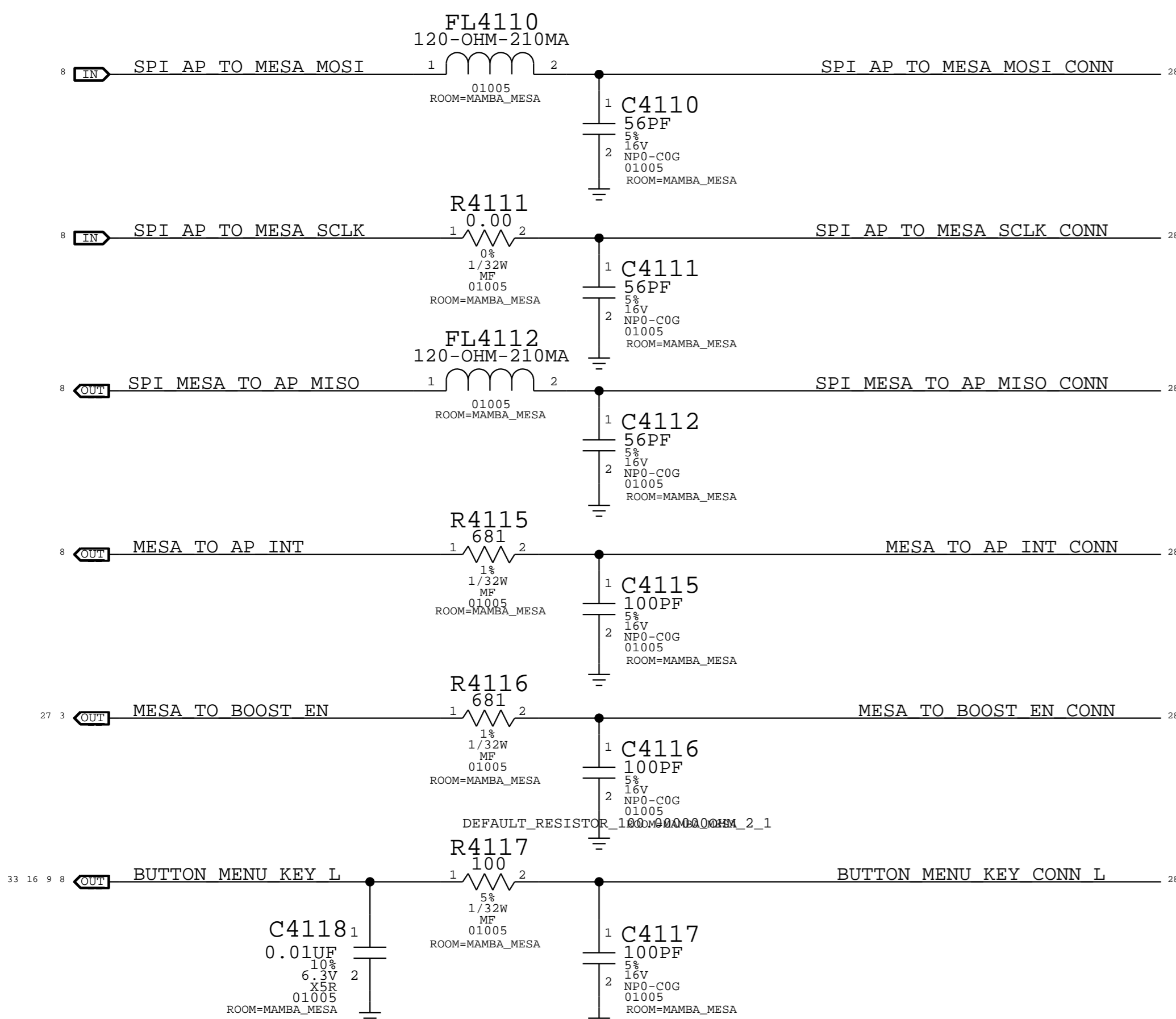
MESA POWER



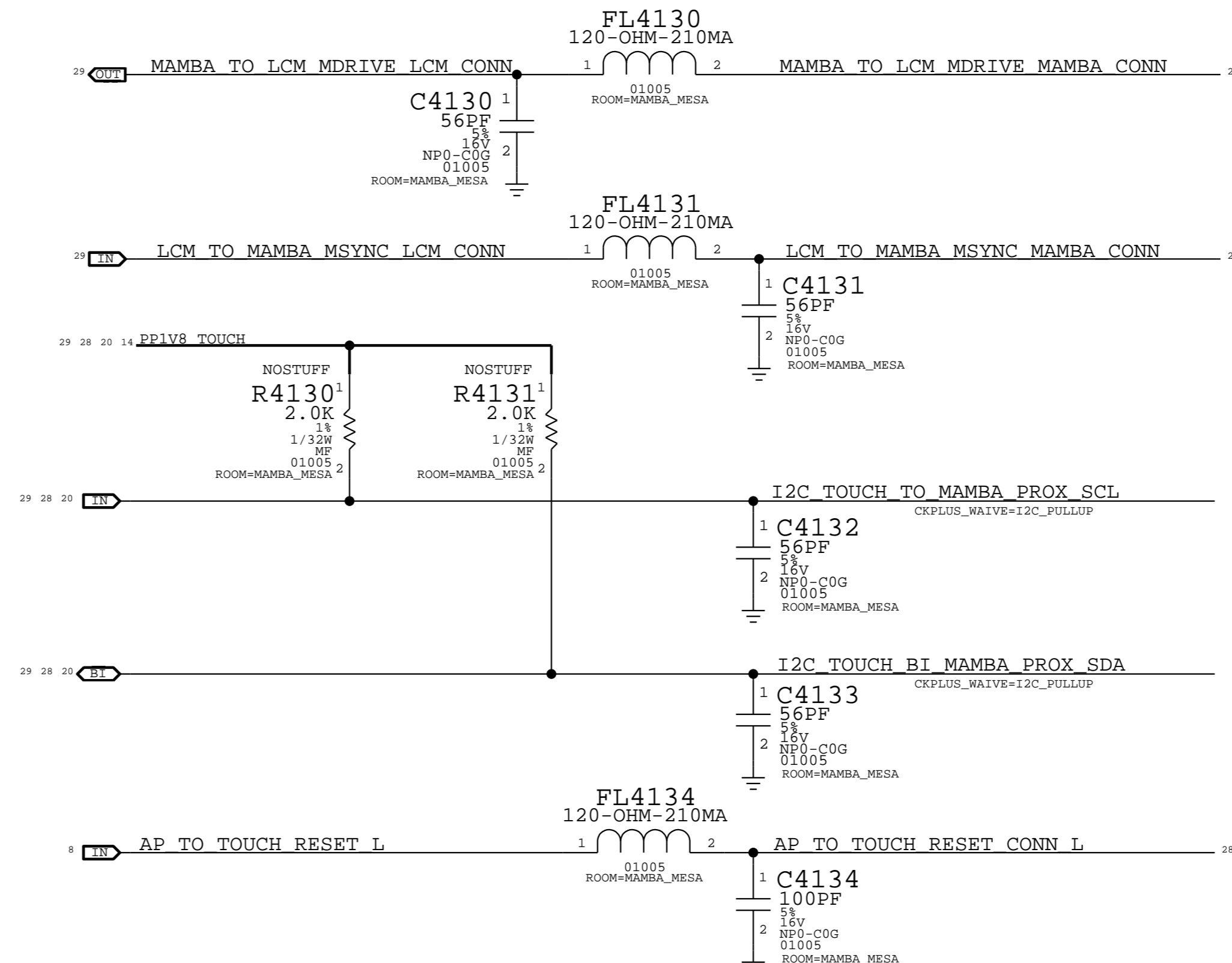
MAMBA POWER



MESA DIGITAL I/O



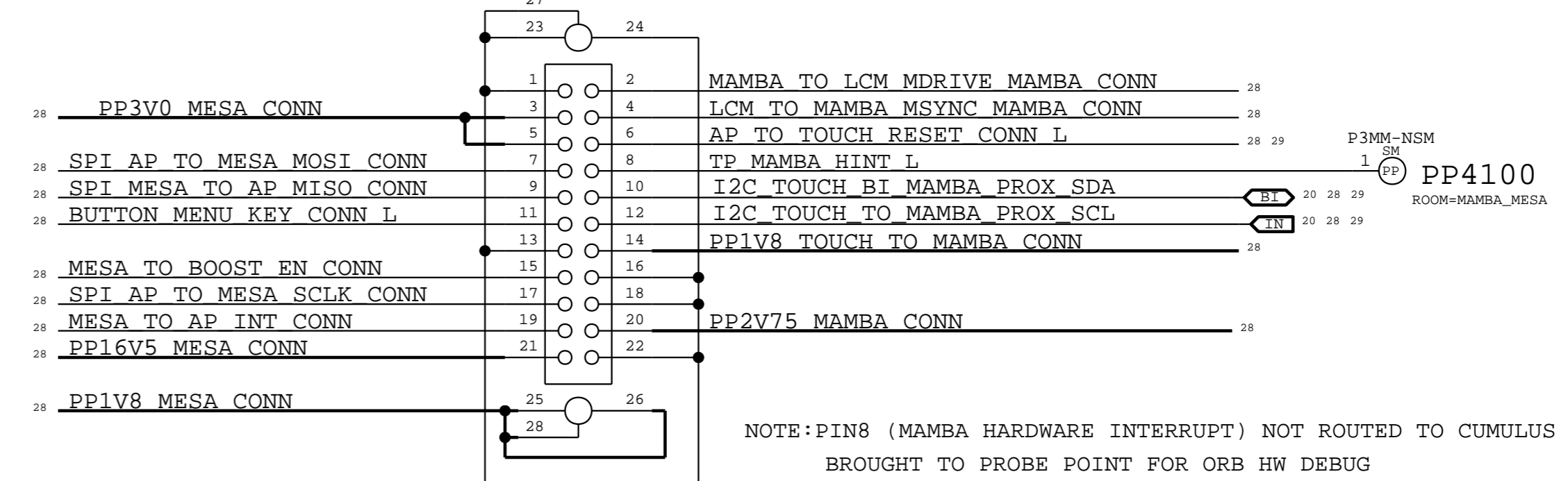
MAMBA DIGITAL I/O



NOTE: TOUCH I2C PULL-UPS TO PP1V8_TOUCH INSIDE KEPLER
ADDING R4130, R4131 AS OPTION FOR TWEAKING VALUES.

THIS ONE ON MLB ---> 516S00056 (RCPT)
516S00057 (PLUG)

CRITICAL
ROOM=MAMBA_MESA
J4100
BB35CRA223A
F-ST-SM



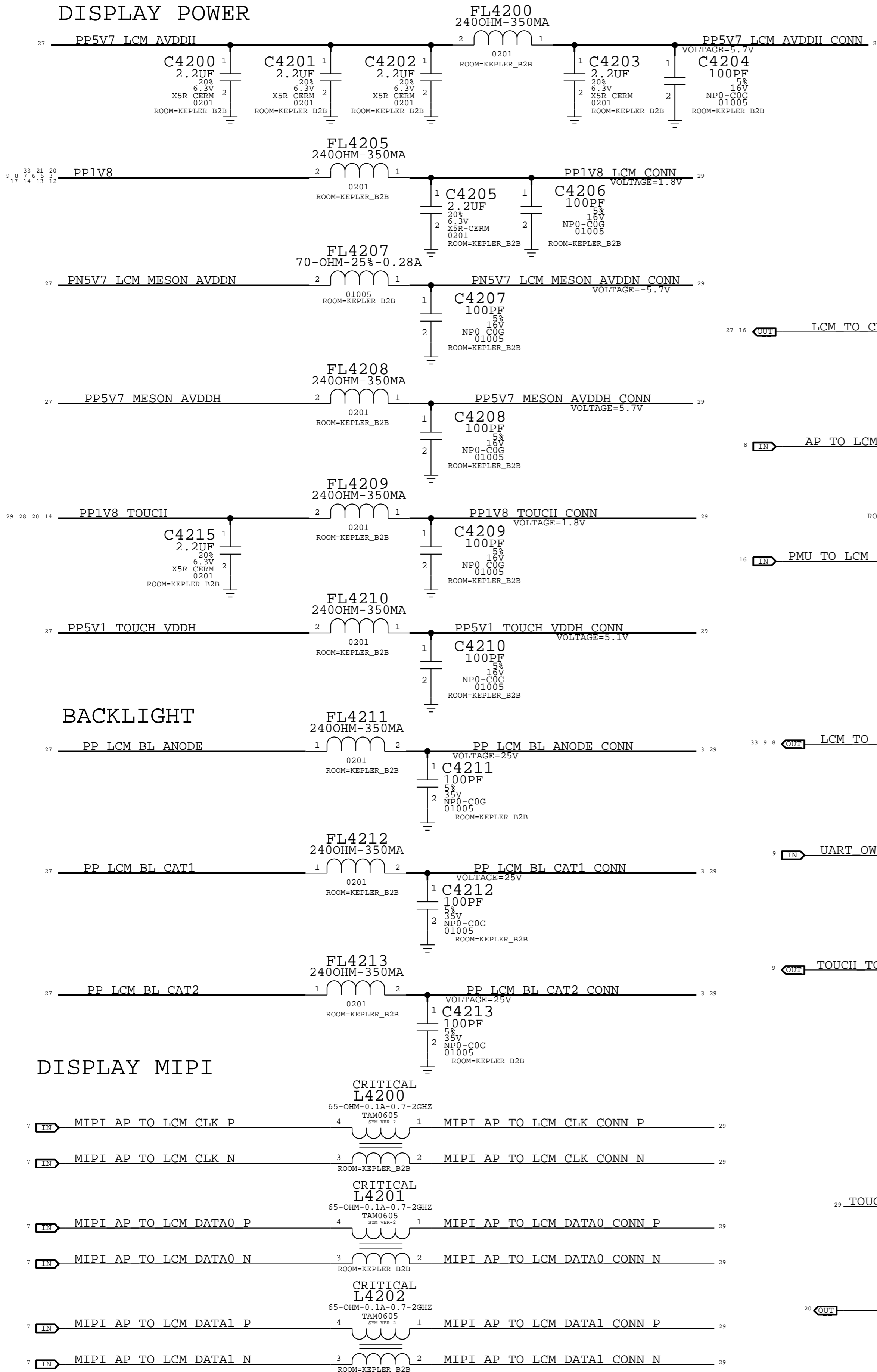
NOTE: PIN8 (MAMBA HARDWARE INTERRUPT) NOT ROUTED TO CUMULUS
BROUGHT TO PROBE POINT FOR ORB HW DEBUG

SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE TOUCH:ORB & MESA B2B			
DRAWING NUMBER 051-1902		SIZE D	
REVISION A.0.0		BRANCH	
PAGE 41 OF 49		SHEET 28 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

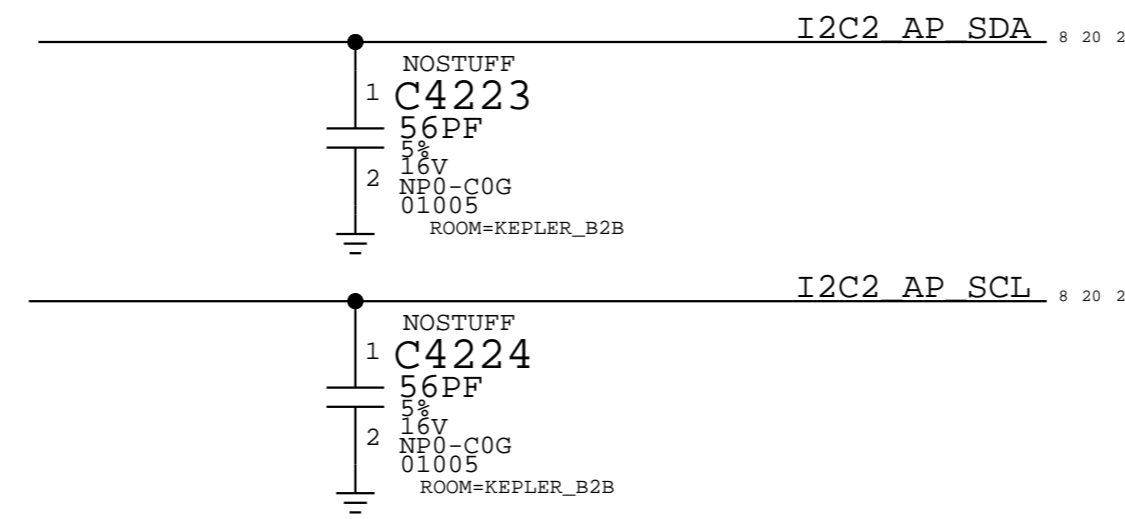
DISPLAY & TOUCH FLEX

DISPLAY CONNECTOR

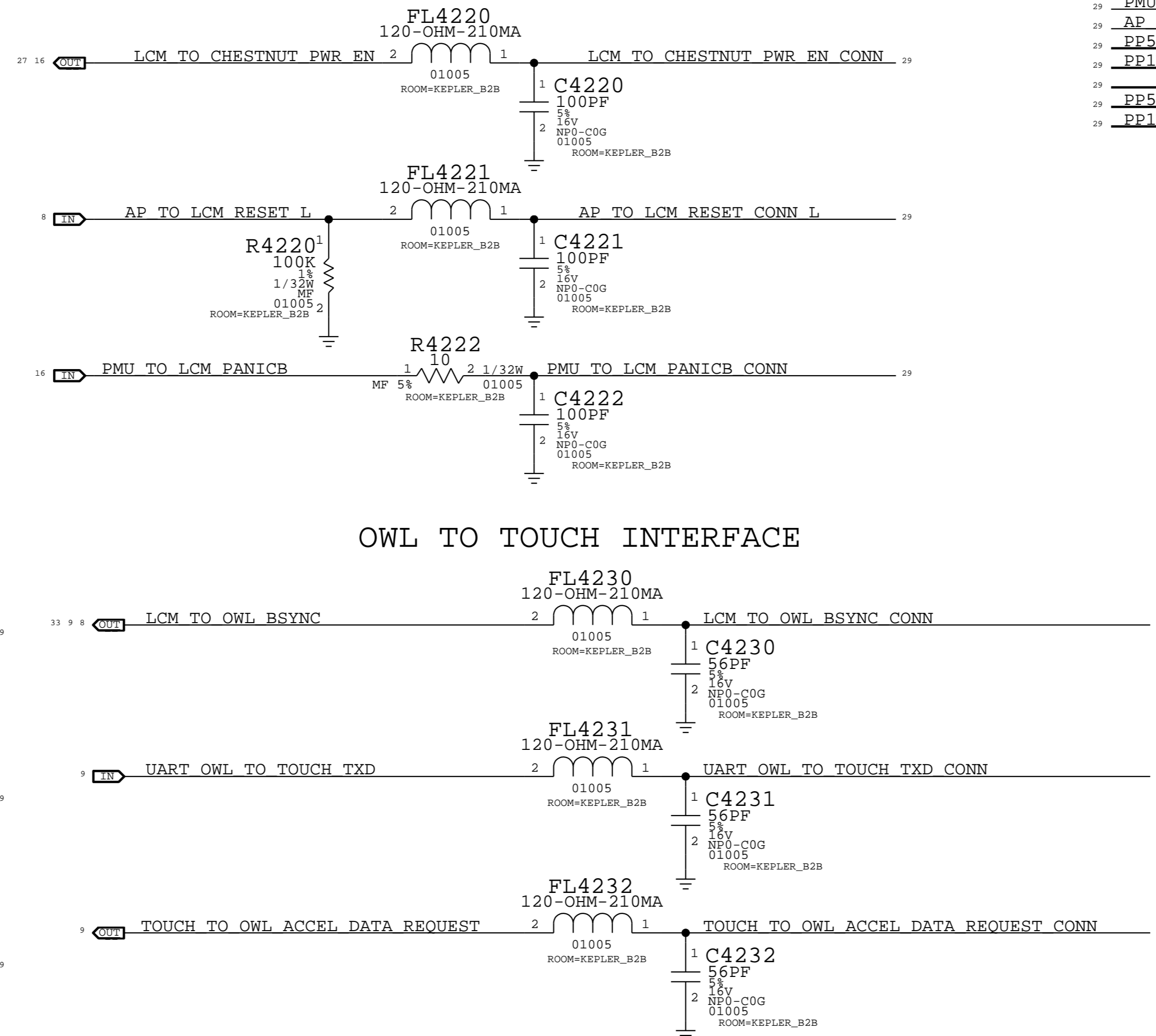
DISPLAY POWER



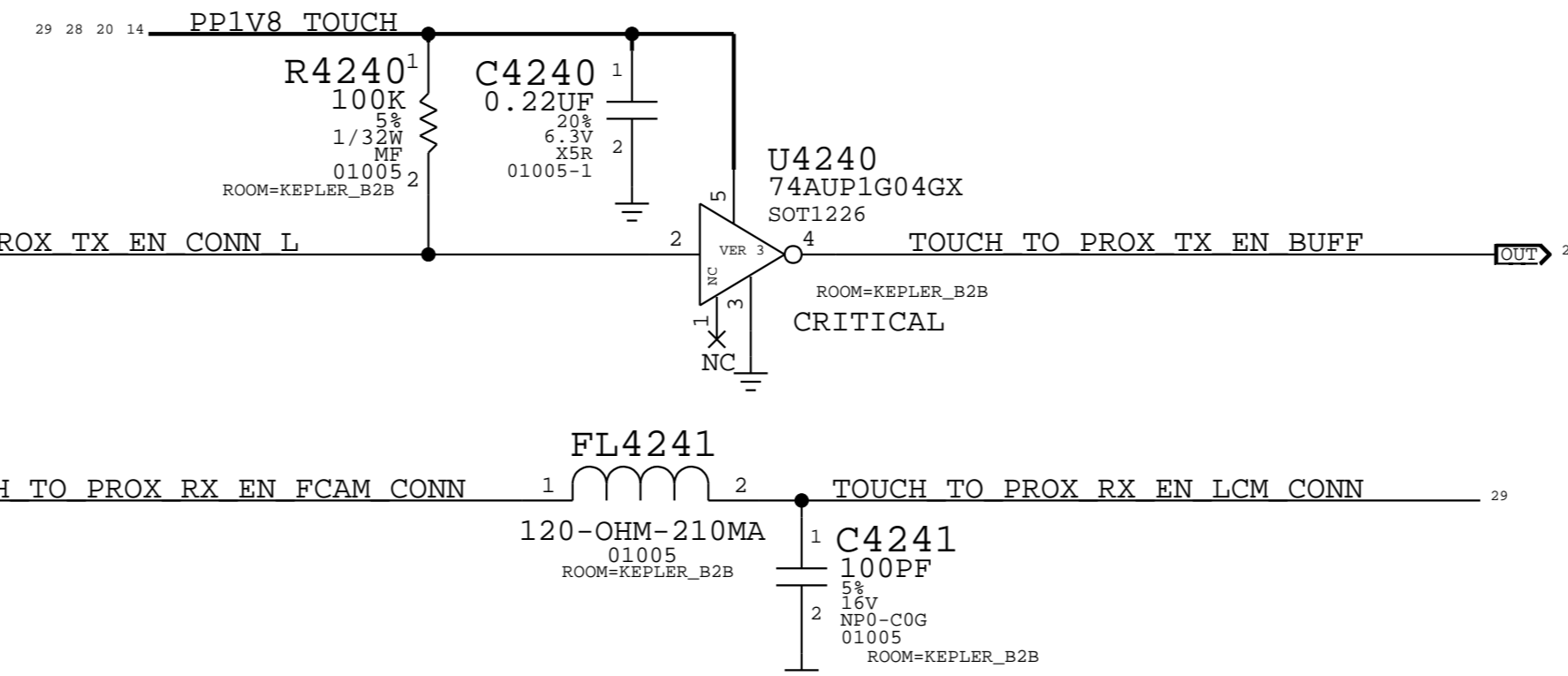
DISPLAY EEPROM I2C



DISPLAY CONTROL SIGNALS

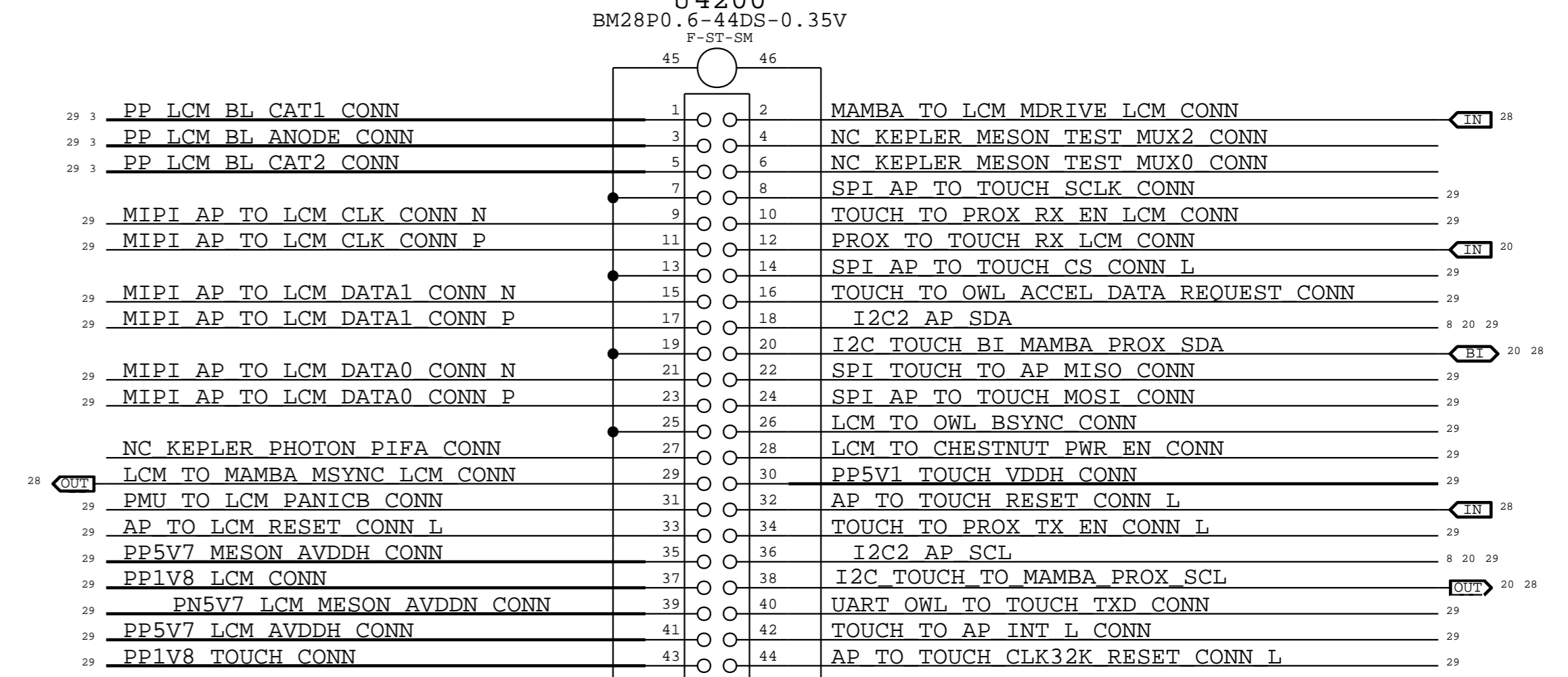


PROX TO TOUCH INTERFACE

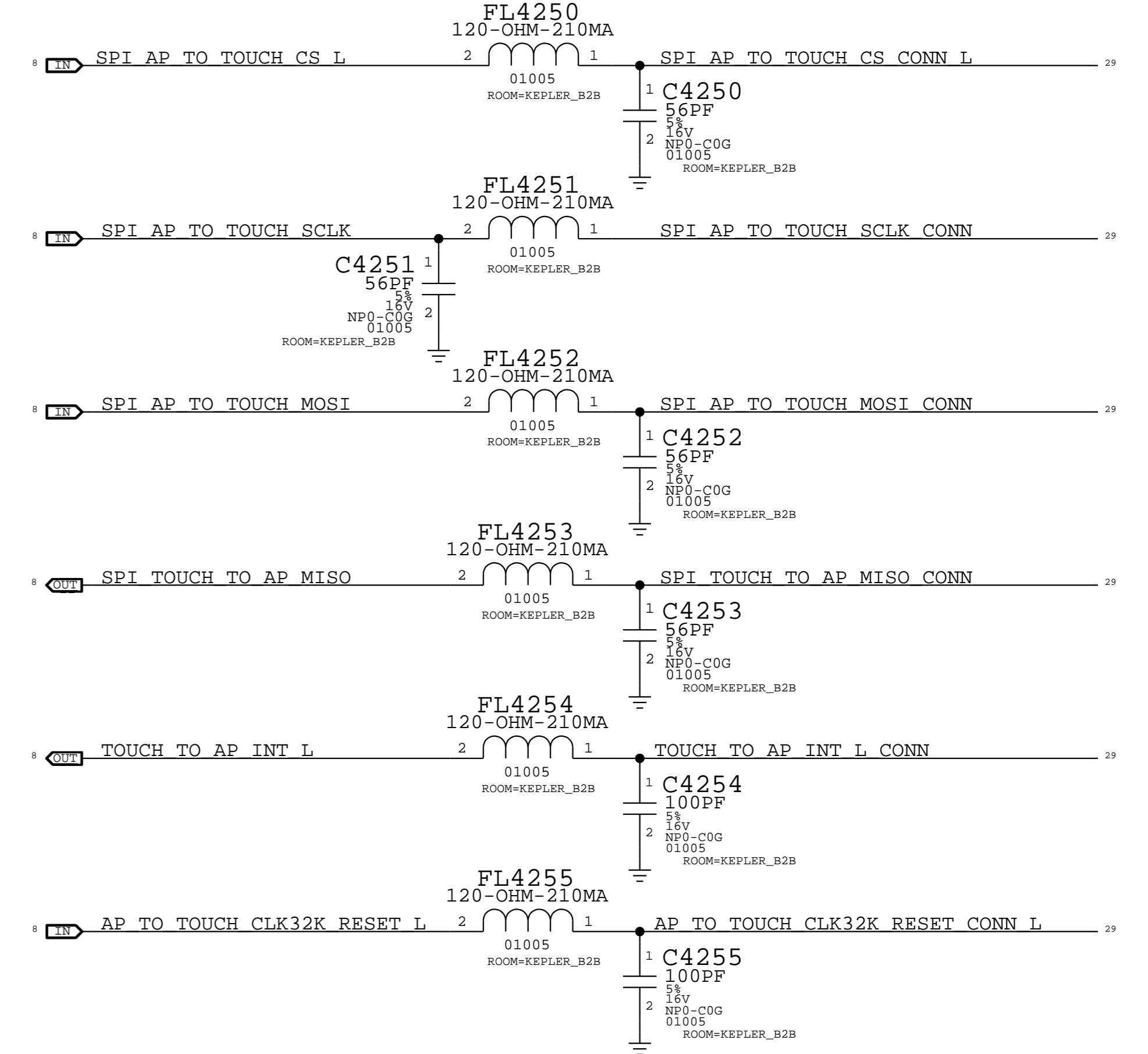


THIS ONE ON MLB ---> 516S00038 (RCPT)
516S00037 (PLUG)

CRITICAL



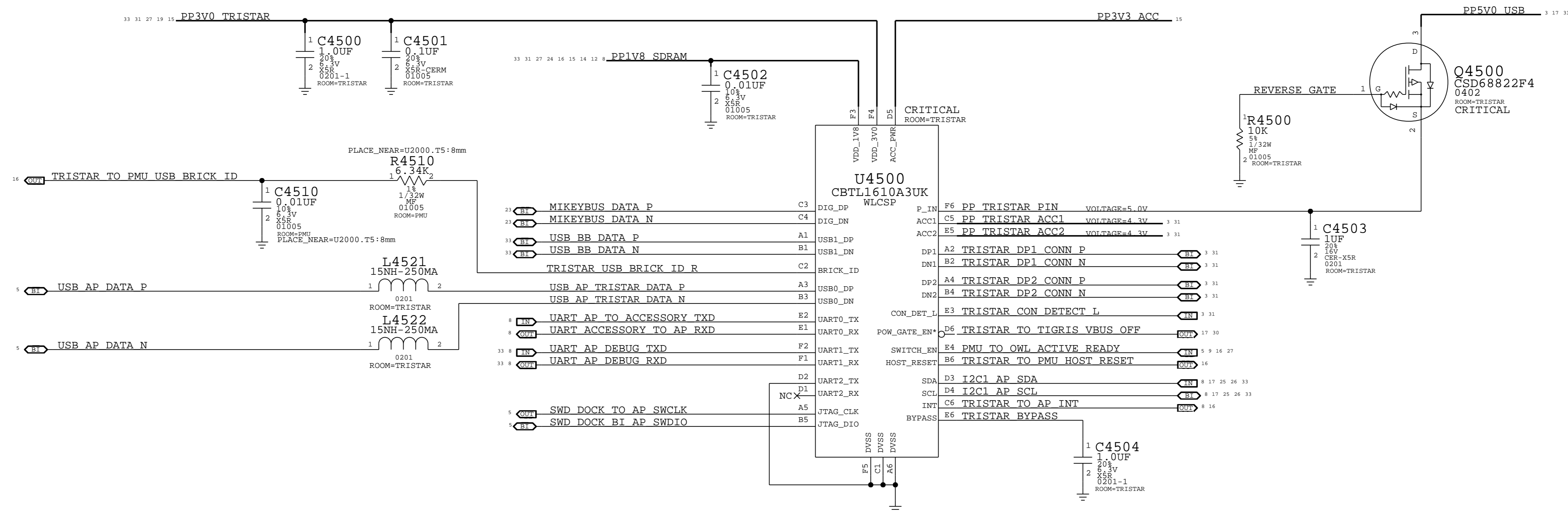
AP TO TOUCH INTERFACE



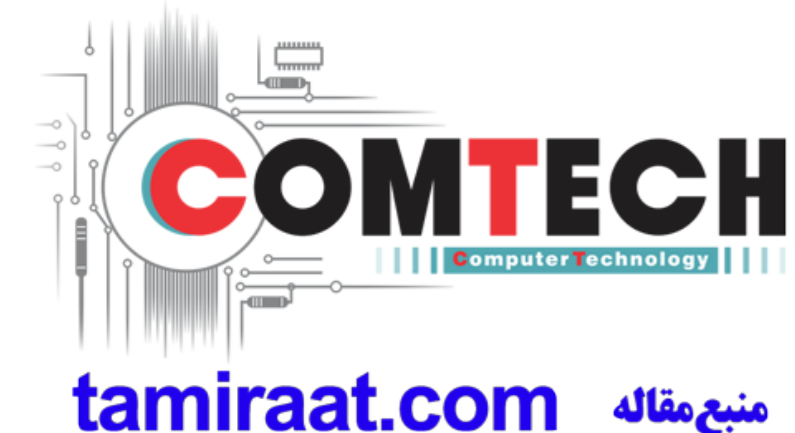
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
DISPLAY:KEPLER B2B			
Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE	42 OF 49
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		SHEET	29 OF 59
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			

TRISTAR 2 (A3)

APN: 343S0695



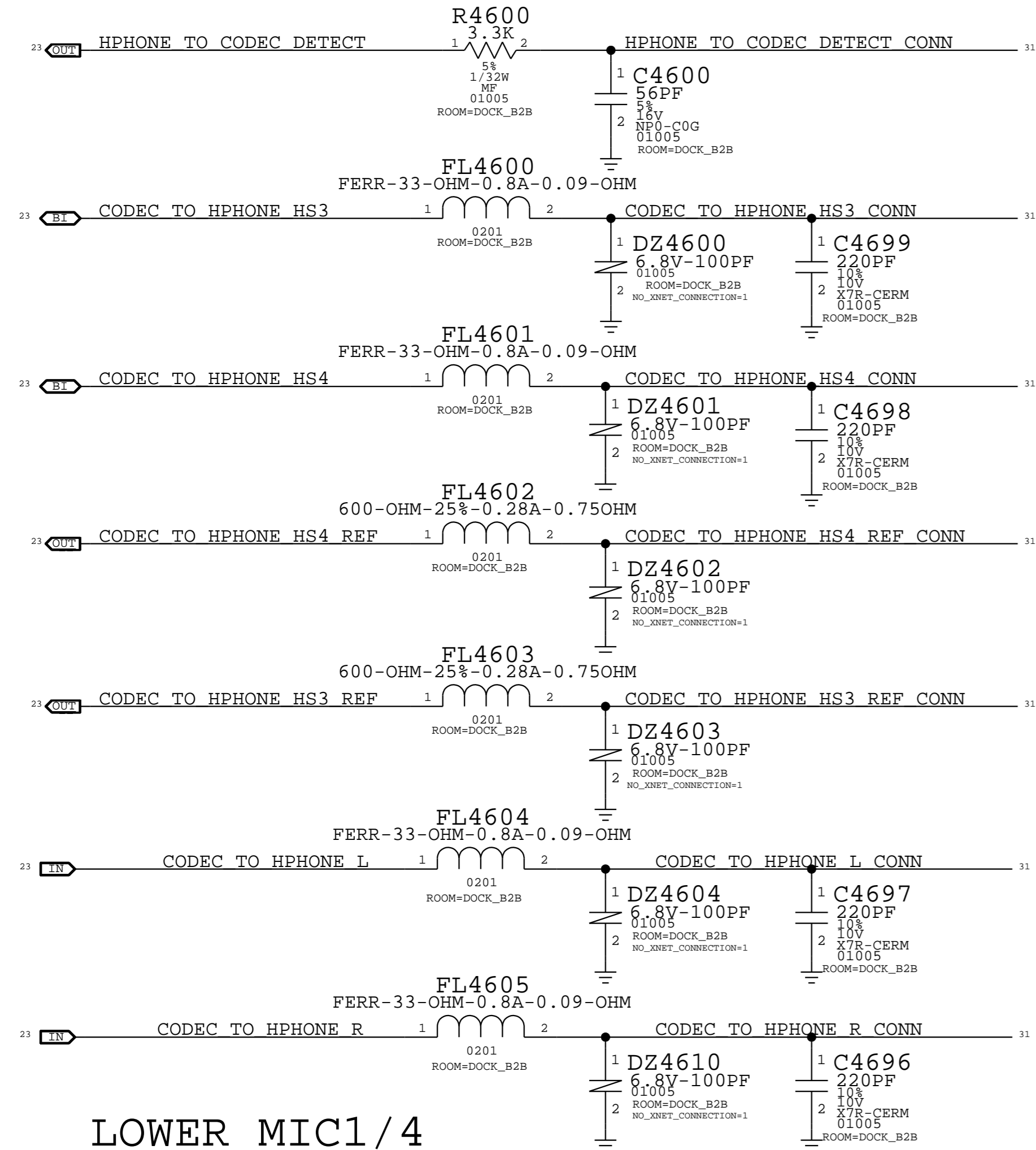
ROOM=TRISTAR
P3MM-NSM
30 17 TRISTAR TO TIGRIS VBUS OFF 1 PP4500



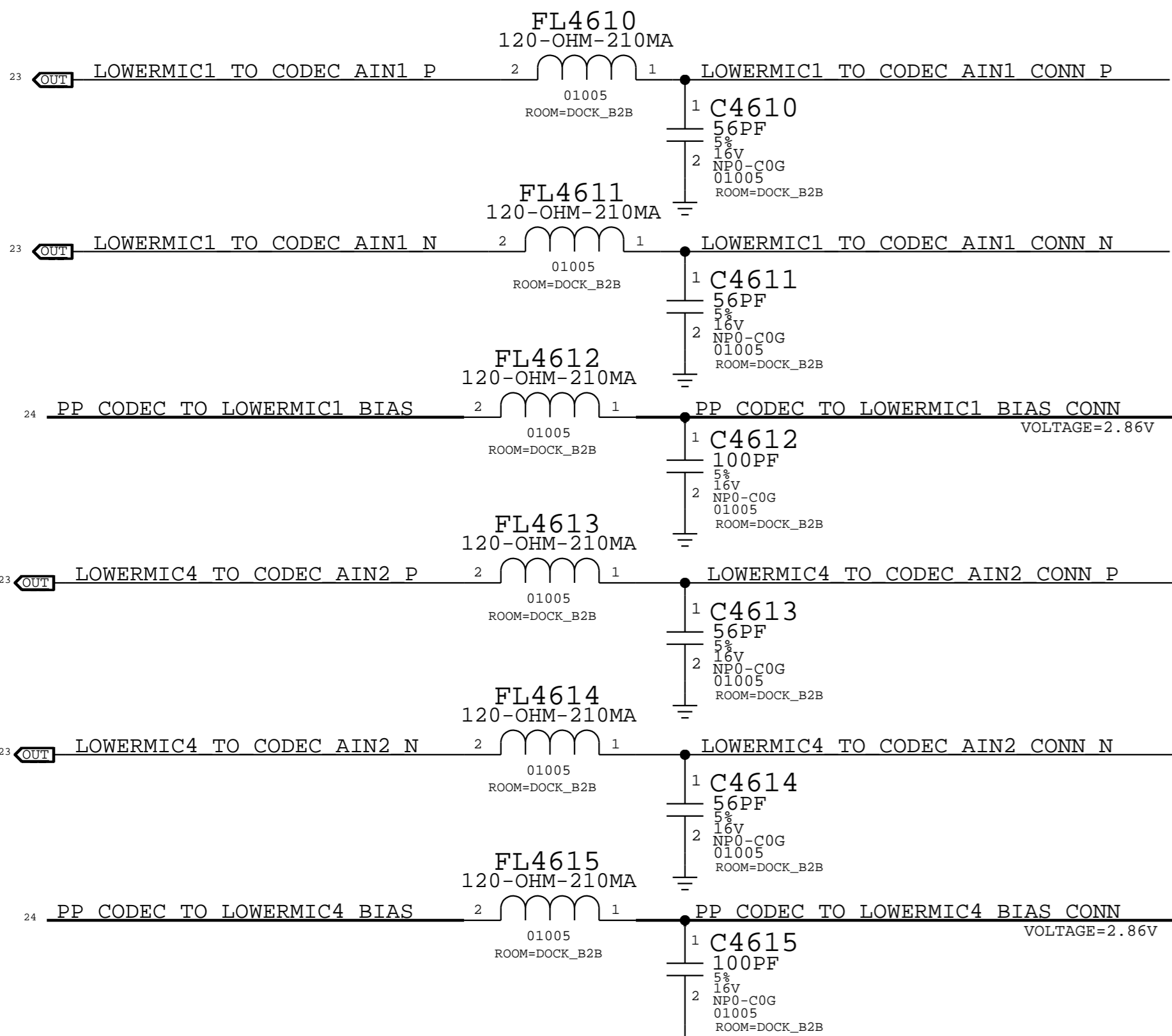
SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
I/O:TRISTAR 2			
	DRAWING NUMBER	051-1902	
	REVISION	A.0.0	
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		PAGE	45 OF 49
		SHEET	30 OF 59

DOCK FLEX CONNECTOR

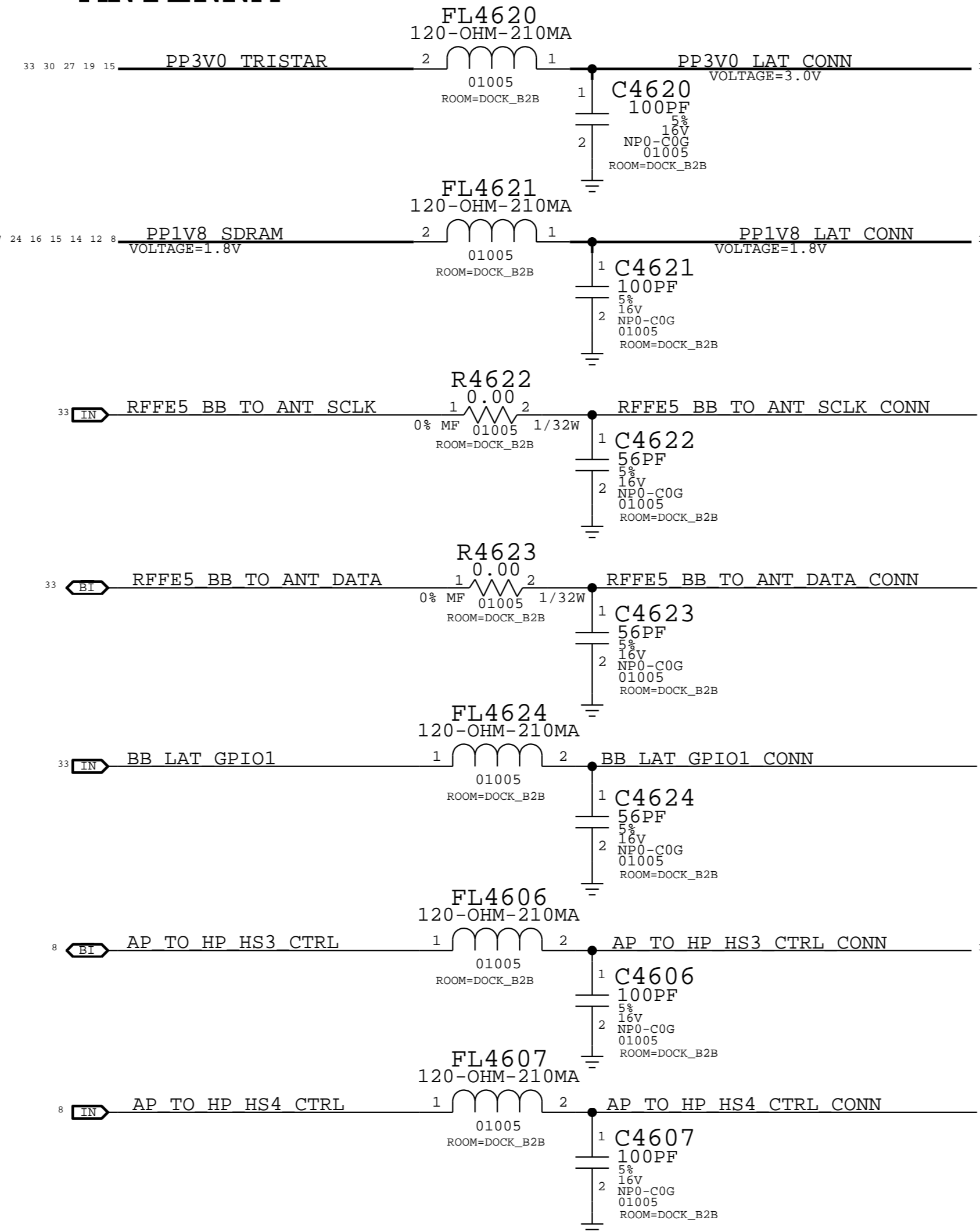
AUDIO JACK



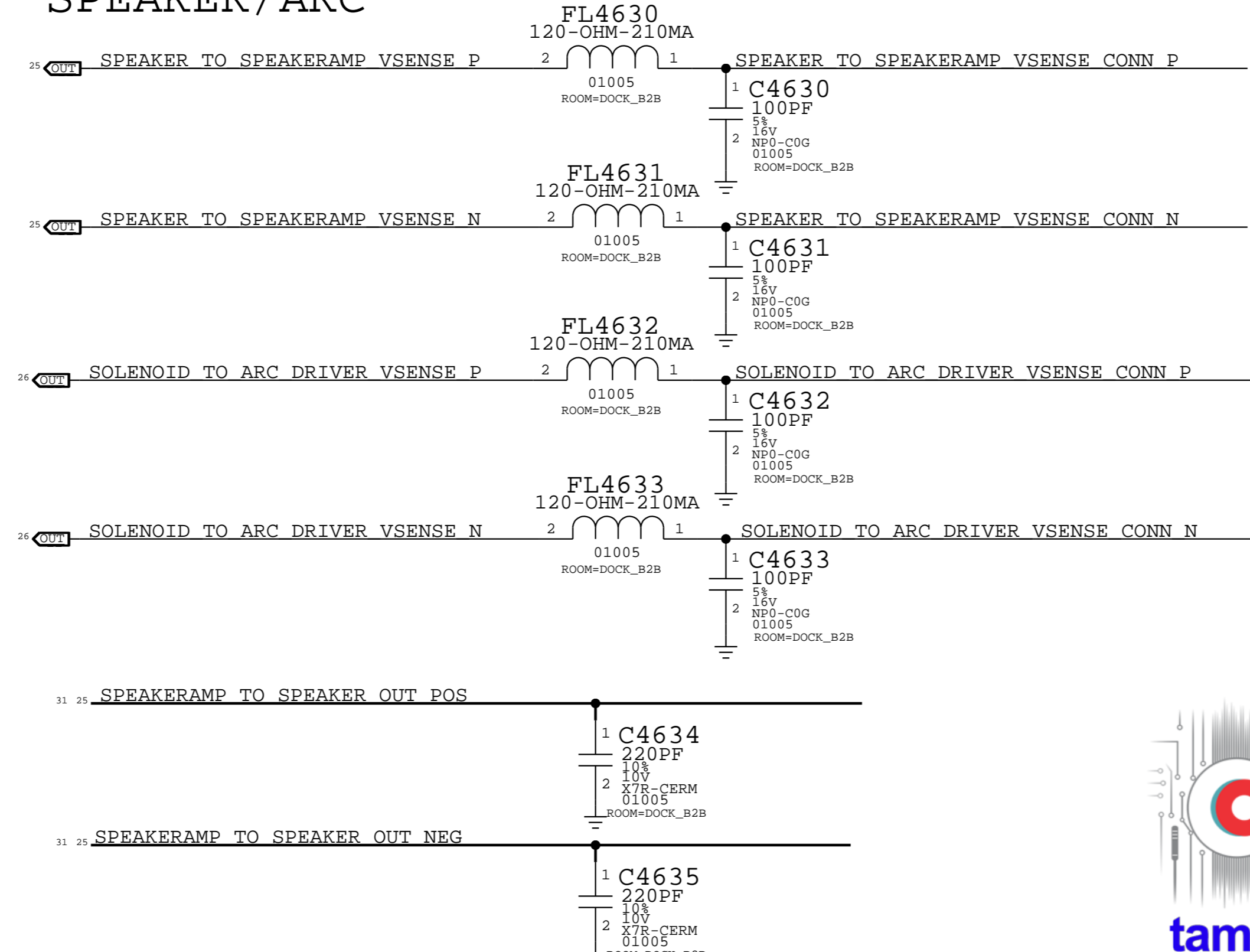
LOWER MIC1/4



ANTENNA

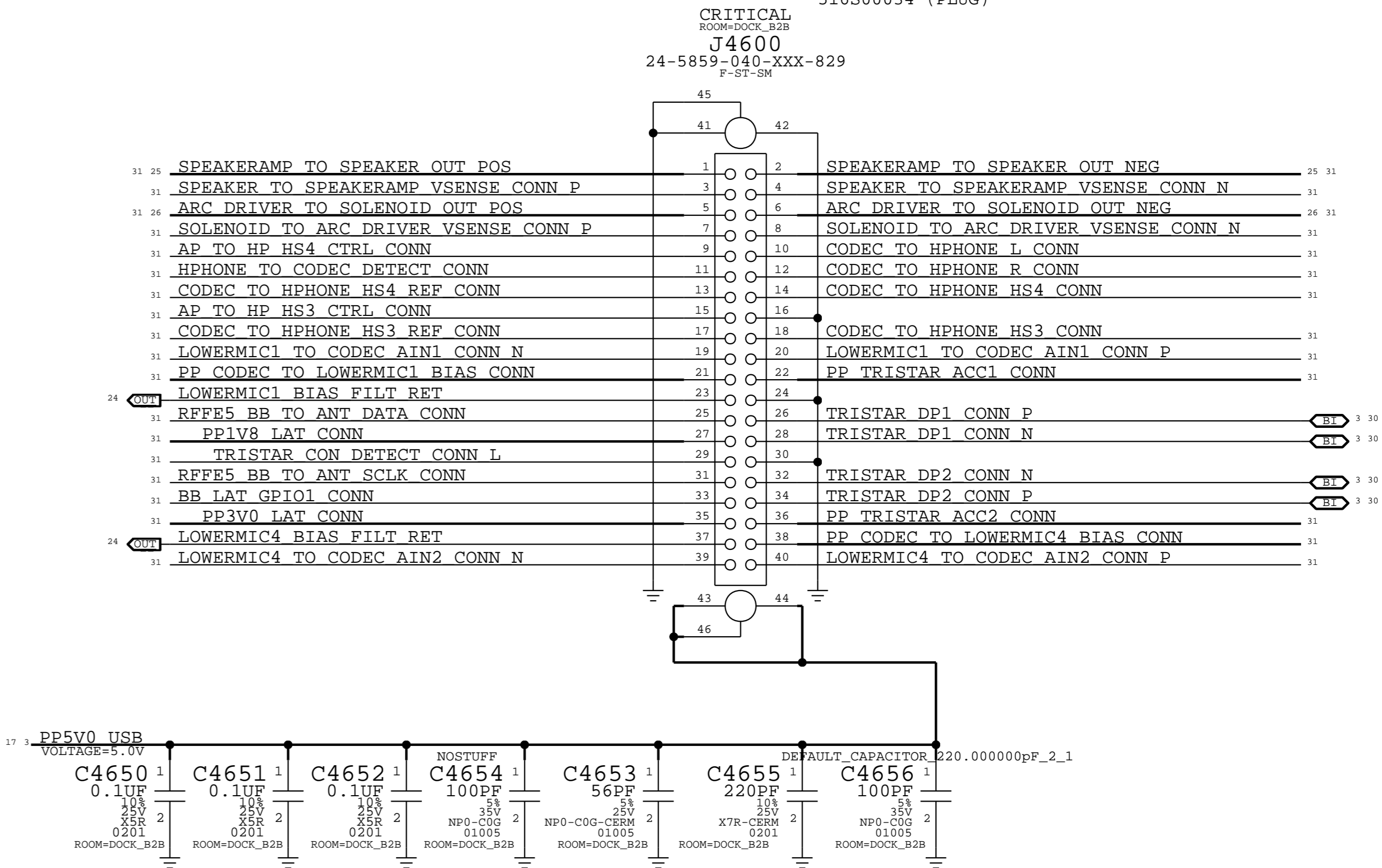


SPEAKER/ARC

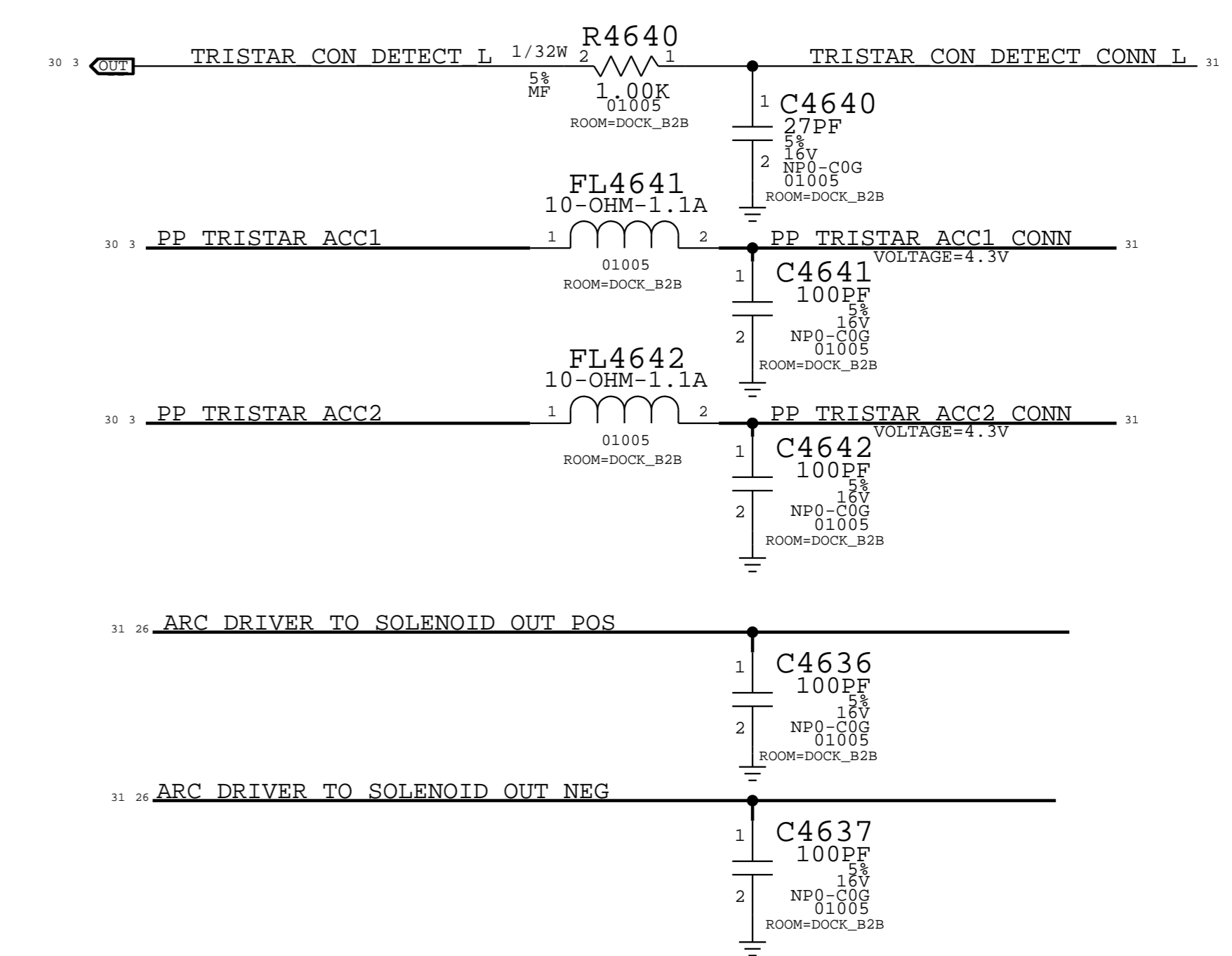


DOCK FLEX CONNECTOR

THIS ONE ON MLB ---> 516S00033 (RCPT)
516S00034 (PLUG)



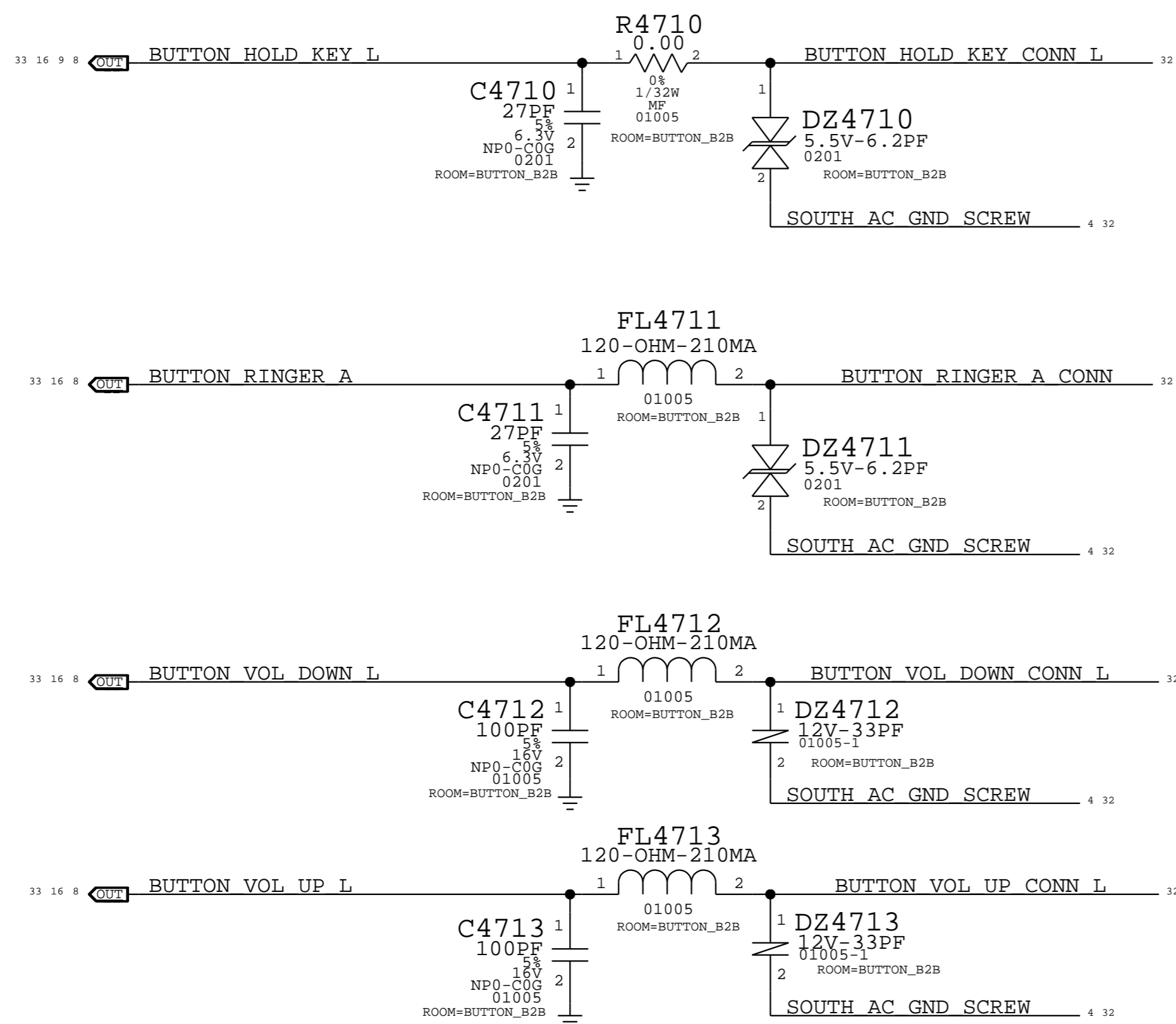
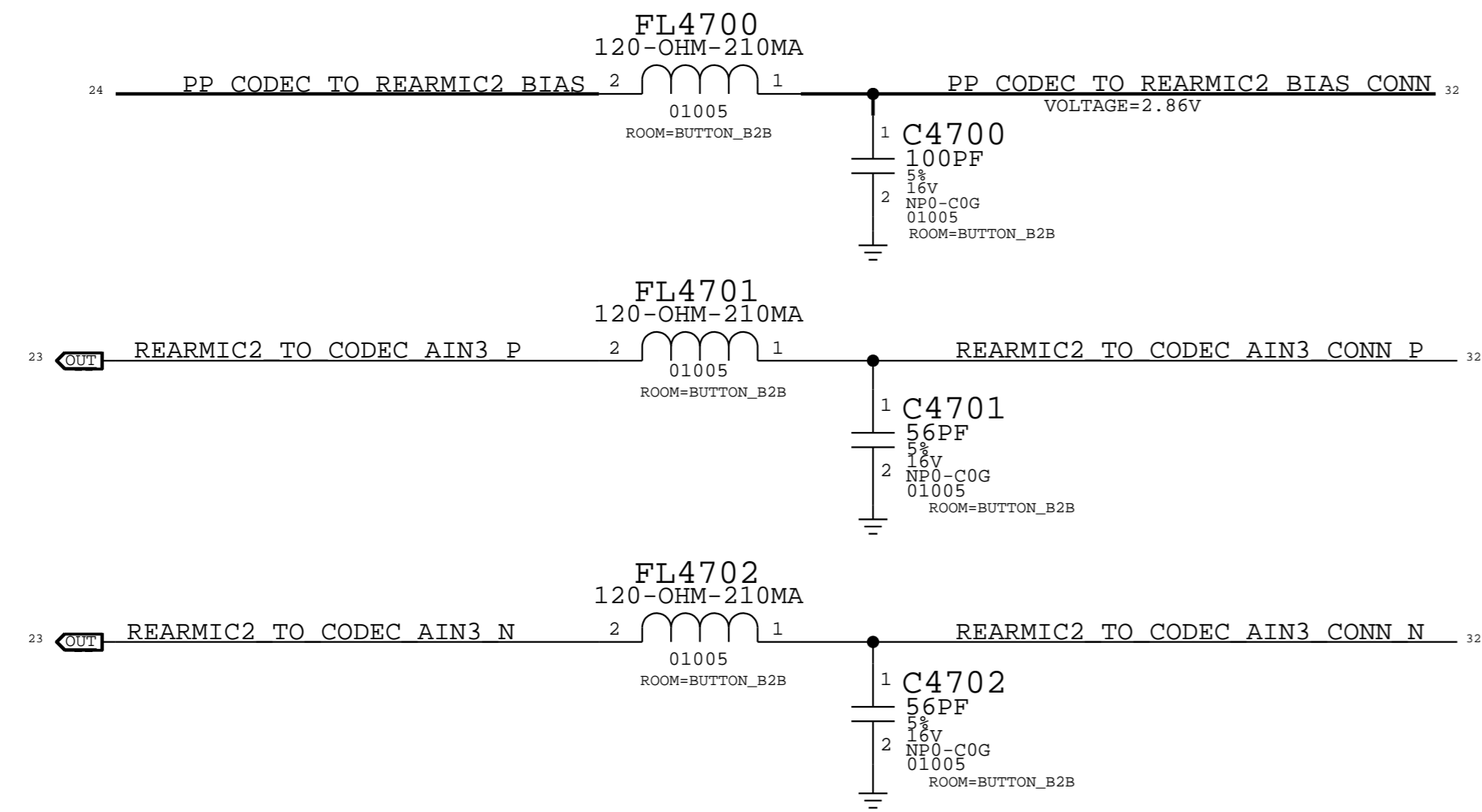
TRISTAR



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE I/O: DOCK FLEX B2B			
DRAWING NUMBER 051-1902		SIZE D	
REVISTION A.0.0		BRANCH	
PAGE 46 OF 49		SHEET 31 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

BUTTON FLEX

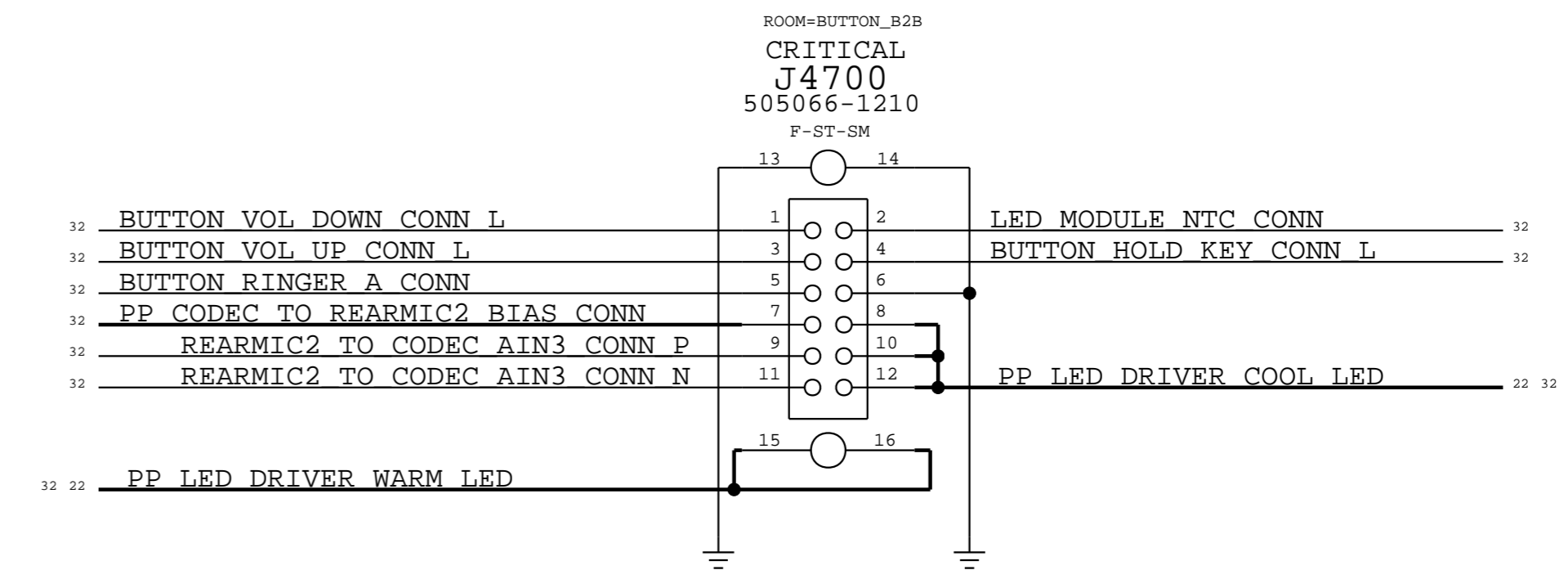
MIC2
ANC REF MIC



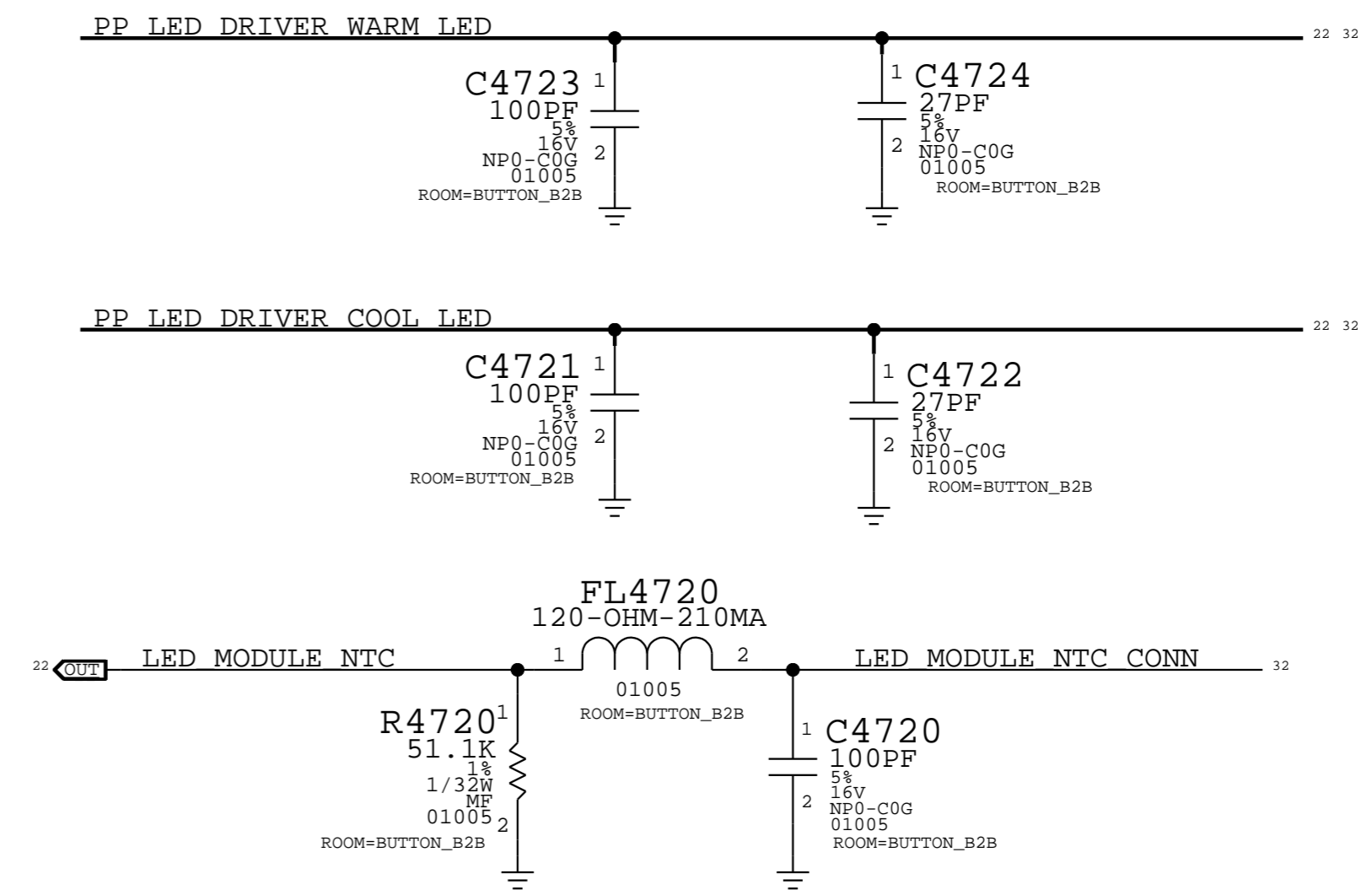
BUTTONS:
HOLD
RINGER
VOL UP/DOWN

BUTTON FLEX CONNECTOR

THIS ONE ON MLB ---> 516S00058 (RCPT)
516S00059 (PLUG)

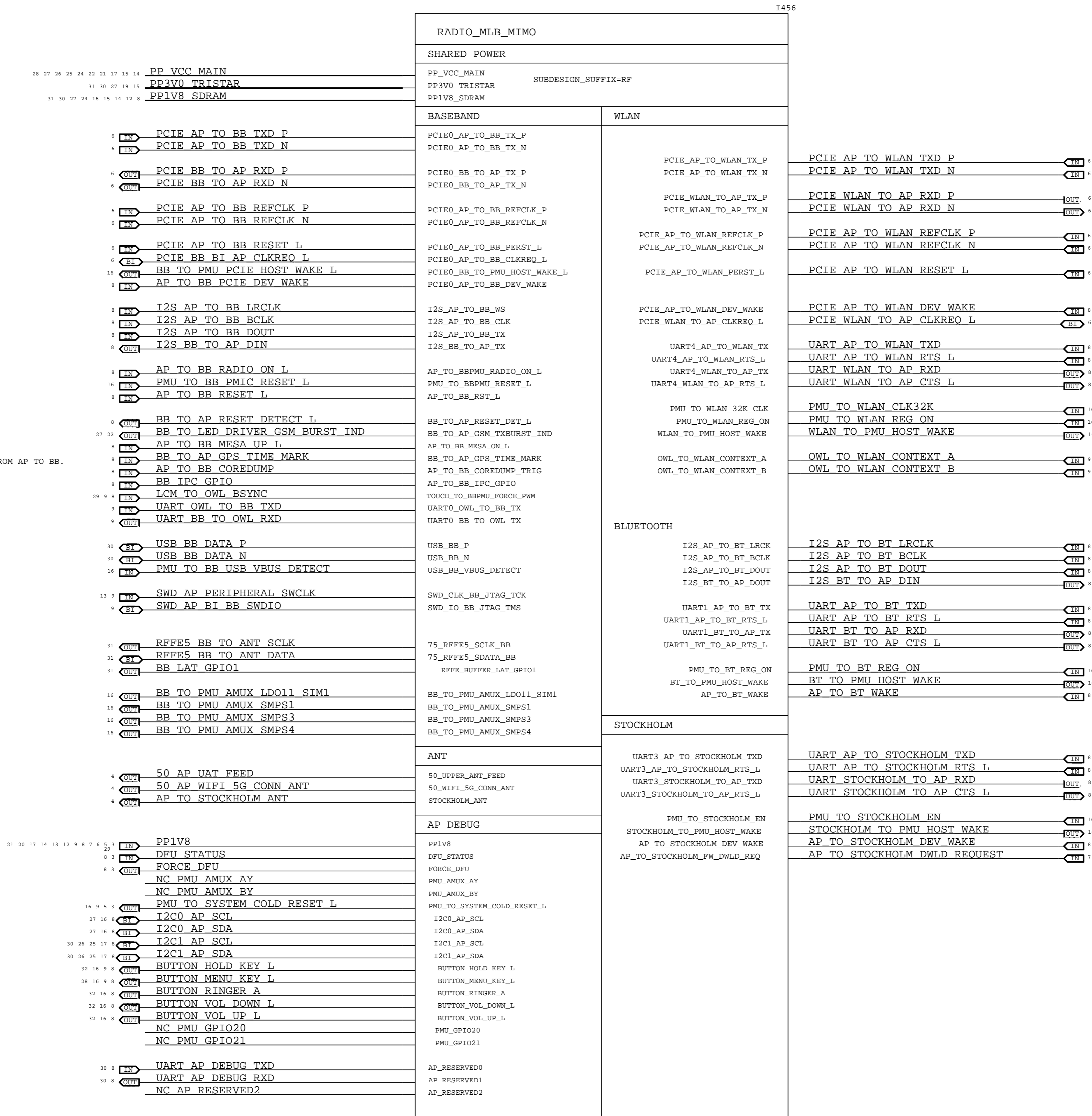


STROBE:
WARM LED
COOL LED
MODULE NTC



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE I/O:BUTTON FLEX B2B			
DRAWING NUMBER 051-1902		SIZE D	
REVISION A.0.0		BRANCH	
PAGE 47 OF 49		SHEET 32 OF 59	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED			

BASEBAND, WLAN, BT & STOCKHOLM



PAGE TITLE BASEBAND:RADIO SYMBOL		
Apple Inc.	DRAWING NUMBER 051-1902	SIZE D
REVISION A.0.0		
BRANCH		
PAGE 49 OF 49		
SHEET 33 OF 59		
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	0004536627	PRODUCTION RELEASED		2015-07-21

N71 RADIO_MLB_MIMO - PVT

JULY 07, 2015

PDF PAGE	CSA PAGE	CONTENTS
2	2	ELNA & UAT ANT FEED
3	3	FE: ANT CONNECTORS AND UAT TUNER
4	30	DEBUG CONN & TEST POINTS
5	31	CELLULAR BASEBAND: POWER1
6	32	CELLULAR BASEBAND: POWER2
7	33	CELLULAR BASEBAND: CONTROL AND INTERFACES
8	34	CELLULAR BASEBAND: GPIOs
9	35	CELLULAR PMU: CONTROL AND CLOCKS
10	36	CELLULAR PMU: SWITCHERS AND LDOS
11	37	CELLULAR PMU: ET MODULATOR
12	38	CELLULAR TRANSCEIVER: POWER
13	39	CELLULAR TRANSCEIVER: PRX PORTS
14	40	CELLULAR TRANSCEIVER: DRX/GPS PORTS
15	41	CELLULAR TRANSCEIVER: TX PORTS
16	42	CELLULAR FRONT END: LB PAD
17	43	CELLULAR FRONT END: MB PAD
18	44	CELLULAR FRONT END: HB PAD
19	45	CELLULAR FRONT END: 2G PA
20	46	CELLULAR FRONT END: LB ASM
21	47	CELLULAR FRONT END: MB-HB ASM
22	48	CELLULAR FRONT END: DIVERSITY
23	49	SIM
24	50	WIFI/BT: WIFI/BT MODULE
25	51	STOCKHOLM

ROW/RF2 HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	ROW
152S1990	1	3.0NH, INDUCTOR	L4105_RF	RF2
131S0377	1	1.2PF, CAPACITOR	C4108_RF	RF2
152S2007	1	8.2NH, INDUCTOR	L4401_RF	ROW
131S0426	1	22PF, CAPACITOR	C4405_RF	ROW
152S2001	1	2.4NH, INDUCTOR	C4405_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4406_RF	RF2
152S2044	1	2.2NH, INDUCTOR	C4406_RF	ROW
152S2021	1	1.5NH, INDUCTOR	C4406_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4407_RF	ROW
152S2056	1	5.6NH, INDUCTOR	L4403_RF	ROW
131S0429	1	8.2PF, CAPACITOR	C4407_RF	ROW
152S2036	1	2.5NH, INDUCTOR	C4407_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4408_RF	RF2
152S00143	1	15NH, INDUCTOR	L4404_RF	ROW
131S0823	1	33PF, CAPACITOR	C4408_RF	ROW
152S2051	1	1.3NH, INDUCTOR	C4408_RF	RF2
152S2042	1	1.8NH, INDUCTOR	C4409_RF	RF2
117S0108	1	51 OHM, RESISTOR	L4410_RF	ROW
131S0363	1	0.6PF, CAPACITOR	L4410_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3910_RF	ROW
152S00026	1	3.5NH, INDUCTOR	L3910_RF	RF2
152S2039	1	3.8NH, INDUCTOR	L3911_RF	ROW
117S0201	1	0 OHM, RESISTOR	L3911_RF	RF2
131S0279	1	1.3PF, CAPACITOR	L3919_RF	ROW
152S2045	1	3.0NH, INDUCTOR	L3919_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3912_RF	RF2
131S0599	1	1.5PF, CAPACITOR	C3922_RF	RF2
131S0630	1	27PF, CAPACITOR	C3911_RF	RF2

LB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	ROW
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	RF2
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	RFC
353S00541	1	IC, PWR AMP, LB_PAD, PT	ULBPA_RF	DARWIN

MB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	ROW
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	RF2
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	RFC
353S00477	1	IC, PWR AMP, MB_PAD, PT	UMBPA_RF	DARWIN

RFC HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	RFC
152S2007	1	8.2NH, INDUCTOR	L4401_RF	RFC
131S0426	1	22PF, CAPACITOR	C4405_RF	RFC
152S2044	1	2.2NH, INDUCTOR	C4406_RF	RFC
131S0631	1	0.3PF, CAPACITOR	L4407_RF	RFC
152S2056	1	5.6NH, INDUCTOR	L4403_RF	RFC
131S0429	1	8.2PF, CAPACITOR	C4407_RF	RFC
152S00143	1	15NH, INDUCTOR	L4404_RF	RFC
131S0823	1	33PF, CAPACITOR	C4408_RF	RFC
117S0108	1	51 OHM, RESISTOR	L4410_RF	RFC
152S00052	1	3.4NH, INDUCTOR	L3910_RF	RFC
152S2039	1	3.8NH, INDUCTOR	L3911_RF	RFC
131S0279	1	1.3PF, CAPACITOR	L3919_RF	RFC

DARWIN HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	DARWIN
152S2007	1	8.2NH, INDUCTOR	L4401_RF	DARWIN
131S0426	1	22PF, CAPACITOR	C4405_RF	DARWIN
152S2044	1	2.2NH, INDUCTOR	C4406_RF	DARWIN
131S0631	1	0.3PF, CAPACITOR	L4407_RF	DARWIN
152S2056	1	5.6NH, INDUCTOR	L4403_RF	DARWIN
131S0429	1	8.2PF, CAPACITOR	C4407_RF	DARWIN
152S00143	1	15NH, INDUCTOR	L4404_RF	DARWIN
131S0823	1	33PF, CAPACITOR	C4408_RF	DARWIN
117S0108	1	51 OHM, RESISTOR	L4410_RF	DARWIN
152S00052	1	3.4NH, INDUCTOR	L3910_RF	DARWIN
152S2039	1	3.8NH, INDUCTOR	L3911_RF	DARWIN
131S0279	1	1.3PF, CAPACITOR	L3919_RF	DARWIN

LAT DIPLEXER1

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	ROW
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RF2
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RFC
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	DARWIN

HB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00376	1	IC, PWR AMP, HB_PAD, TQS	UHBPA_RF	ROW
353S4494	1	IC, PWR AMP, HB_PAD, AVAGO	UHBPA_RF	RF2
353S00376	1	IC, PWR AMP, HB_PAD, TQS	UHBPA_RF	RFC
353S00478	1	IC, PWR AMP, HB_PAD, PT	UHBPA_RF	DARWIN

19.2MHZ XTAL ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ
197S0598	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ

VINYL

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
337S00176	1	IC, VINYL	U5101_RF	ROW
337S00176	1	IC, VINYL	U5101_RF	RF2

VINYL RESISTOR


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
117S0161	1	0 OHM, RESISTOR	R3402_RF	RFC
117S0161	1	0 OHM, RESISTOR	R3402_RF	DARWIN

HW_REV1_ID RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
118S0646	1	51.1 KOHM, RESISTOR	R3503_RF	DARWIN

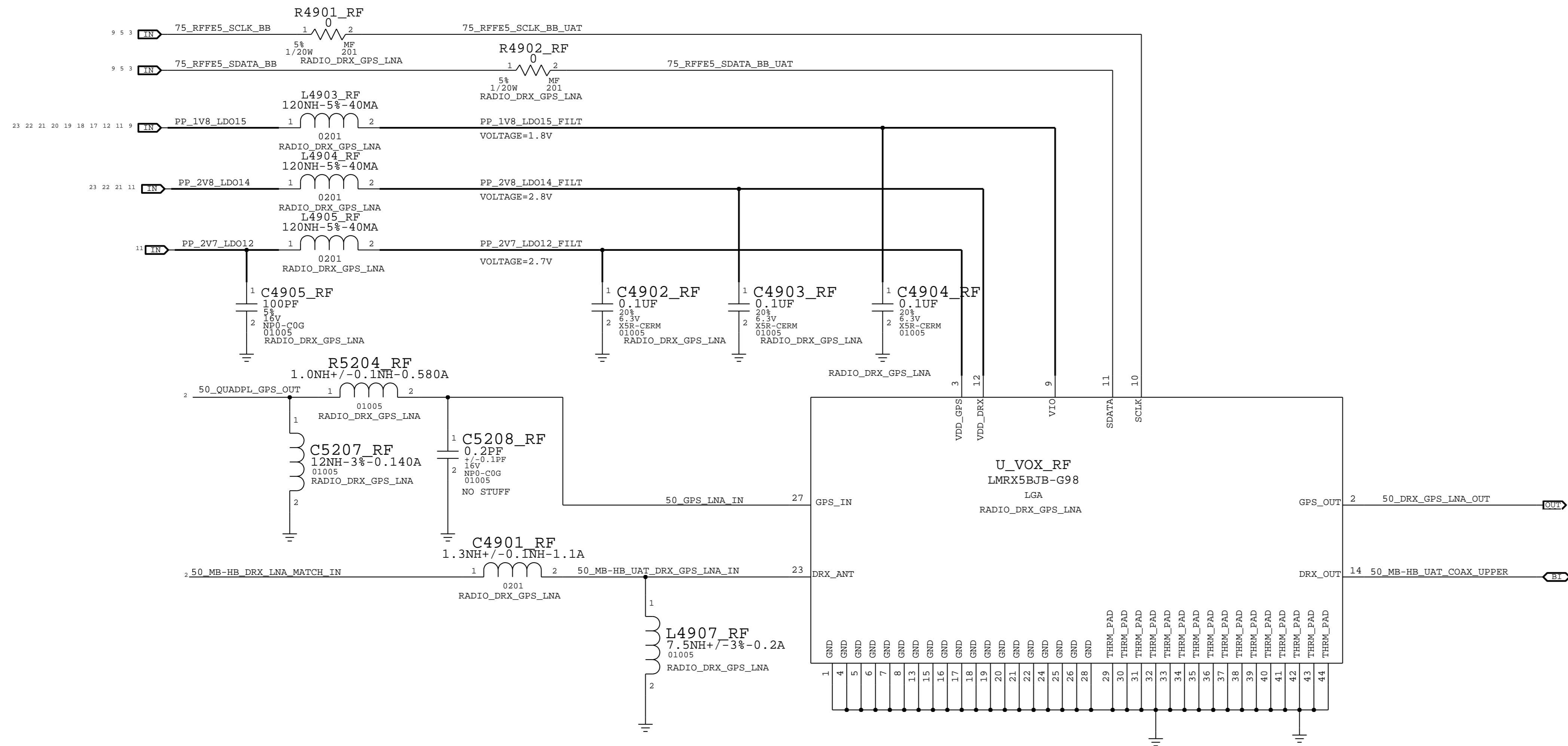
SIM ESD DIODE ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S00042	377S0163	ALTERNATE	VR301_RF	ON SEMI ESD DIODE

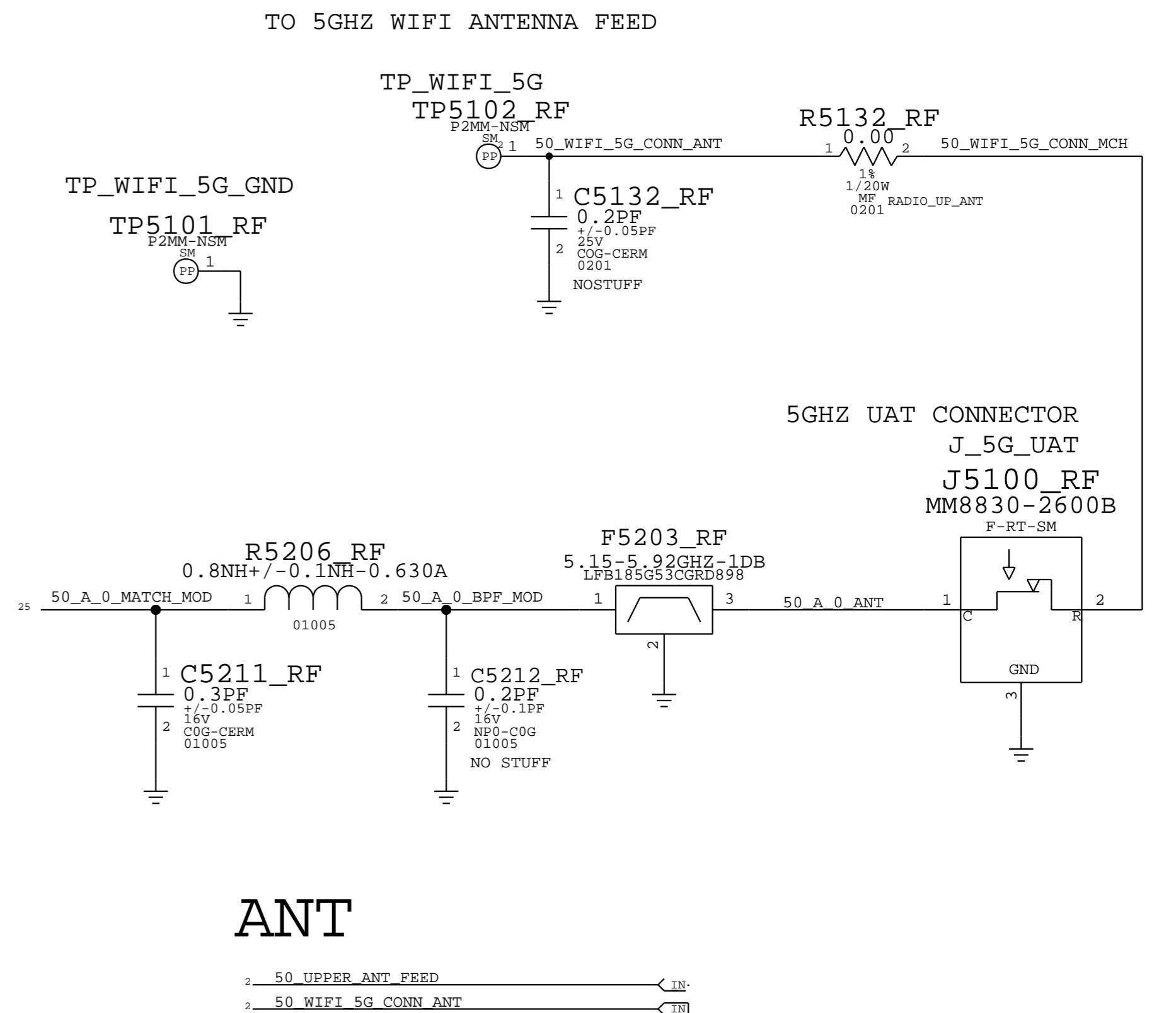
DRAWING TITLE		SCHEM, SINGLE, BRD, N71	
 Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE I NOT TO REPRODUCE OR COPY IT I NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART I ALL RIGHTS RESERVED		PAGE	1 OF 51
		SHEET	34 OF 59

N71-SPECIFIC RADIO PAGE 2

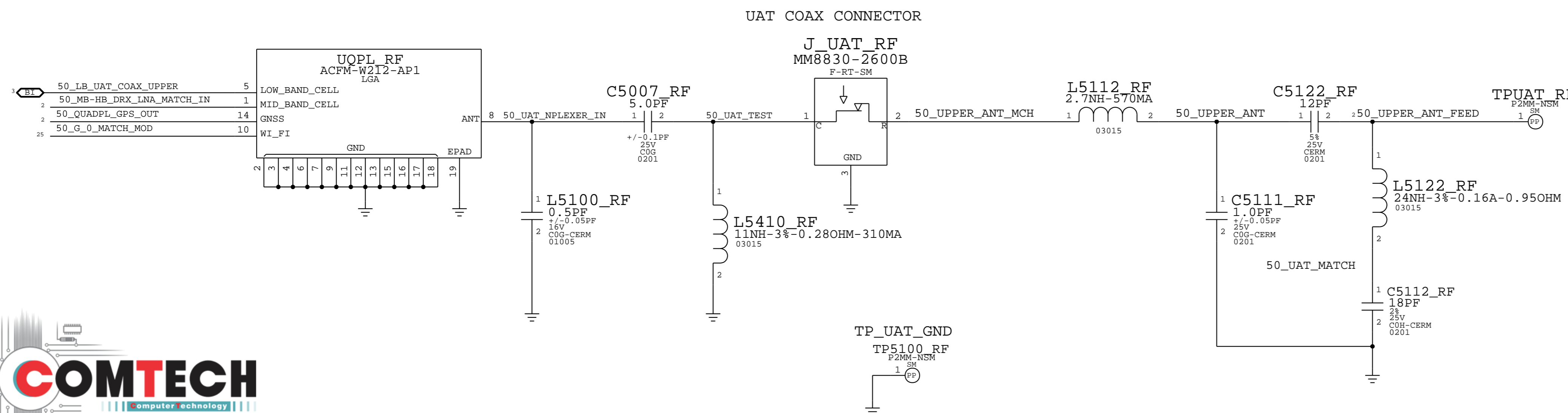
DIVERSITY LNA



WIFI ANT FEED



UAT ANT FEED



ANT



CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

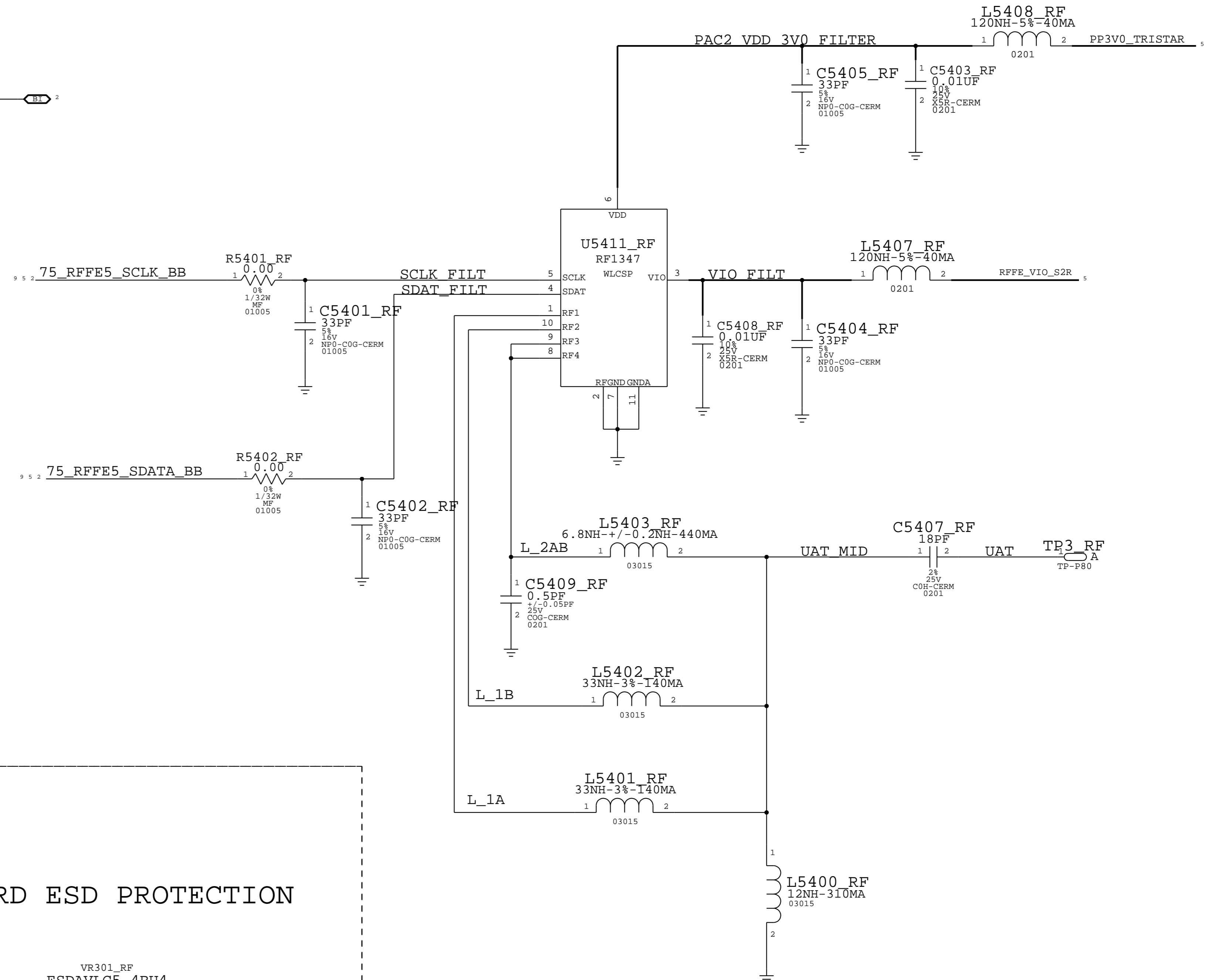
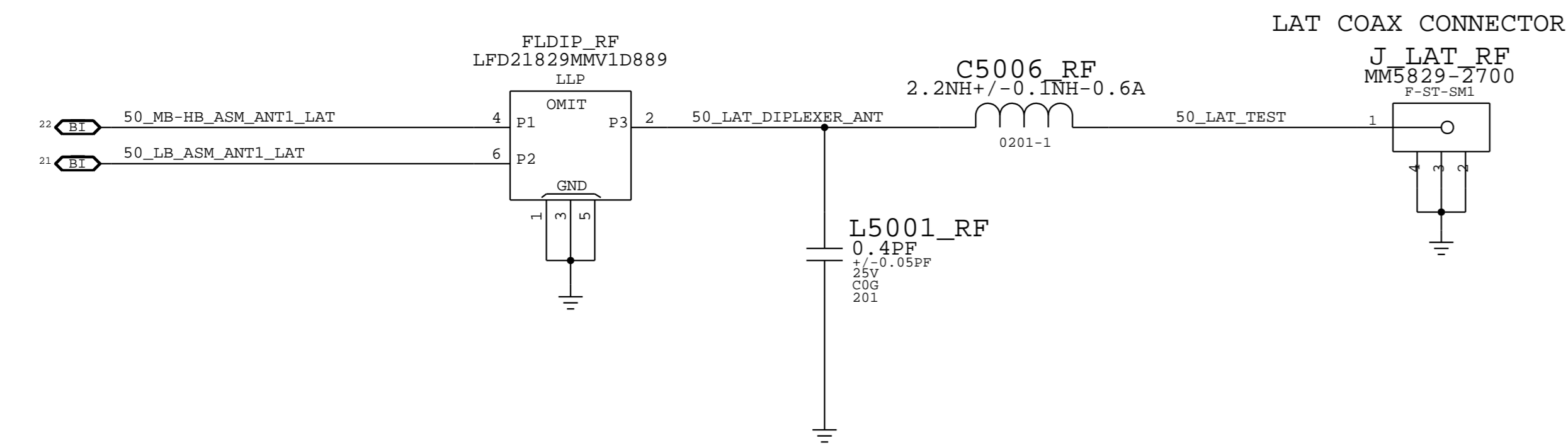
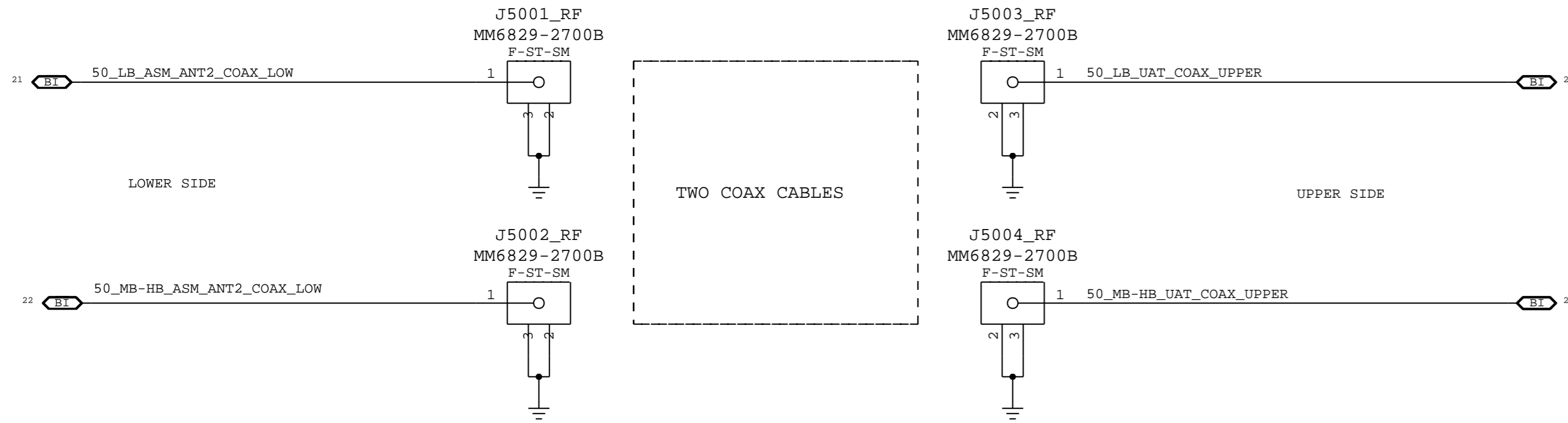
PAGE TITLE		
ELNA & UAT ANT FEED		
DRAWING NUMBER	051-1902	SIZE
REVISION	A.0.0	D
NOTICE OF PROPRIETARY PROPERTY:		
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		
II NOT TO REPRODUCE OR COPY IT		
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART		
IV ALL RIGHTS RESERVED		
BRANCH		
PAGE	2 OF 51	
SHEET	35 OF 59	

N71-SPECIFIC RADIO PAGE 3

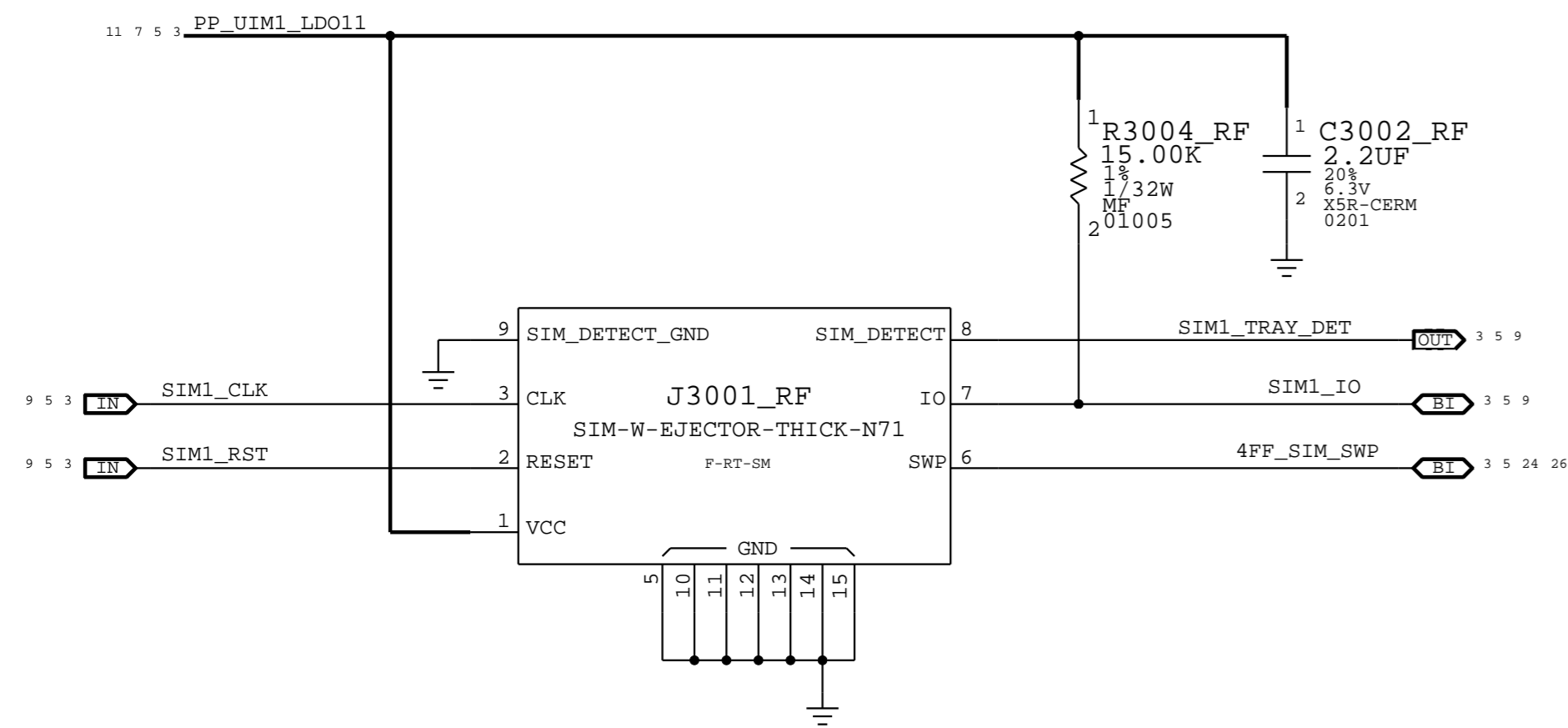
ANTENNA FEEDS AND CONNECTORS



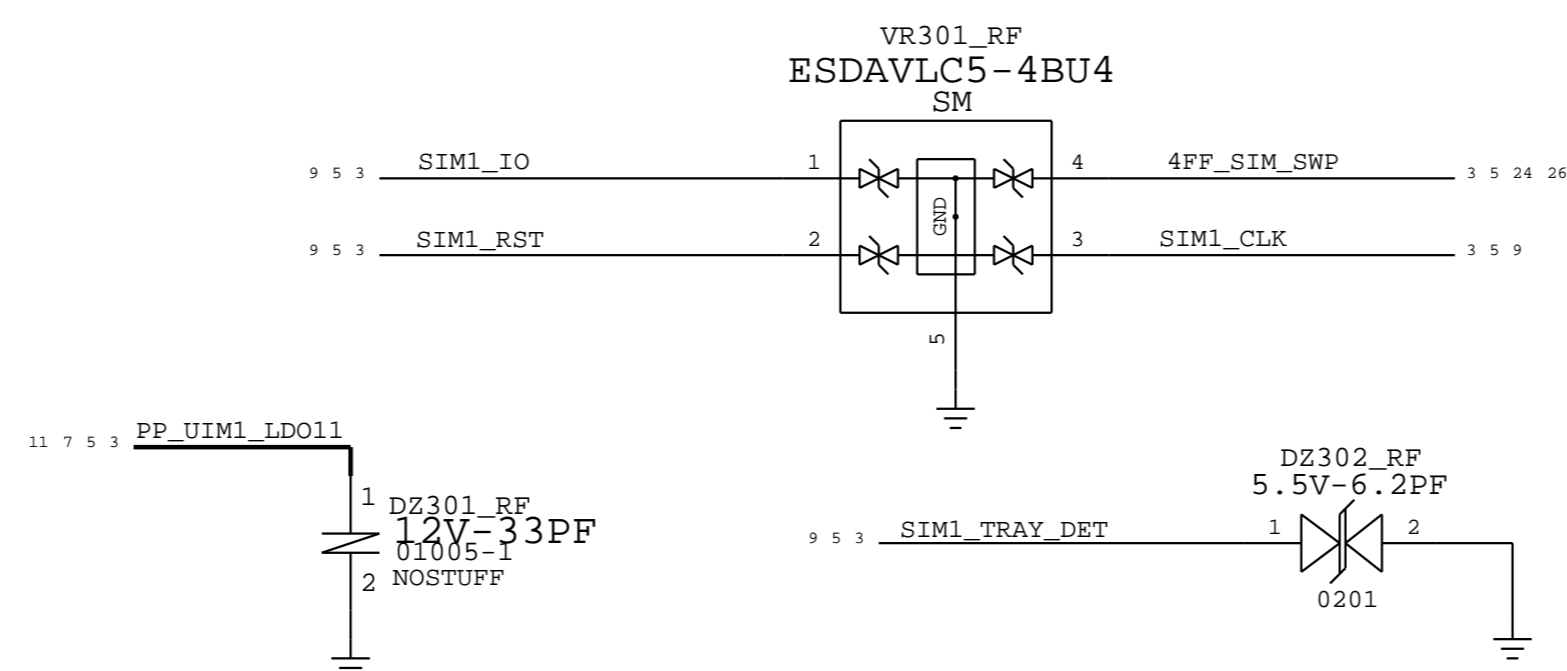
UAT TUNER



SIM CARD CONNECTOR



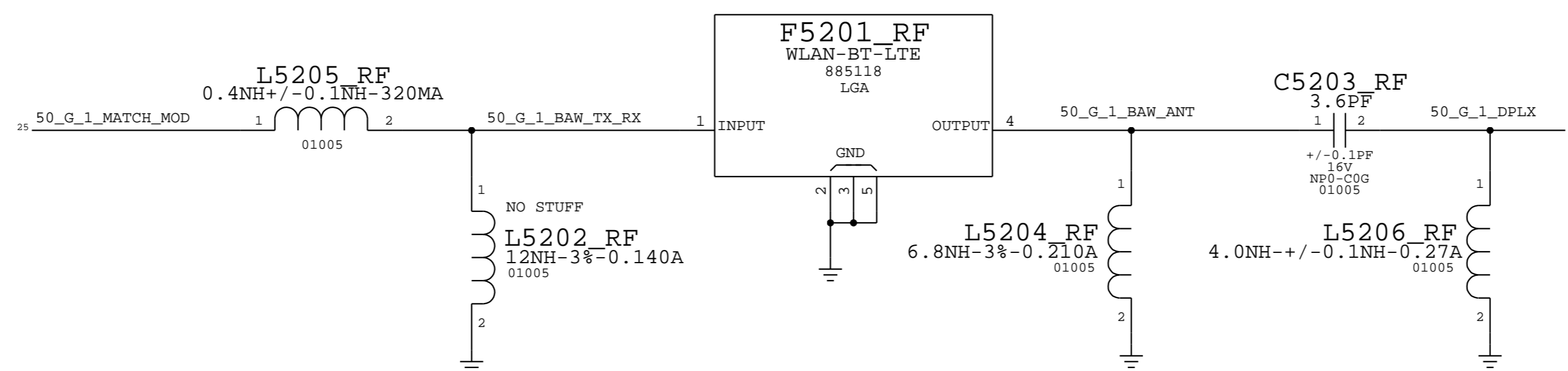
SIM CARD ESD PROTECTION



PAGE TITLE FE: ANT CONNECTORS AND UAT TUNER		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED	BRANCH	
	PAGE	3 OF 51
	SHEET	36 OF 59

N71-SPECIFIC RADIO PAGE 4

WLAN LAT 2.4GHZ BAW BPF



AP TO BB/WLAN/BT/SH CONNECTIONS

MLB PROBE POINTS

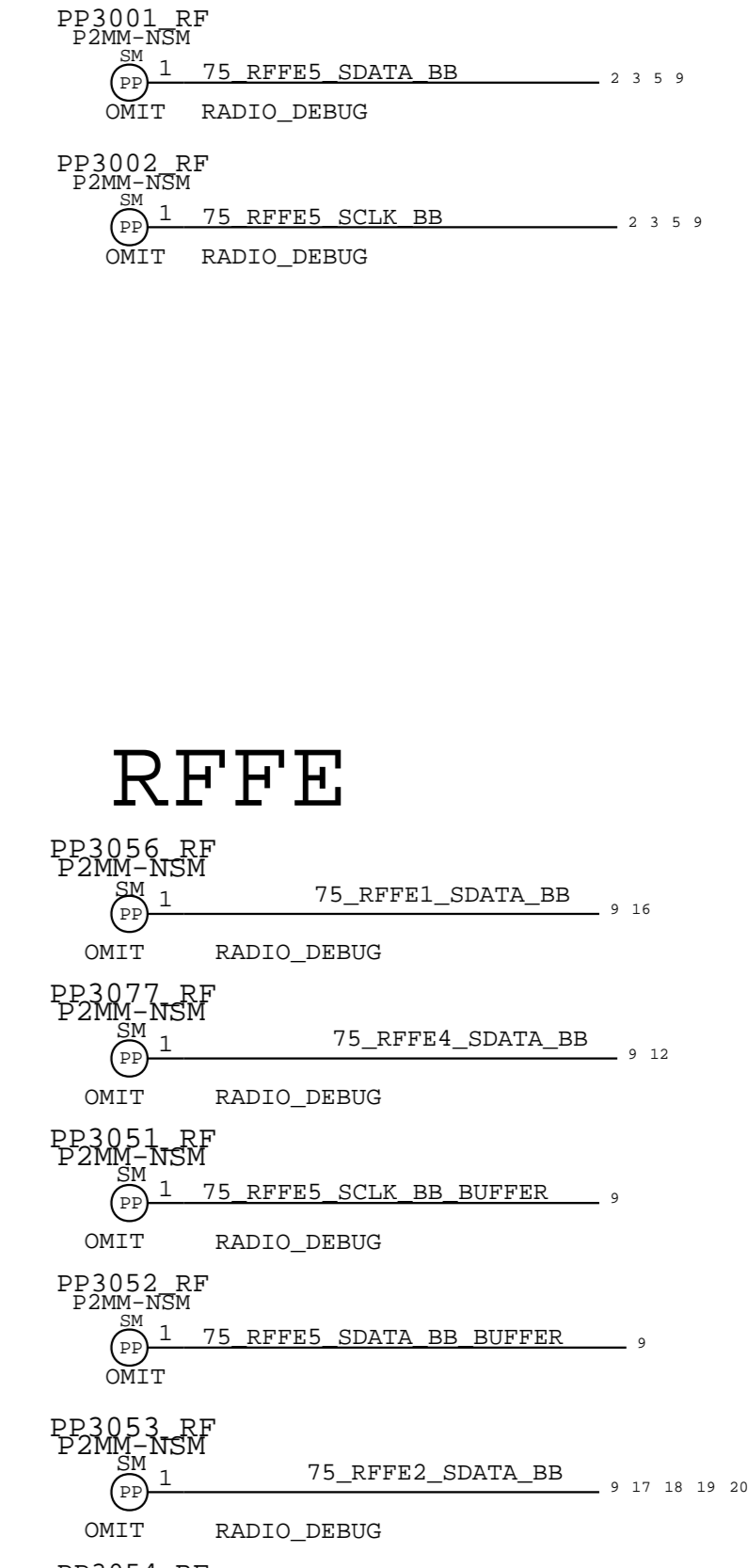
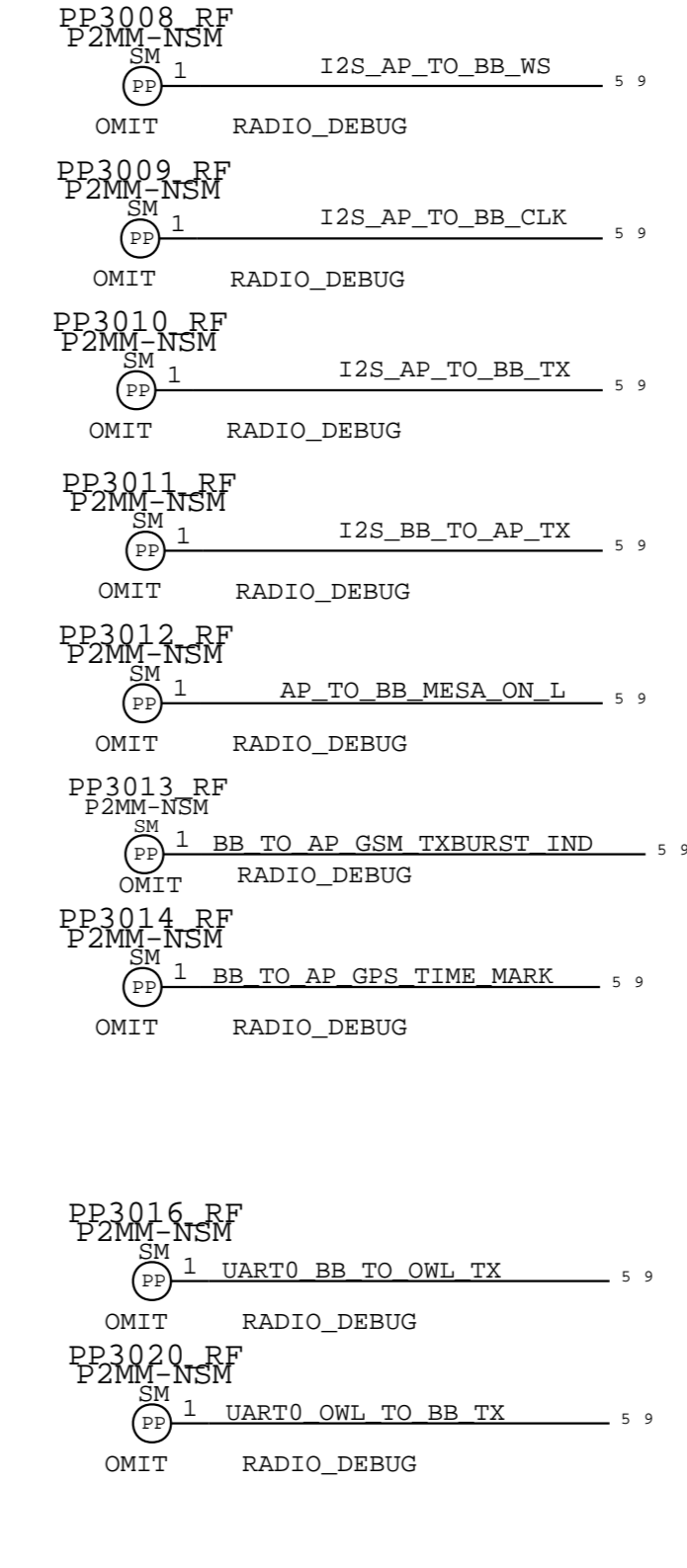
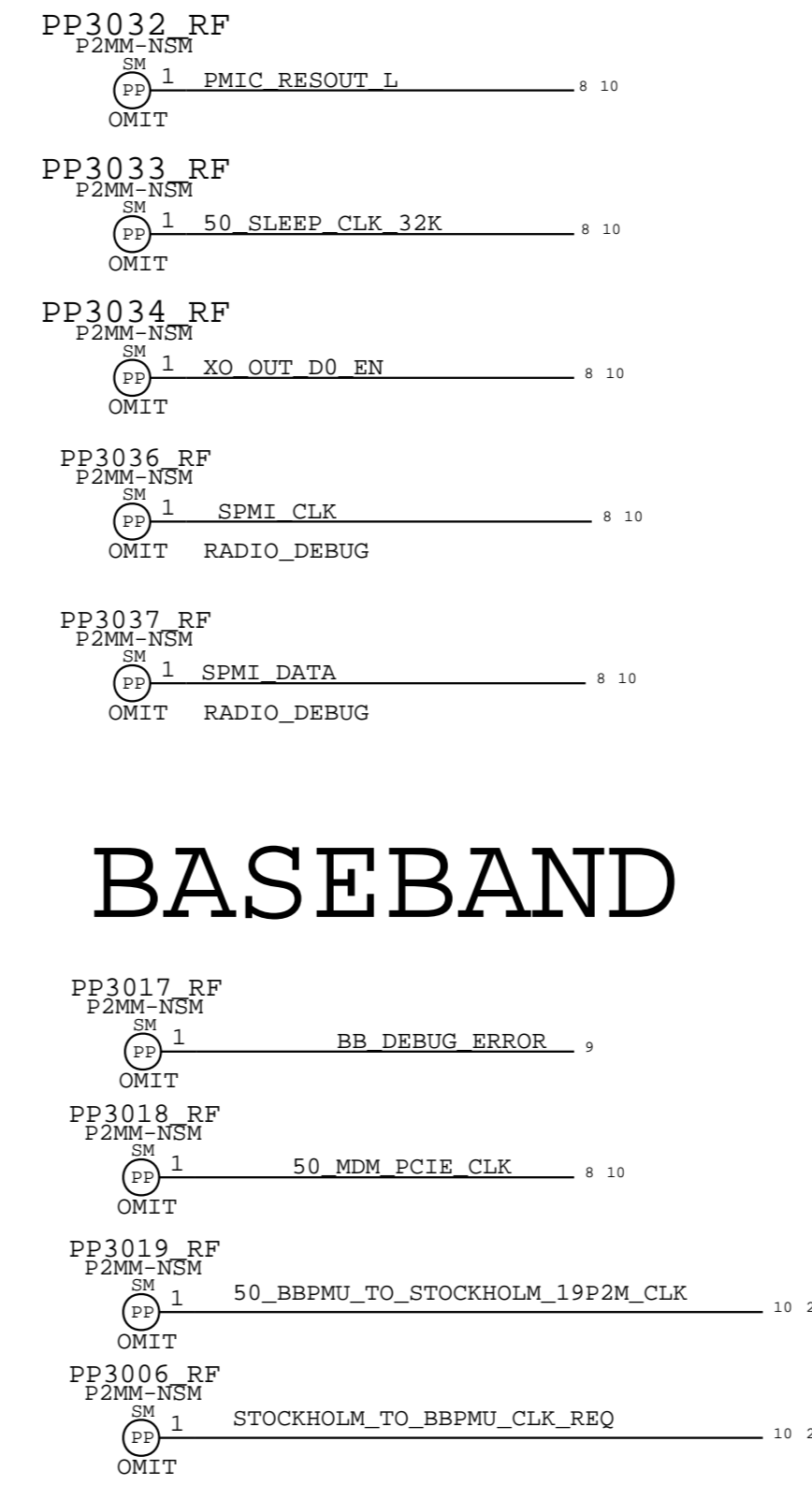
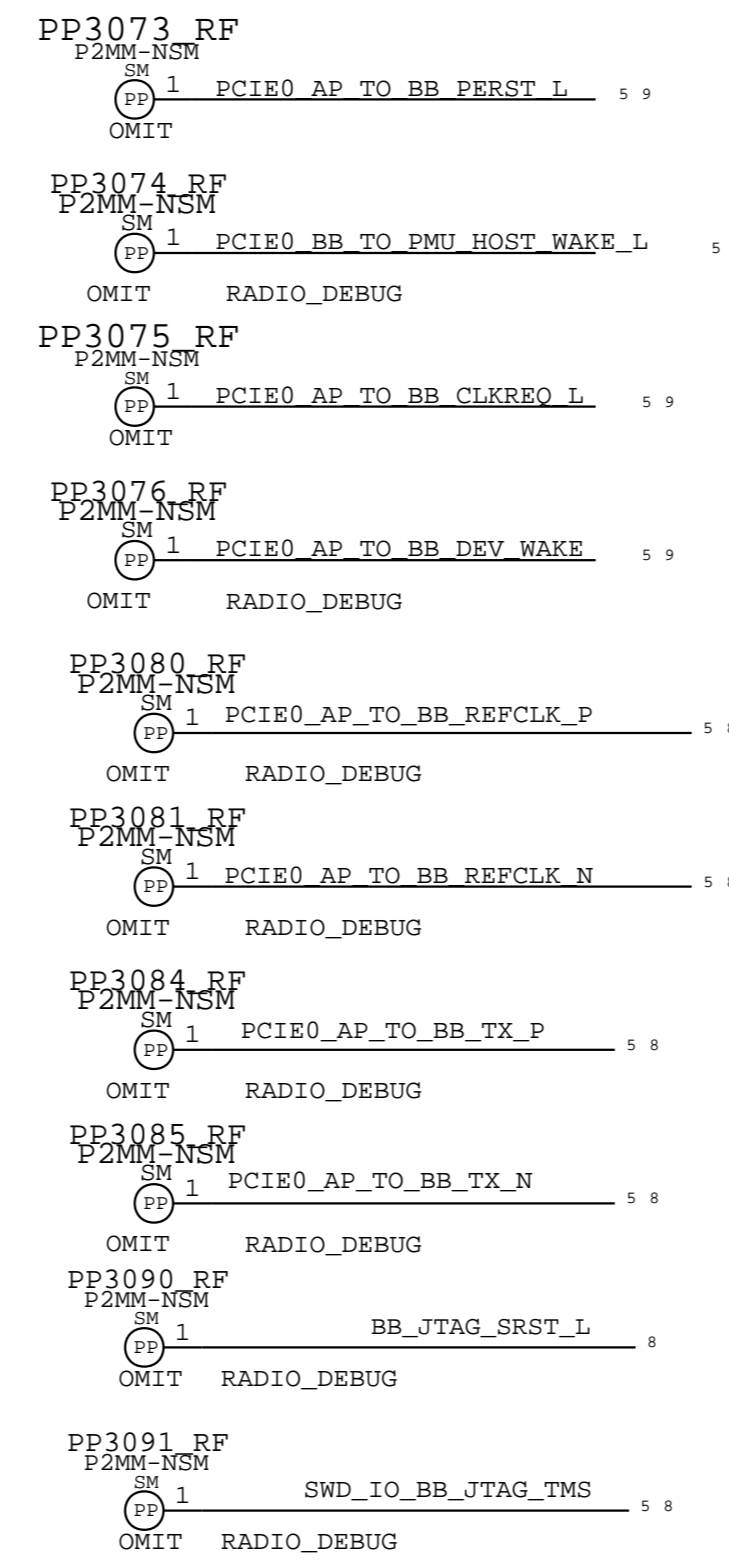
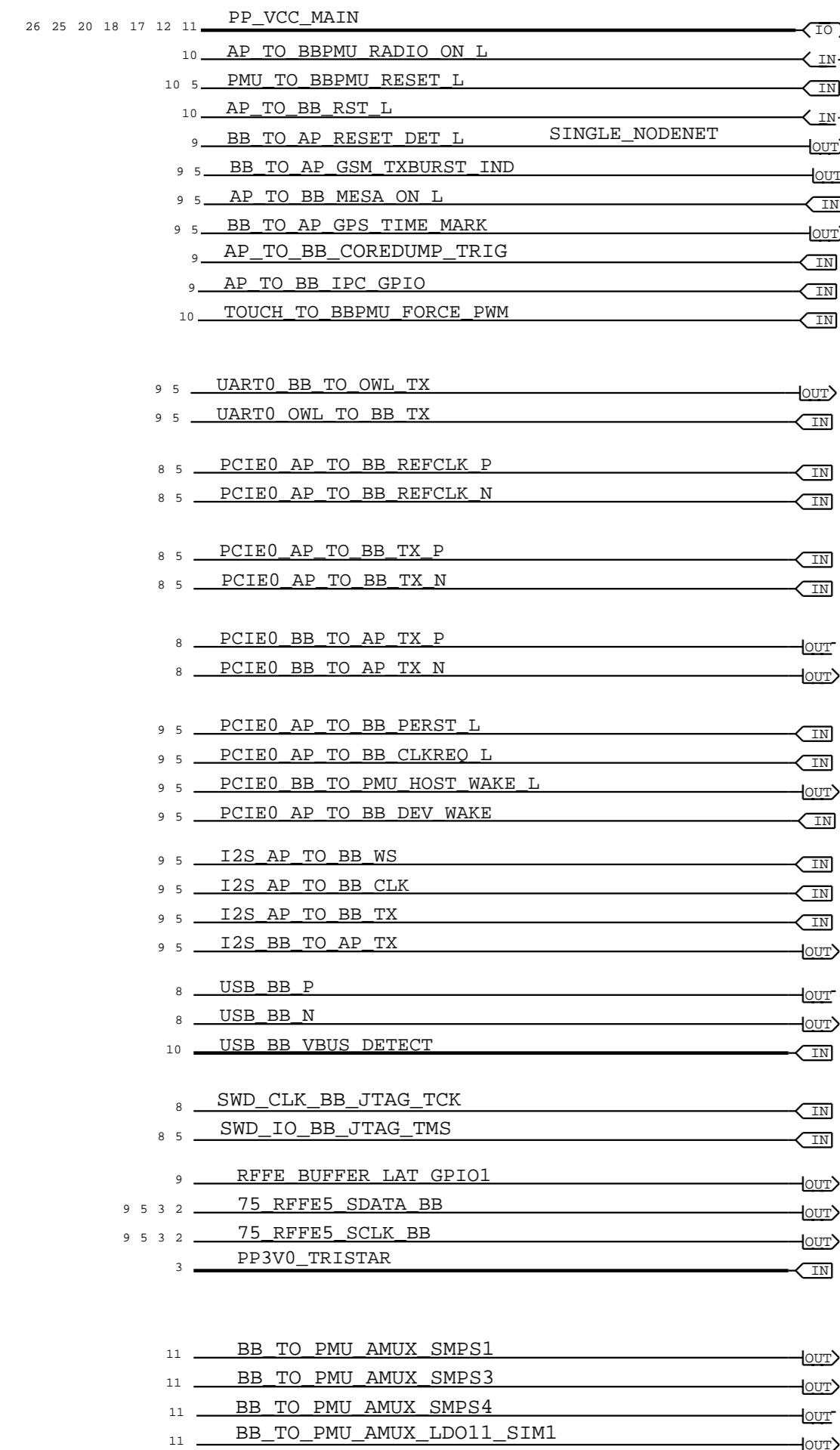
BASEBAND

WLAN/BT

PCIE

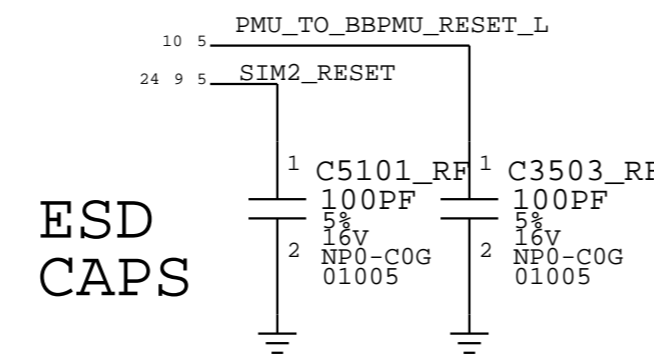
PMU

ANT TUNER

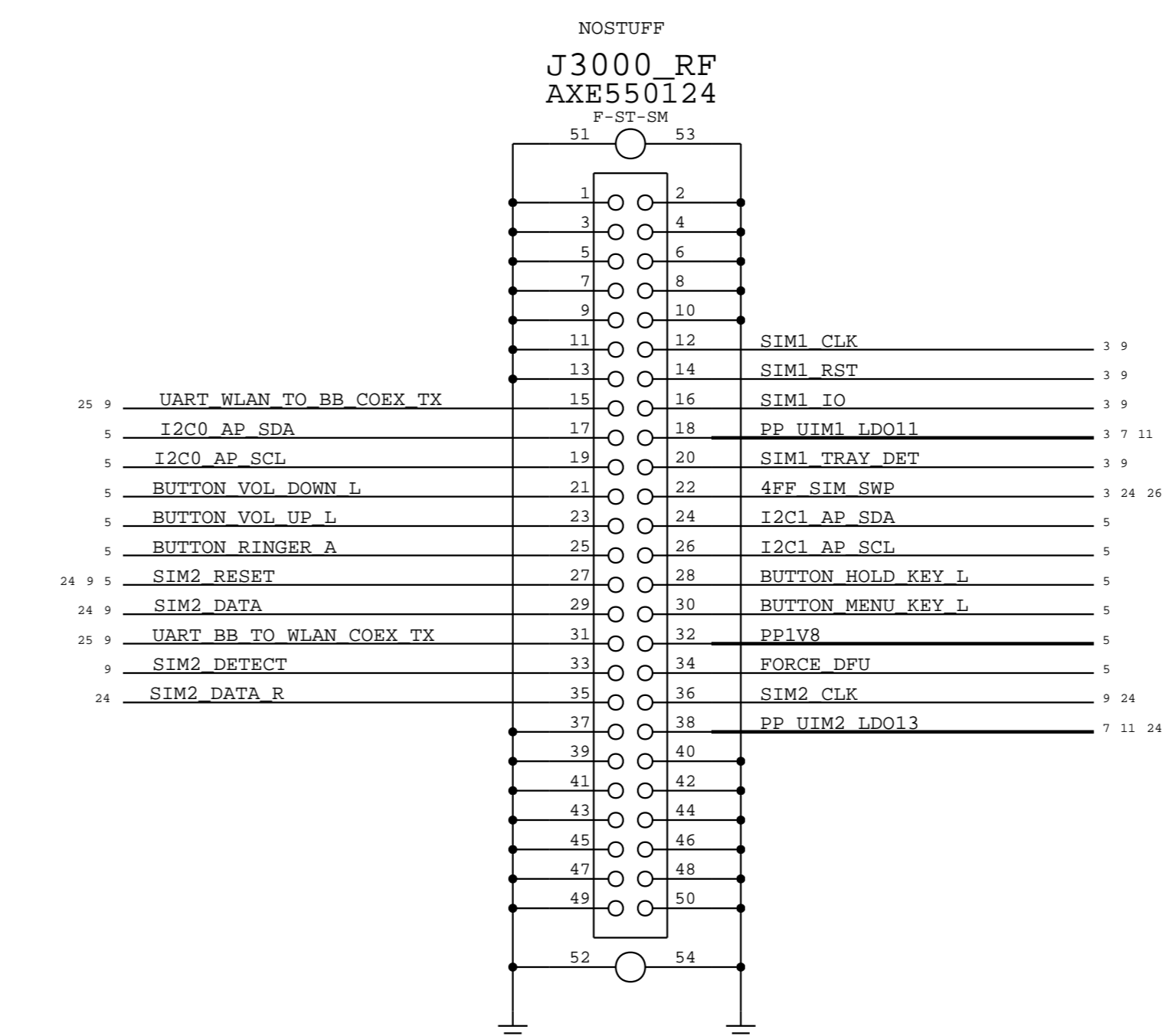
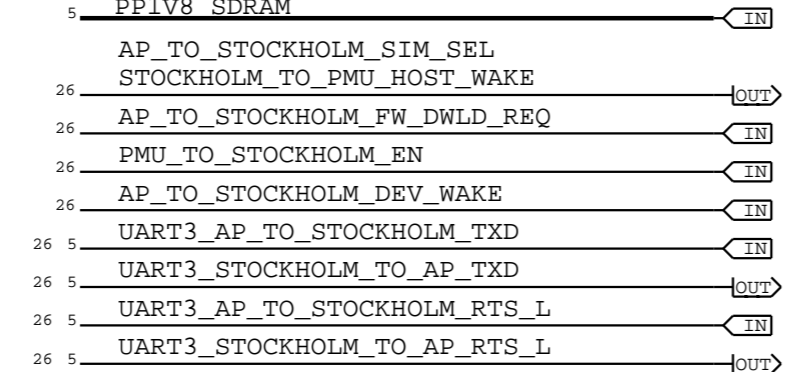
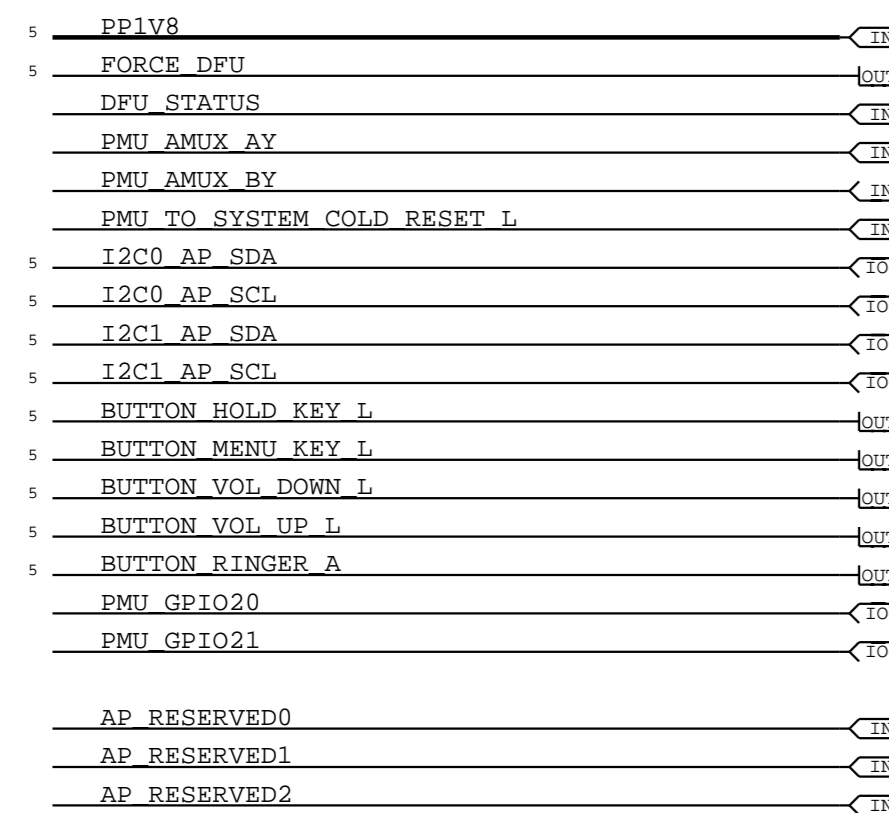


AP DEBUG

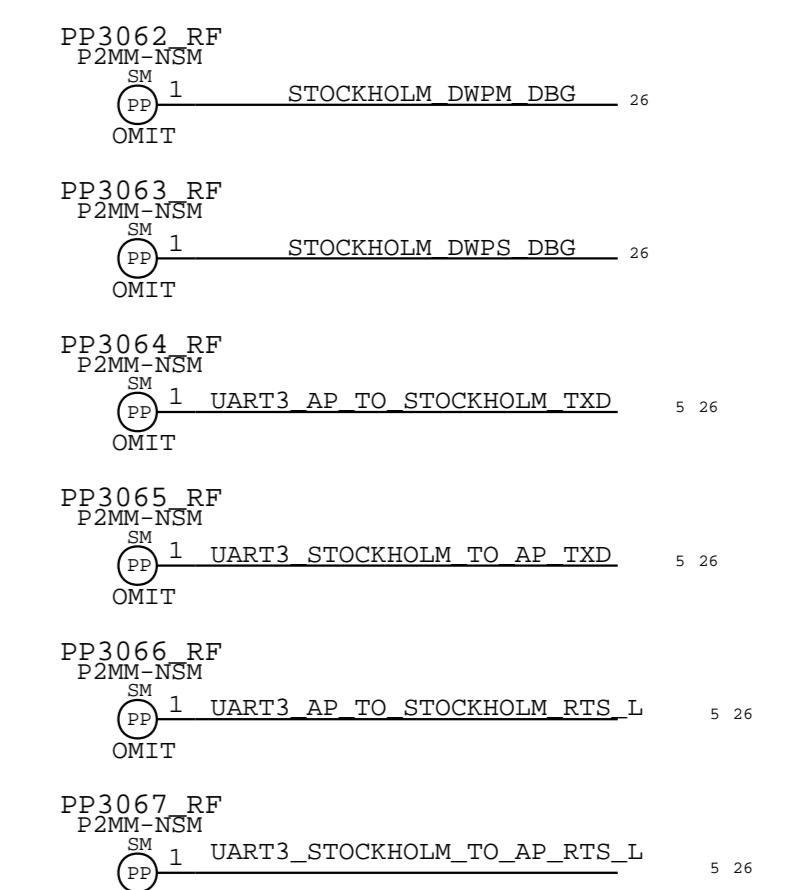
STOCKHOLM



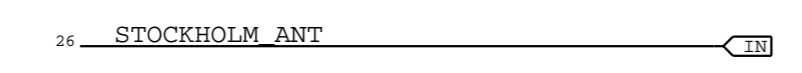
DEBUG CONNECTOR



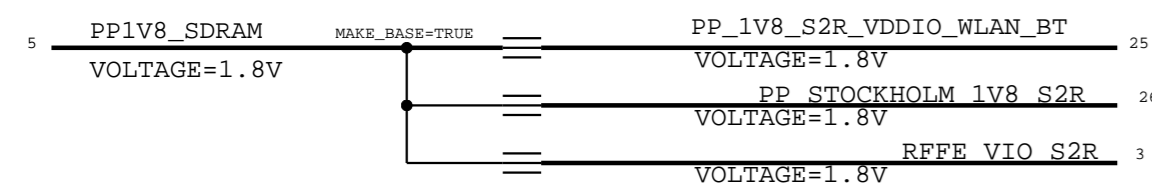
STOCKHOLM



ANT

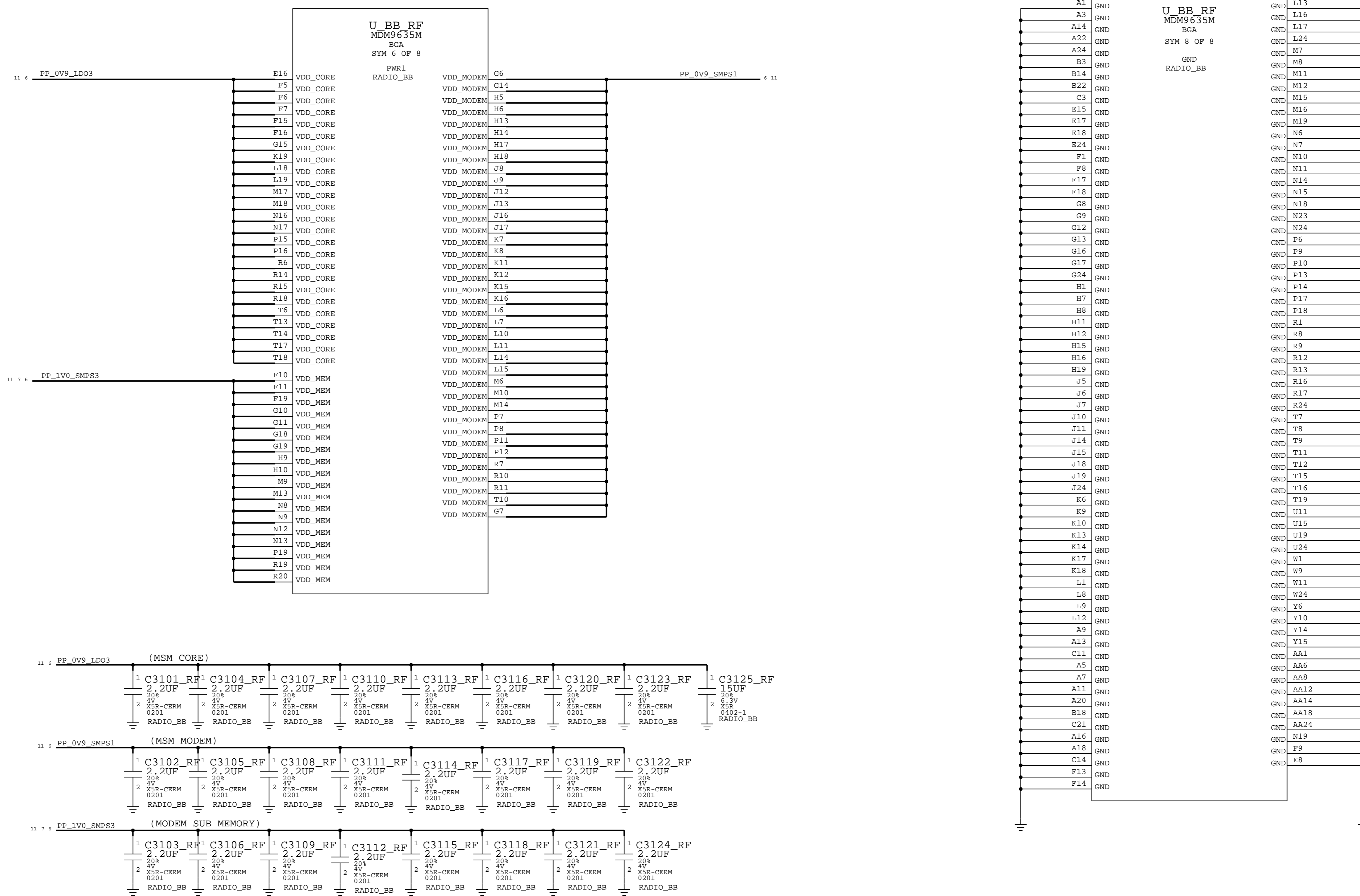


POWER



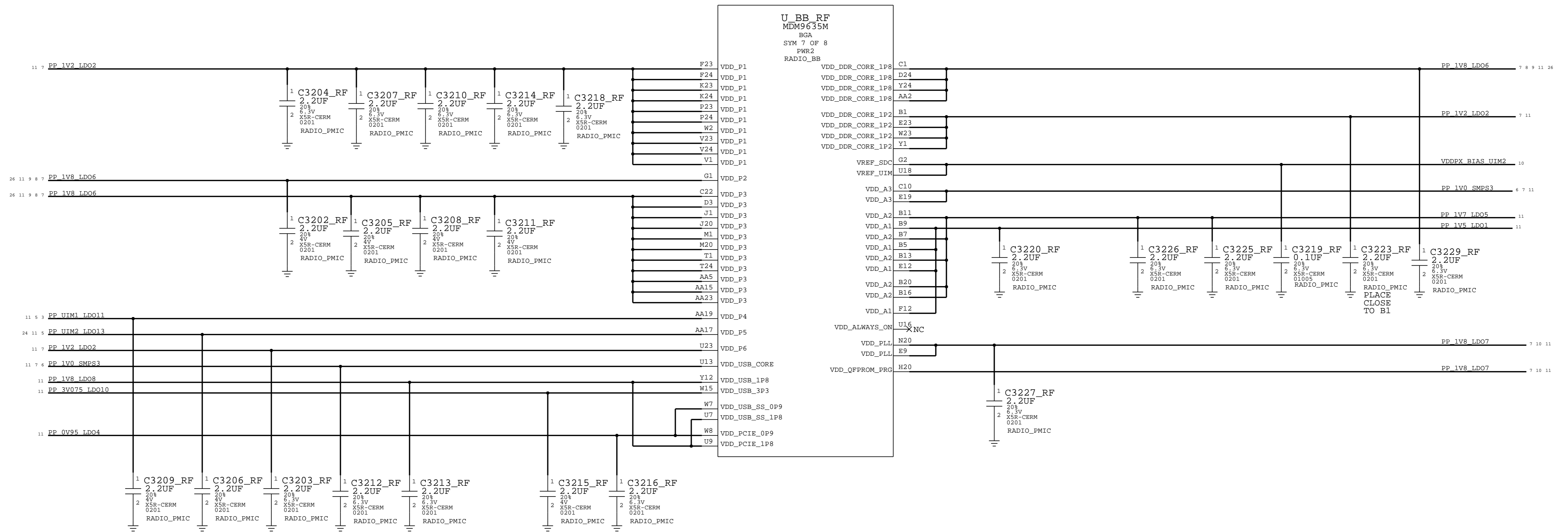
PAGE TITLE		DRAWING NUMBER	SIZE
DEBUG CONN & TEST POINTS		051-1902	D
Apple Inc.		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE	30 OF 51
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		SHEET	38 OF 59
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			

BASEBAND: POWER 1

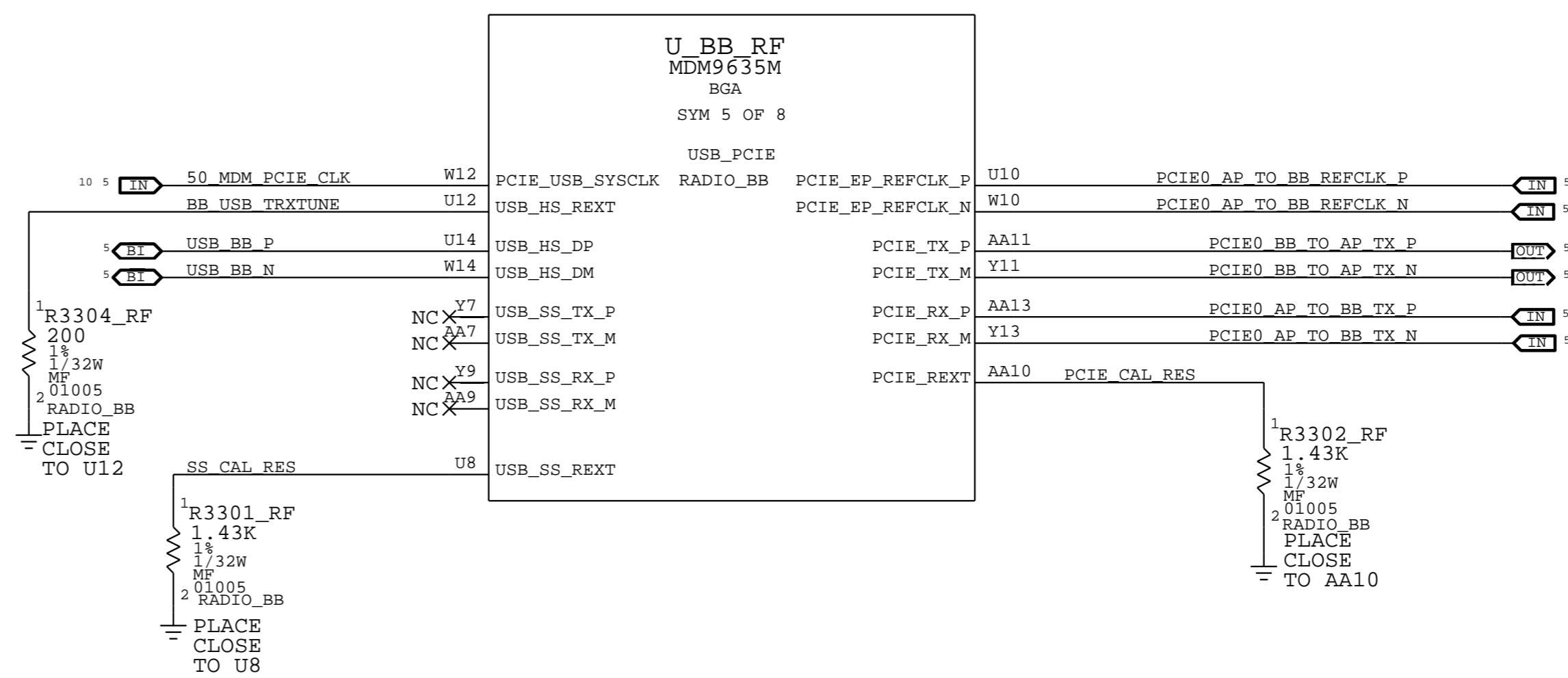
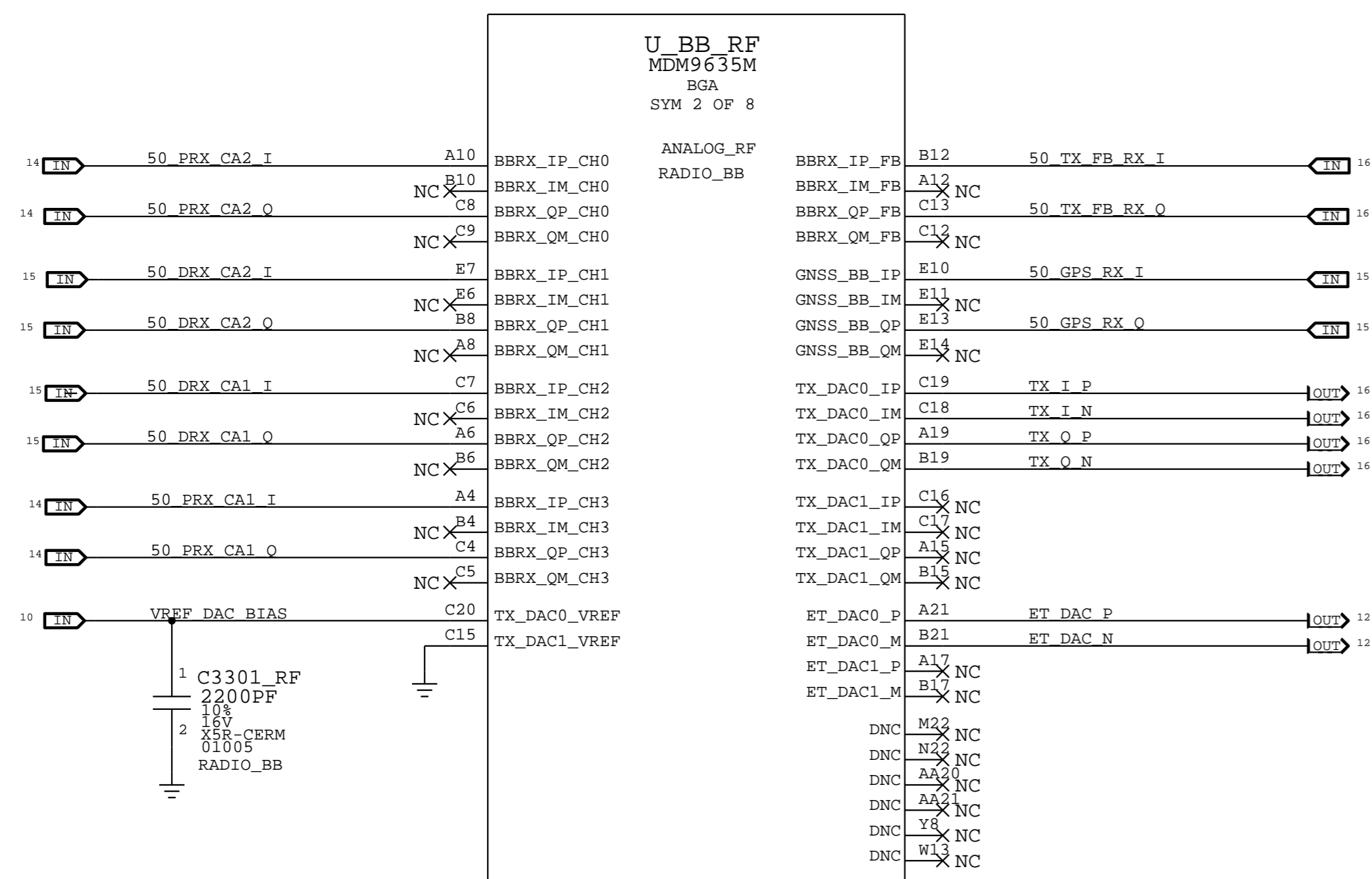
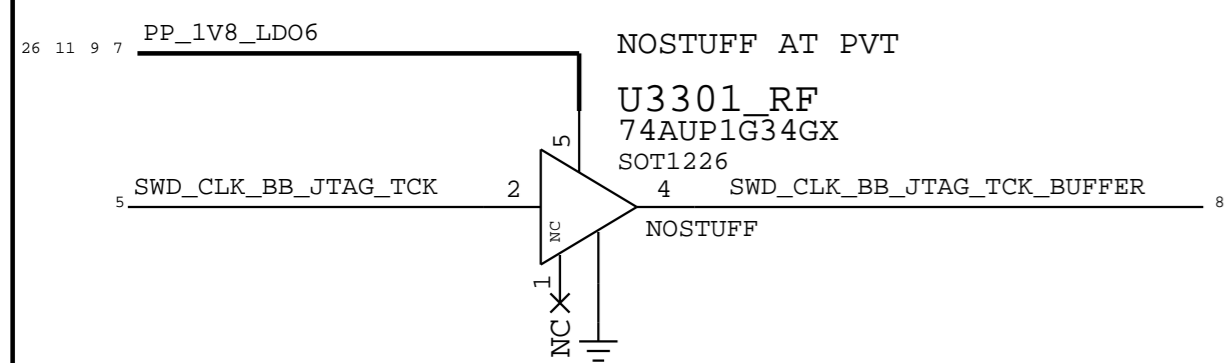
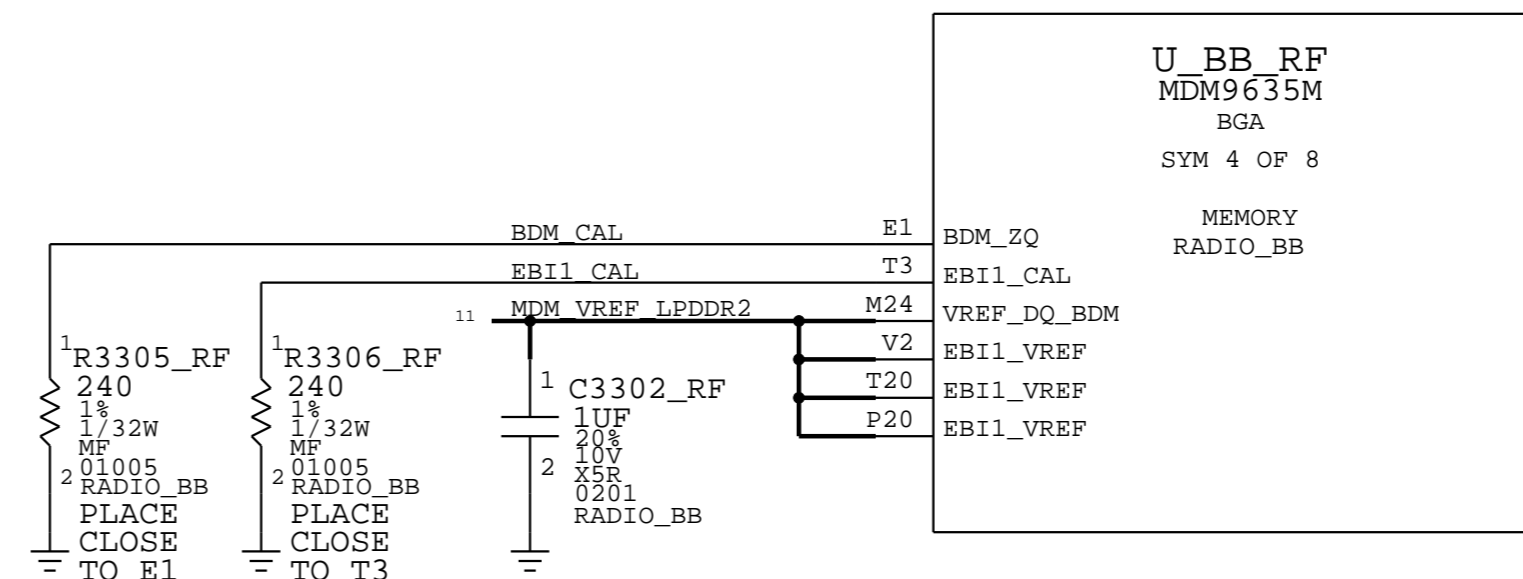
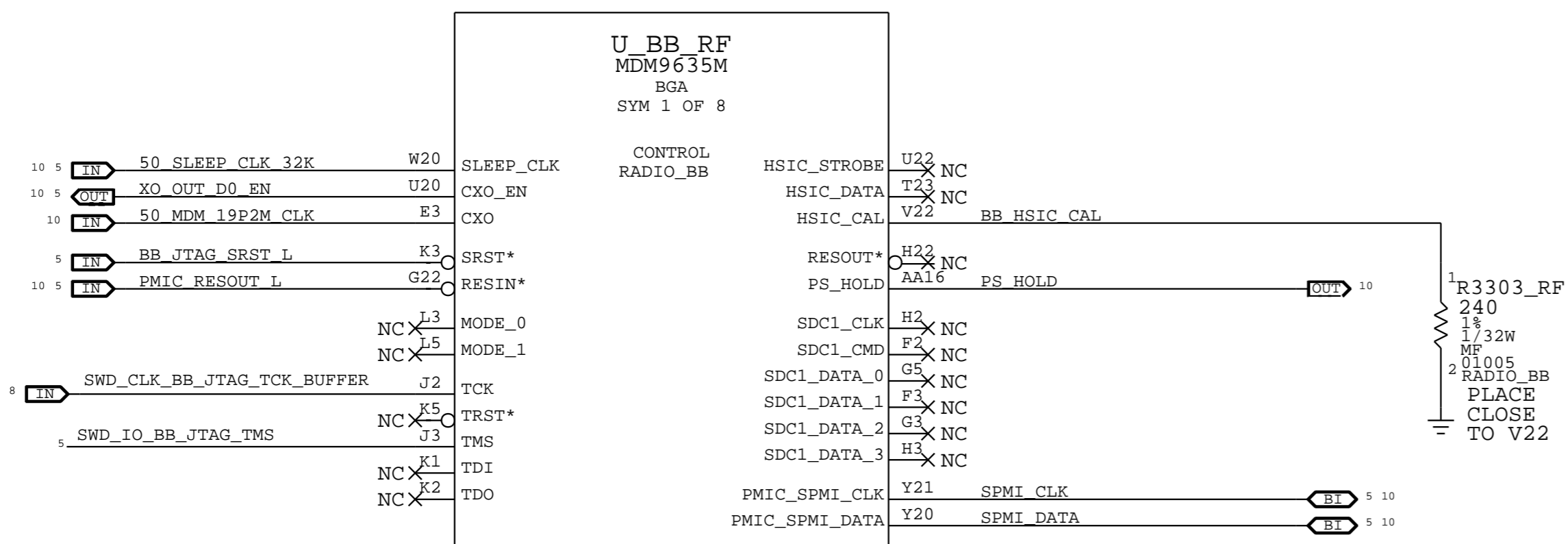


PAGE TITLE CELLULAR BASEBAND: POWER1		DRAWING NUMBER 051-1902	SIZE D
Apple Inc.		REVISION A.0.0	BRANCH
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		PAGE 31 OF 51	SHEET 39 OF 59

BASEBAND: POWER 2



BASEBAND: CONTROL AND INTERFACES

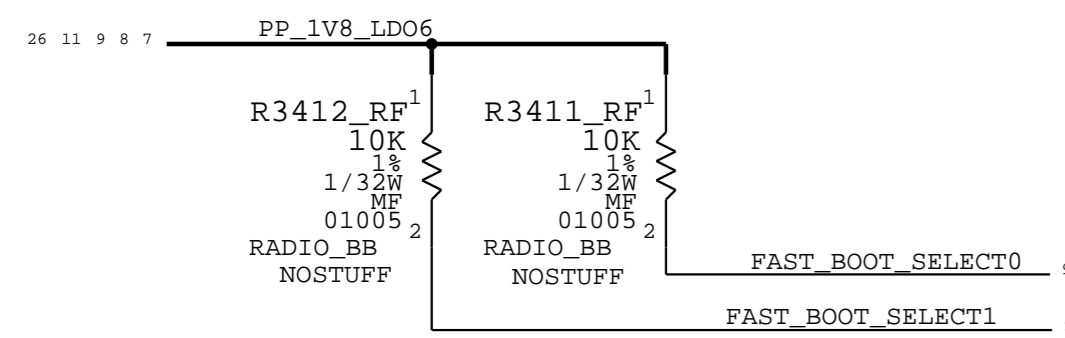
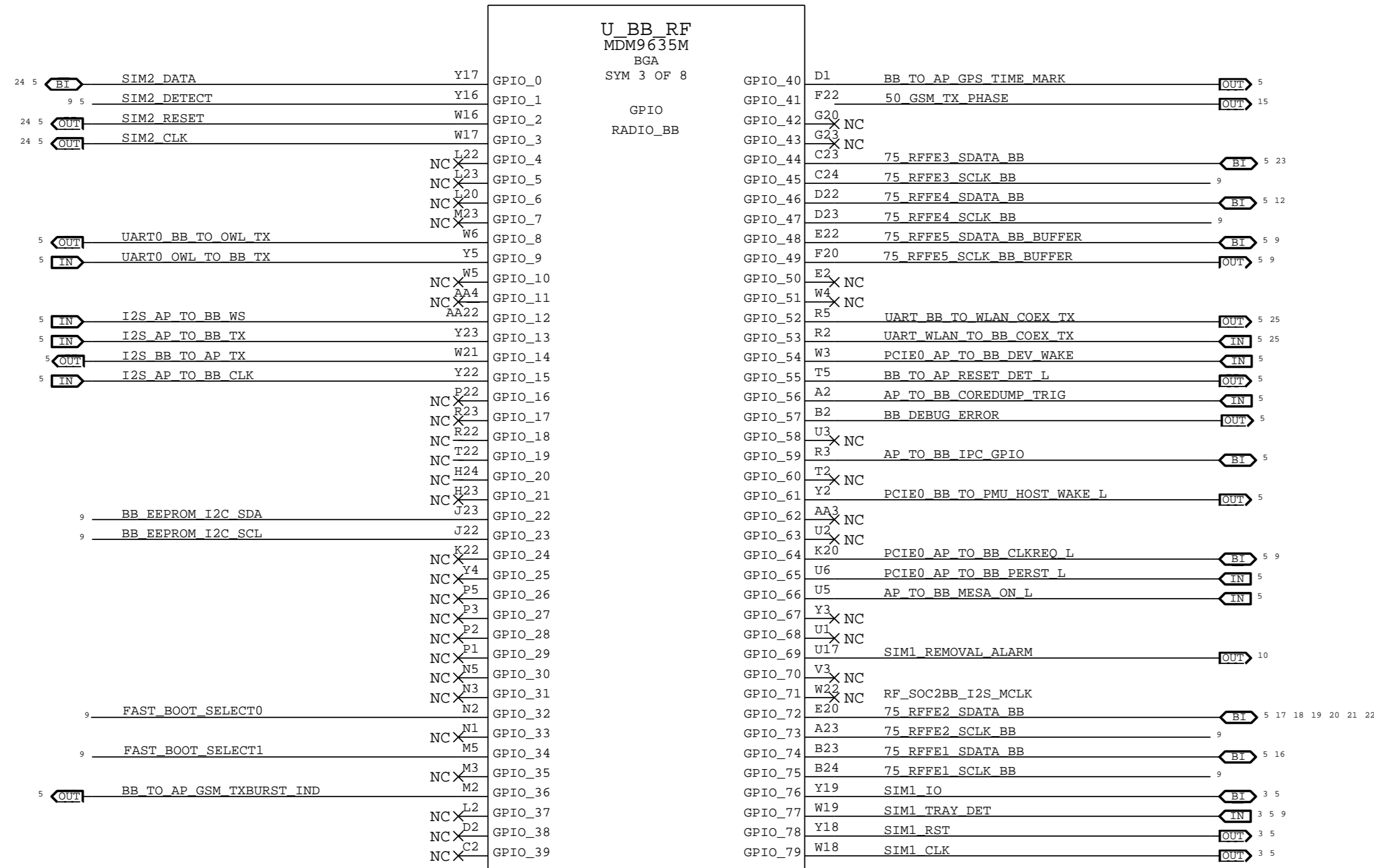


PAGE TITLE		CELLULAR BASEBAND: CONTROL AND INTERFACES	
DRAWING NUMBER		051-1902	SIZE D
REVISION		A.0.0	
BRANCH			
PAGE		33 OF 51	
SHEET		41 OF 59	



NOTICE OF PROPRIETARY PROPERTY:
 THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:
 I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE
 II NOT TO REPRODUCE OR COPY IT
 III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART
 IV ALL RIGHTS RESERVED

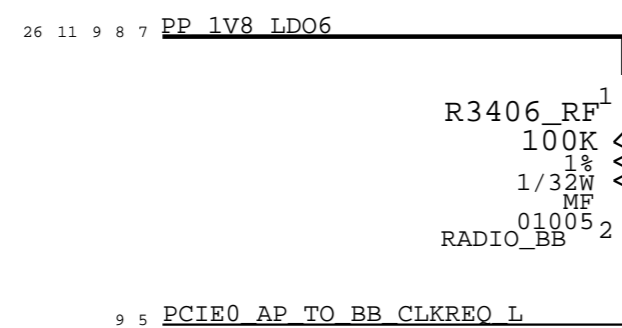
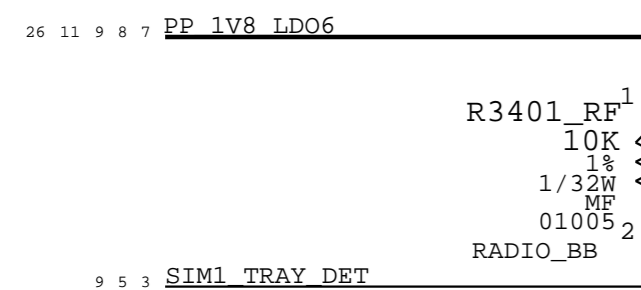
BASEBAND: GPIOS



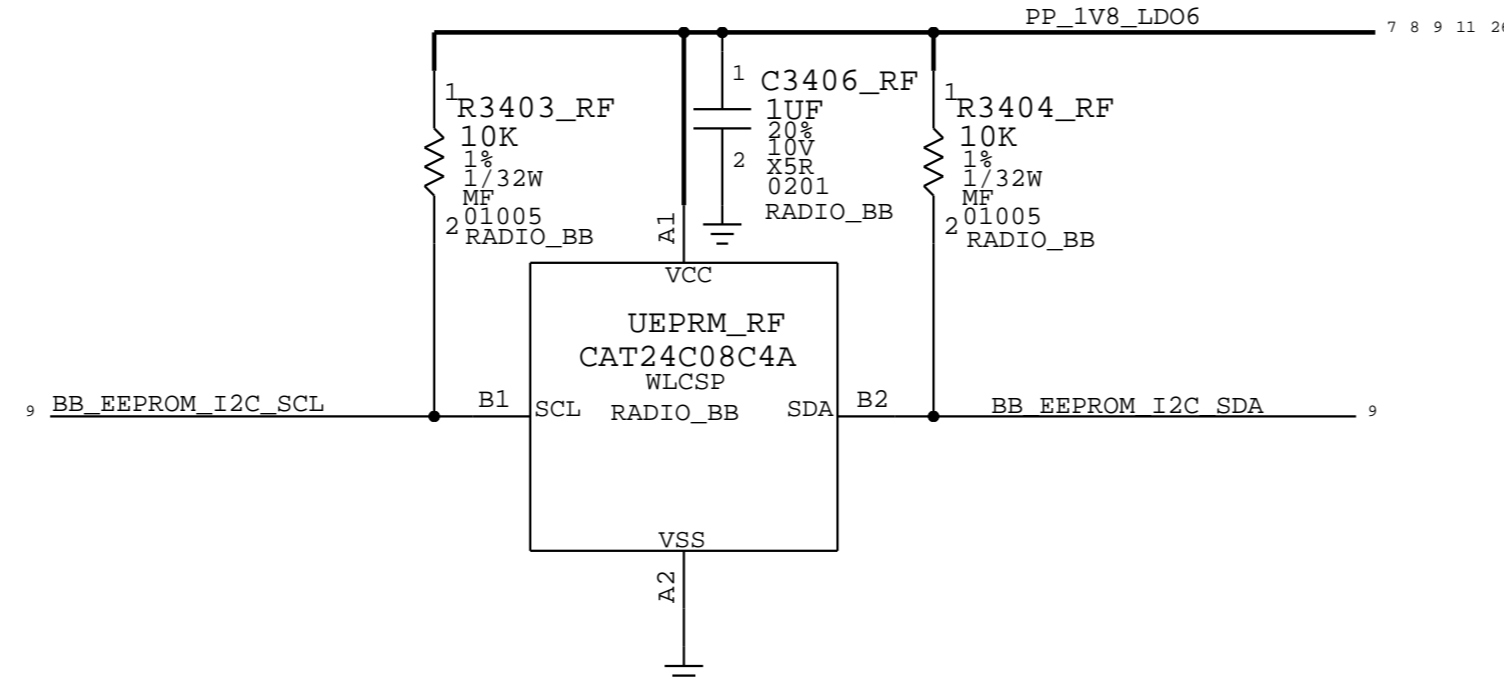
STUFF R3411 FOR PCIE BOOT (UNFUSED BB)
STUFF R3412 FOR USB BOOT (UNFUSED BB)

OPTION	SEL2	SEL1	SELO
GPIO	35	34	32
PCIE	0	0	1
HSUSB	0	1	0
HSIC	0	1	1

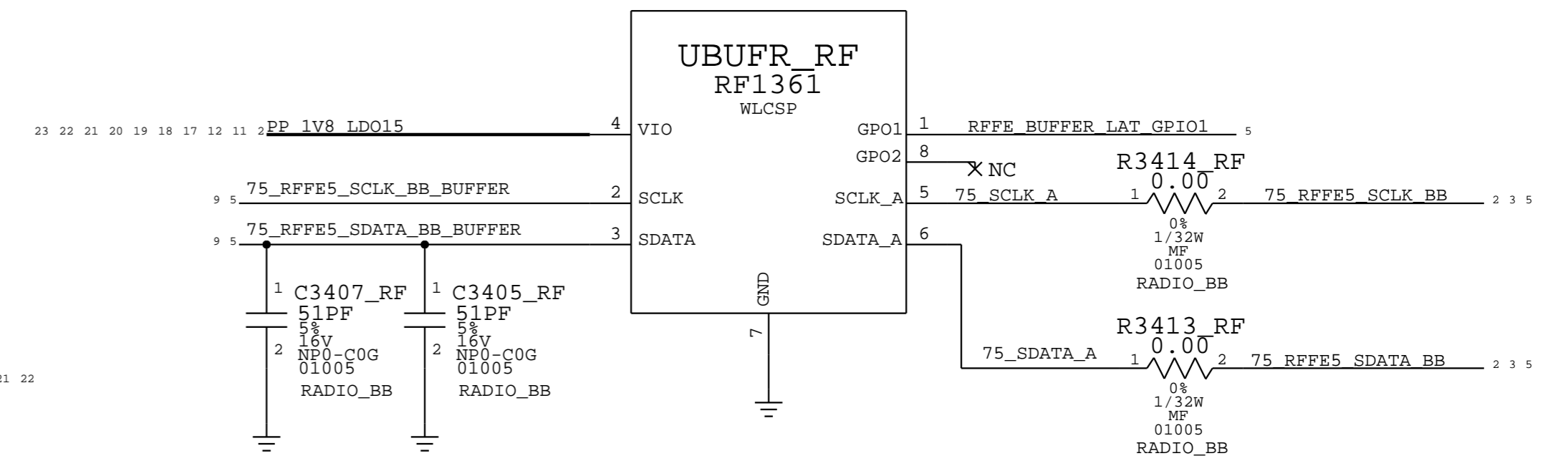
NOSTUFF R3402 WHEN VINYL PRESENT
STUFF R3402 WHEN VINYL NOT PRESENT



BB EEPROM

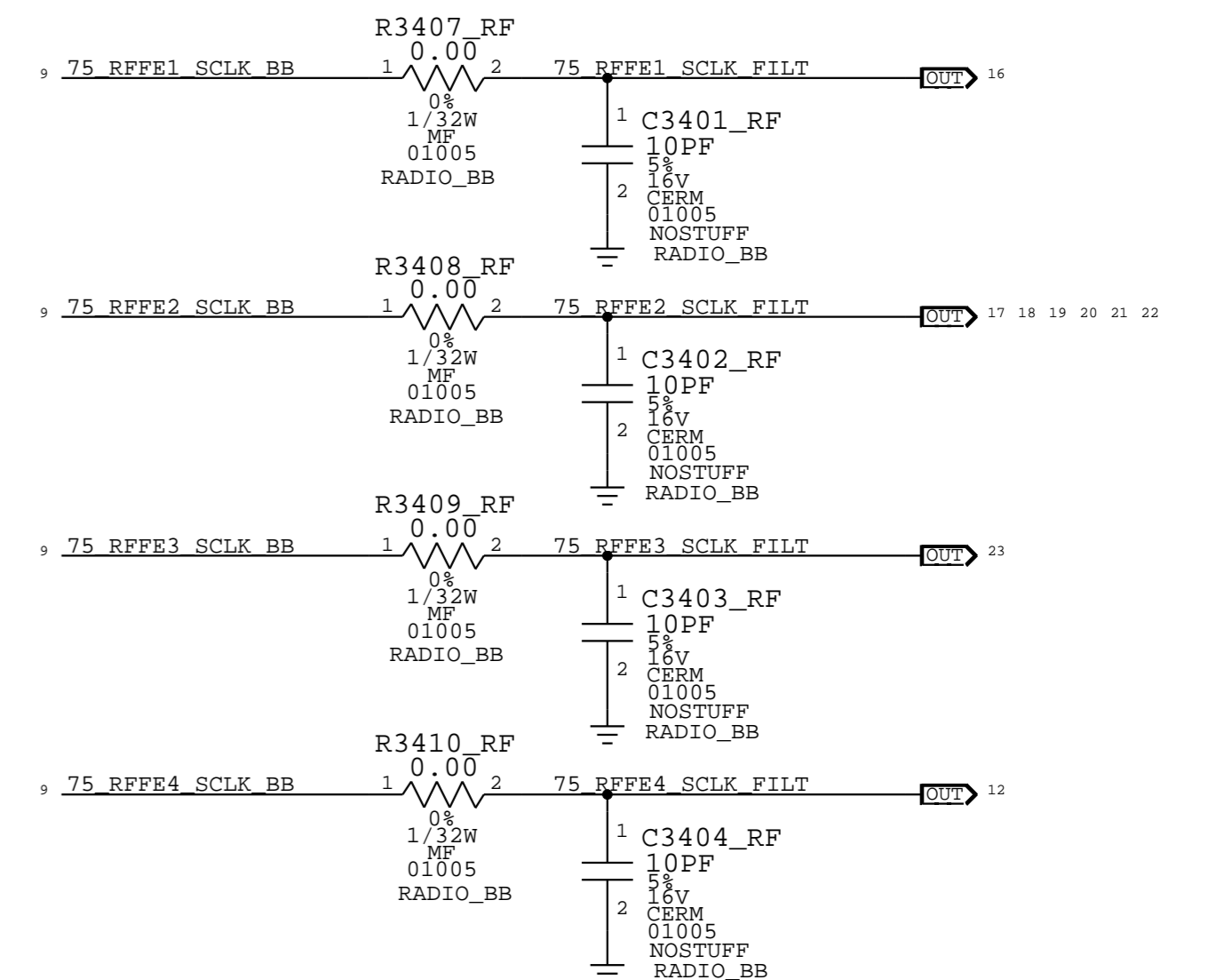


BUFFER ON RFFE5
SCLK/SDATA_A IS OUTPUT



PLACE C3405_RF CLOSE TO BUFFER
PLACE C3407_RF CLOSE TO MDM

RFFE CLOCK FILTERS



RFFE USAGE TABLE

- RFFE1 WTR
- RFFE2 LB/MB/HB PAD, 2G PA, LB/MB/HB ASM
- RFFE3 DIV ASM
- RFFE4 QPOET
- RFFE5 DIV LNA, ANT TUNERS

PCIE PULL-UPS TO BB RAIL



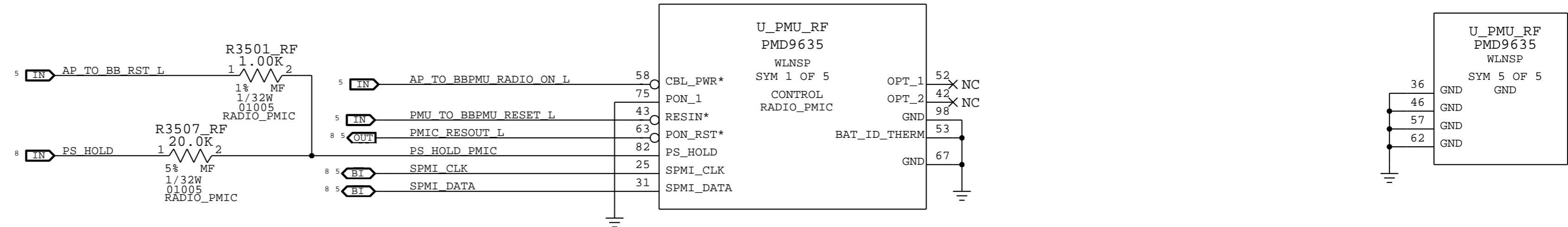
PAGE TITLE		DRAWING NUMBER	SIZE
CELLULAR BASEBAND: GPIOS		051-1902	D
Apple Inc.		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
		PAGE	34 OF 51
		SHEET	42 OF 59

PMU: CONTROL AND CLOCKS

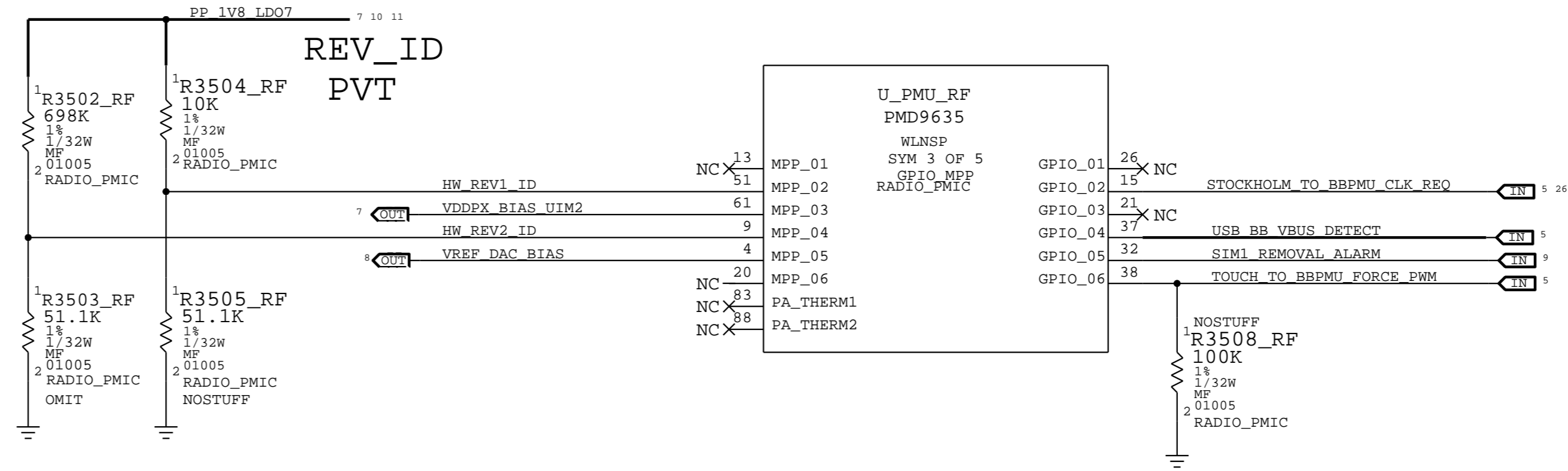
HW_REV2_ID	R3502	R3503	CONFIG
1.80V	698K	-	MLB
0.12V	698K	51.1K	SELF GEN

HW_REV_ID	R3504	R3505	REVISION
0.10V	887K	51.1K	DEV1
0.30V	255K	51.1K	DEV2
0.50V	124K	51.1K	DEV3
0.70V	82.5K	51.1K	DEV4/PROTOMLB1
0.90V	51.1K	51.1K	PROTOMLB2
1.10V	31.6K	51.1K	DEV5/PROTO1
1.20V	50K	100K	PROTO2
1.31V	39K	105K	EVT
1.43V	13.3K	51.1K	EVT_ALT
1.55V	8.25K	51.1K	CARRIER BUILD
1.67V	3.92K	51.1K	DVT
1.80V	10K	-	PVT

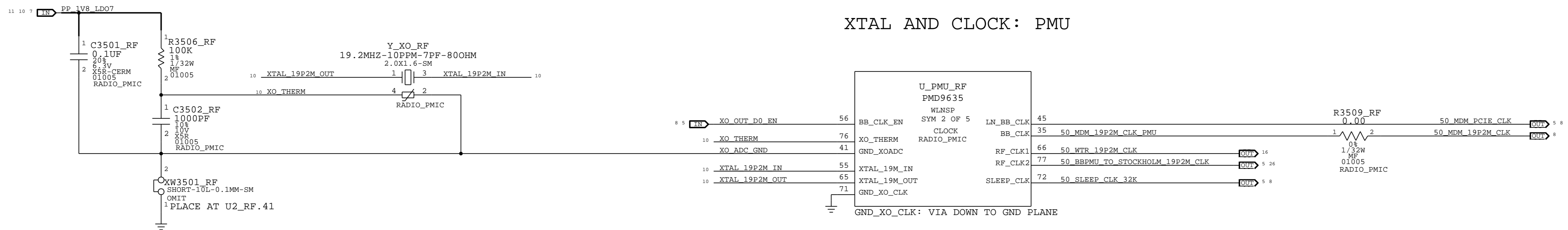
RESET AND CONTROL: PMU



MPPS AND GPIOs: PMU

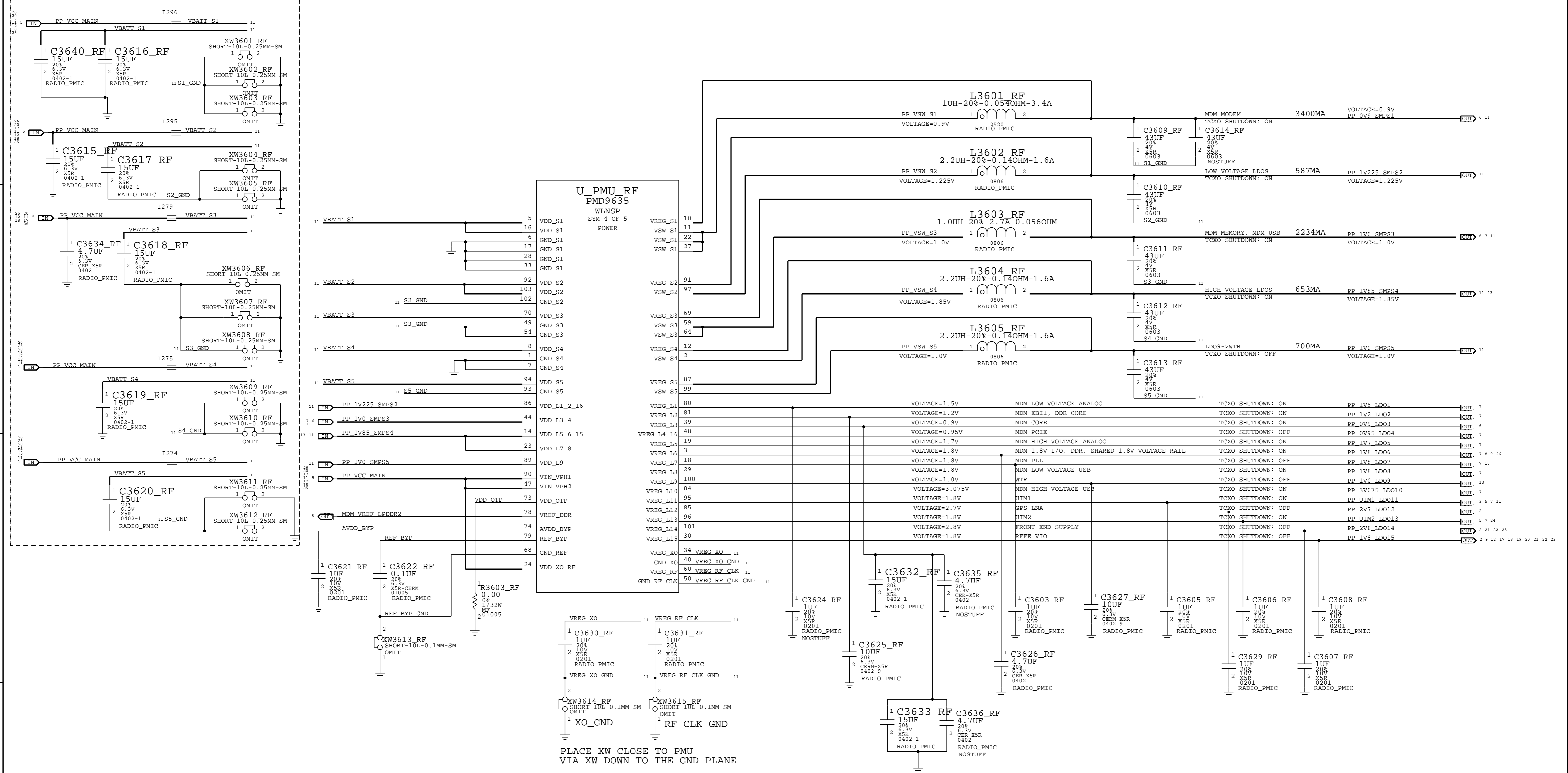


XTAL AND CLOCK: PMU



PMU: SWITCHERS AND LDOS

SWITCHERS BULK CAPS



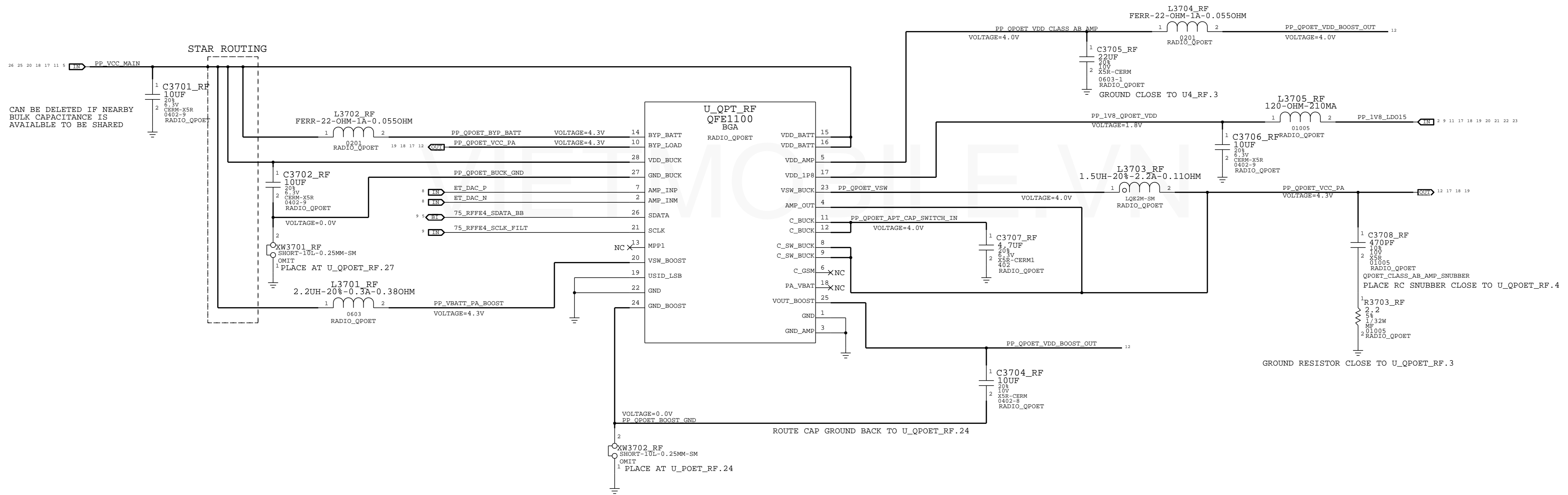
PLACE XW CLOSE TO PMU
VIA XW DOWN TO THE GND PLANE



PAGE TITLE		CELLULAR PMU: SWITCHERS AND LDOS	
DRAWING NUMBER	051-1902	SIZE	D
REVISION	A.0.0		
BRANCH			
PAGE	36 OF 51		
SHEET	44 OF 59		

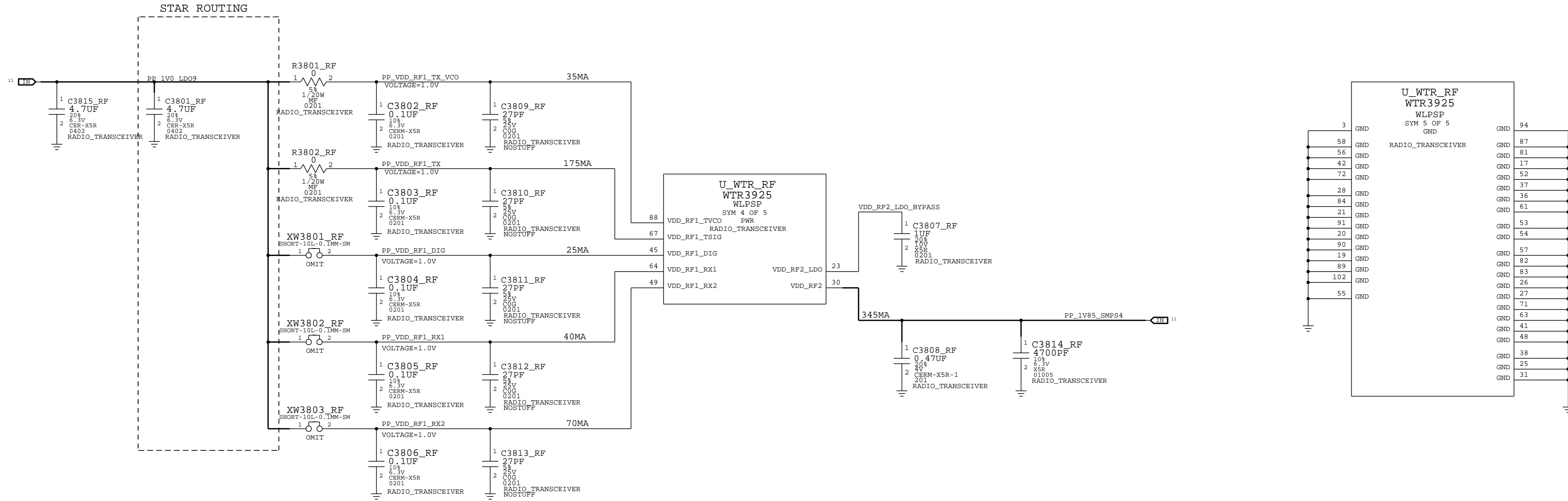
NOTICE OF PROPRIETARY PROPERTY:
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE
II NOT TO REPRODUCE OR COPY IT
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART
IV ALL RIGHTS RESERVED

PMU: ET MODULATOR



PAGE TITLE CELLULAR PMU: ET MODULATOR		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH
		PAGE 37 OF 51
		SHEET 45 OF 59

TRANSCEIVER: POWER



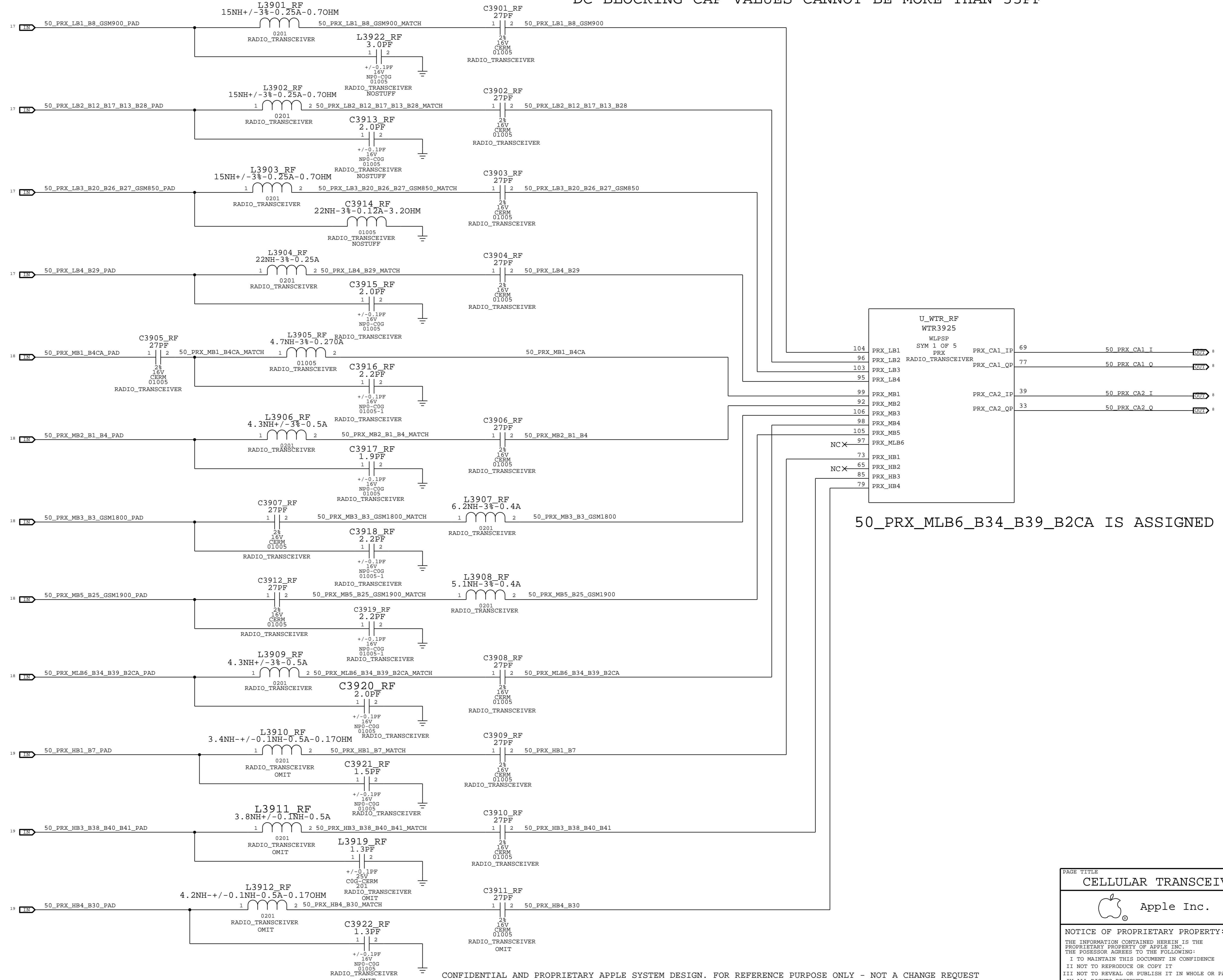
PAGE TITLE		CELLULAR TRANSCEIVER: POWER	
DRAWING NUMBER		051-1902	SIZE D
REVISION		A.0.0	
BRANCH			
PAGE		38 OF 51	
SHEET		46 OF 59	

NOTICE OF PROPRIETARY PROPERTY:
 THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:
 I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE
 II NOT TO REPRODUCE OR COPY IT
 III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART
 IV ALL RIGHTS RESERVED



TRANSCEIVER: PRX PORTS

DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF



50_PRX_MLB6_B34_B39_B2CA IS ASSIGNED TO MB4

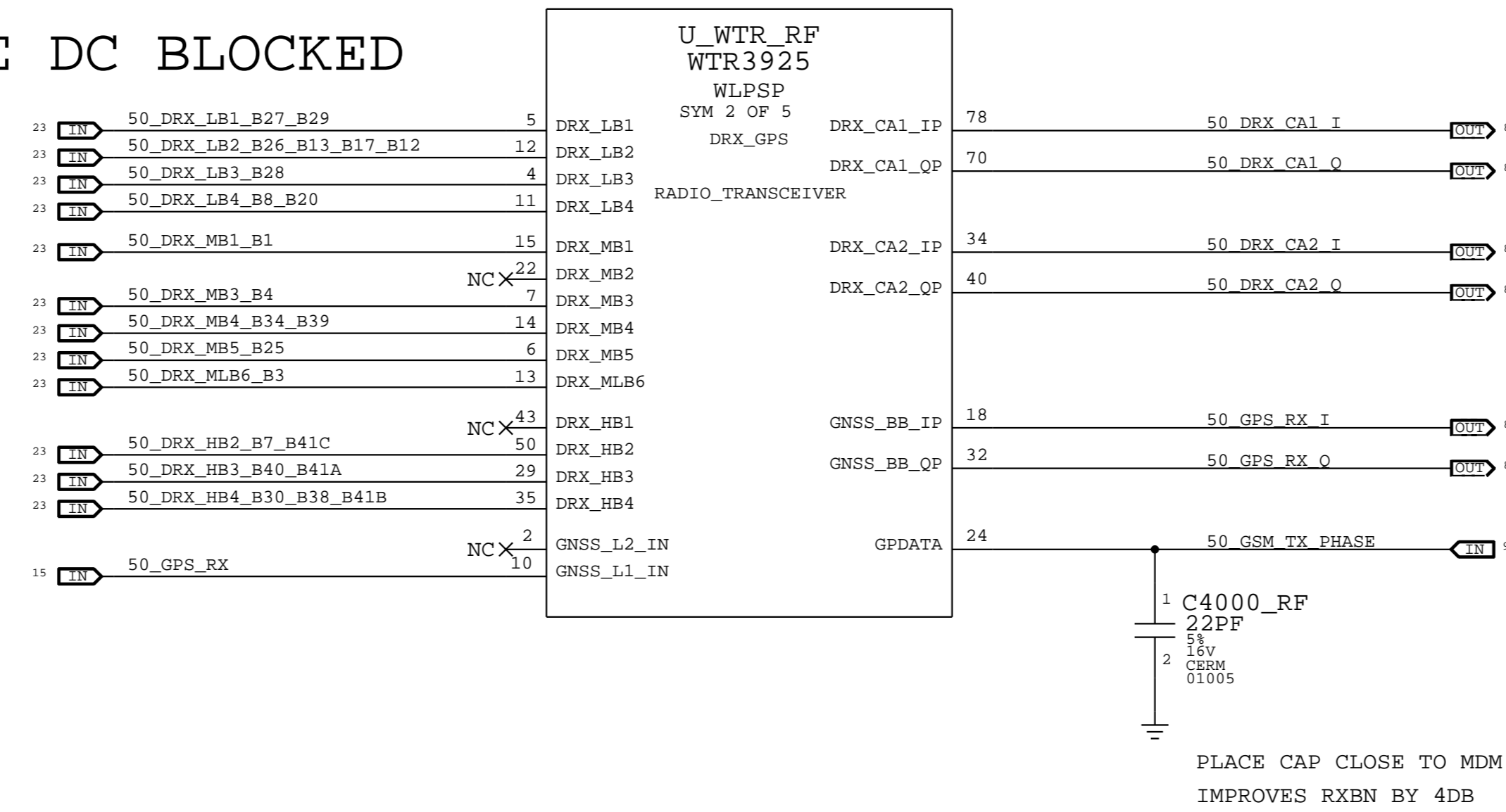


CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

PAGE TITLE		
CELLULAR TRANSCEIVER: PRX PORTS		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED	BRANCH	
	PAGE	39 OF 51
	SHEET	47 OF 59

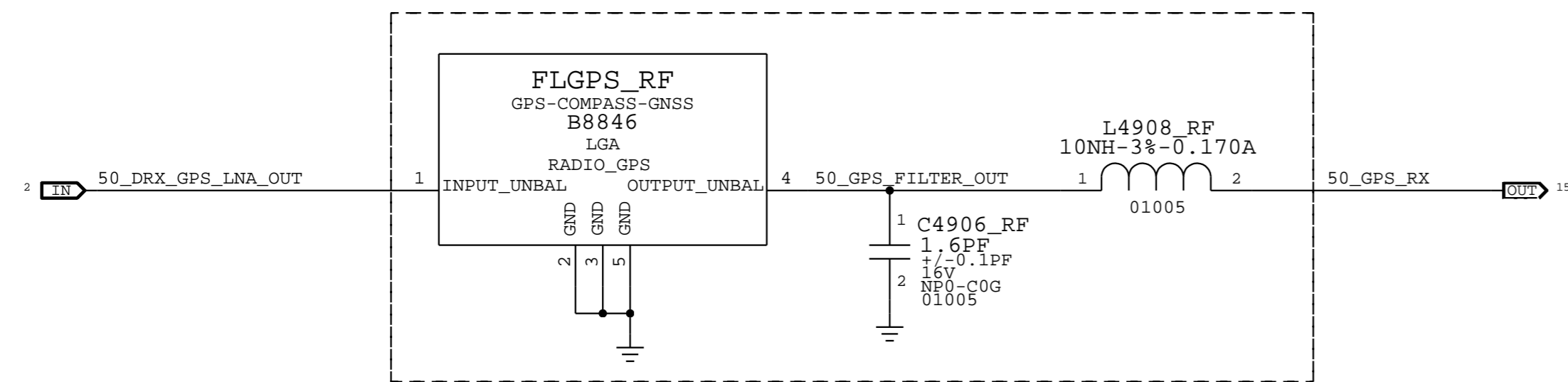
TRANSCEIVER: DRX/GPS PORTS

DRX MODULE PORTS ARE DC BLOCKED

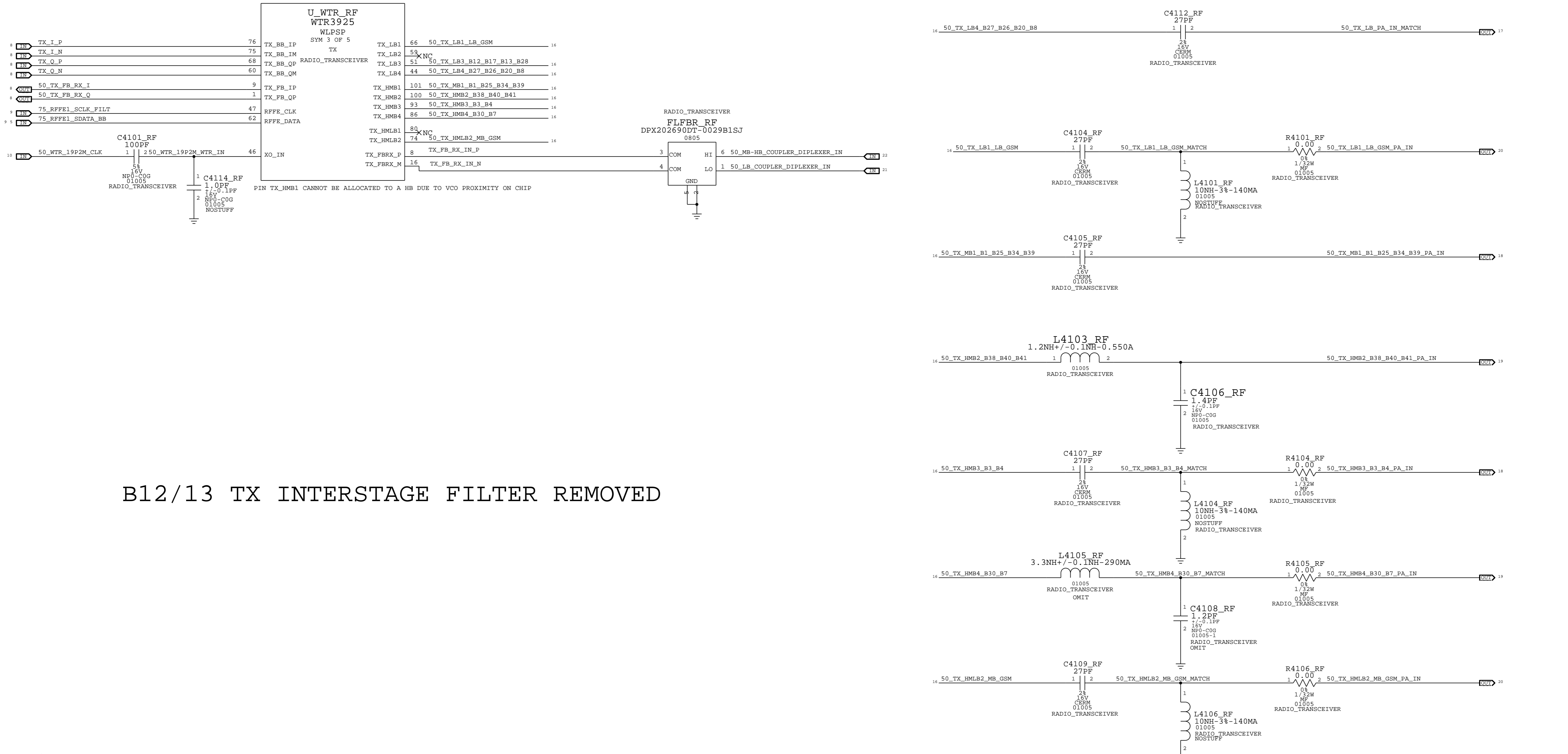


GPS FILTER

PLACE NEAR U_WTR



TRANSCEIVER: TX PORTS

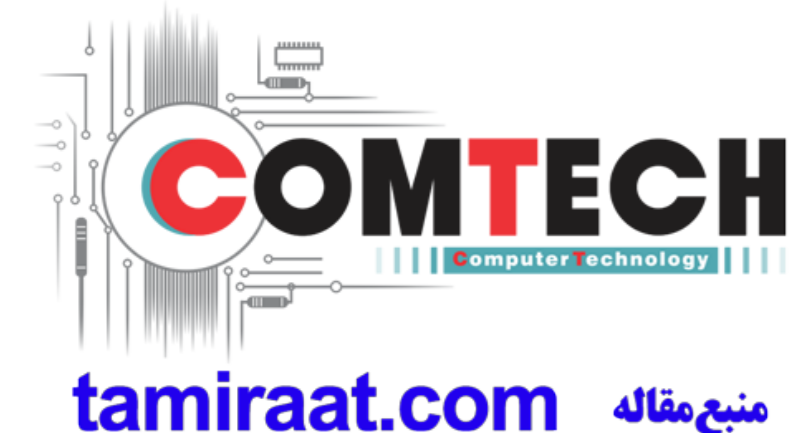
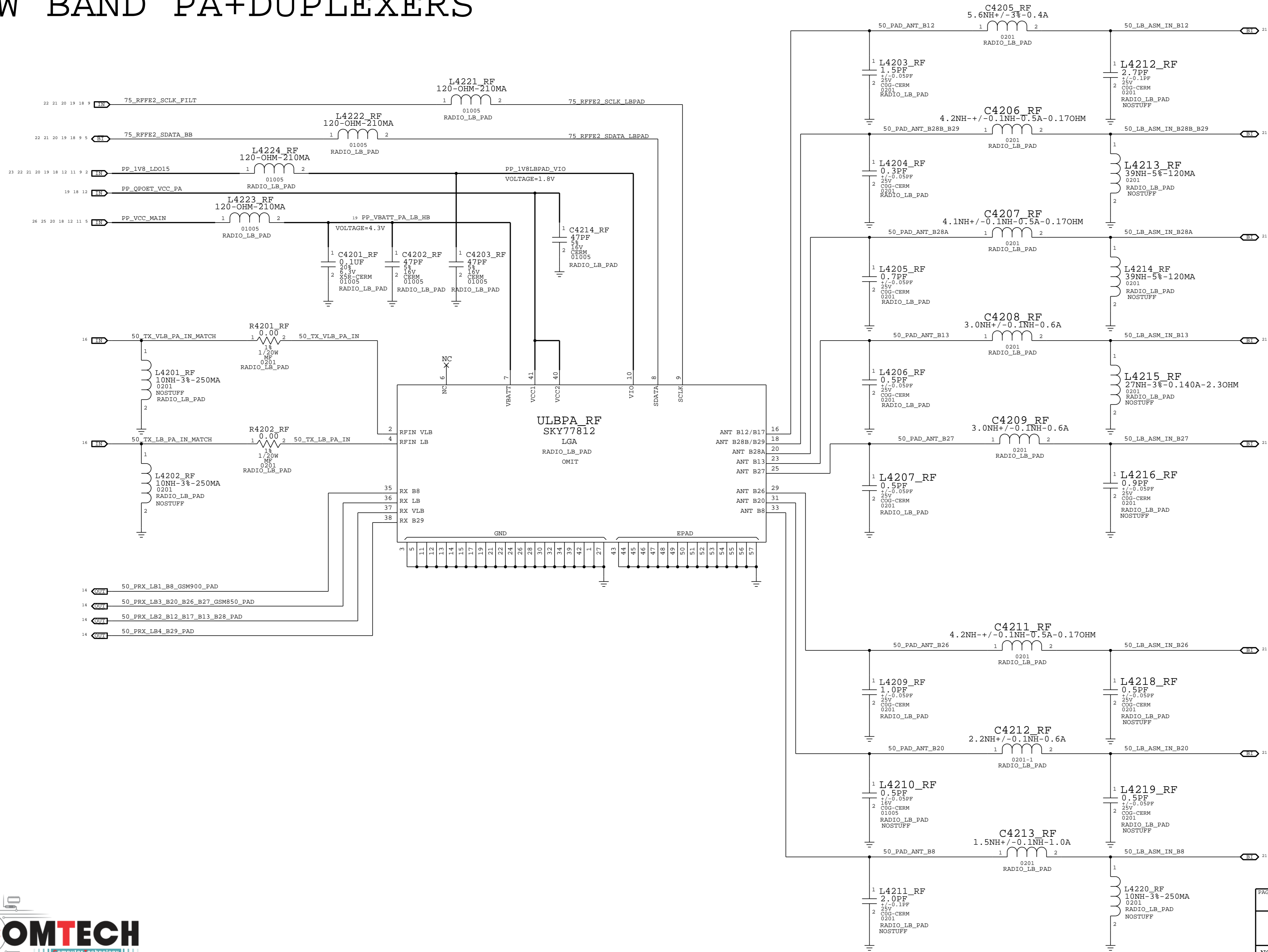


B12/13 TX INTERSTAGE FILTER REMOVED



PAGE TITLE		
CELLULAR TRANSCEIVER: TX PORTS		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED	BRANCH	
	PAGE	41 OF 51
	SHEET	49 OF 59

LOW BAND PA+DUPLEXERS



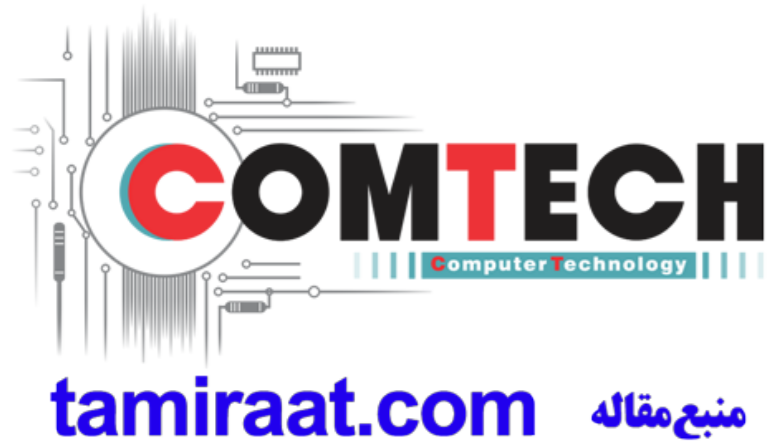
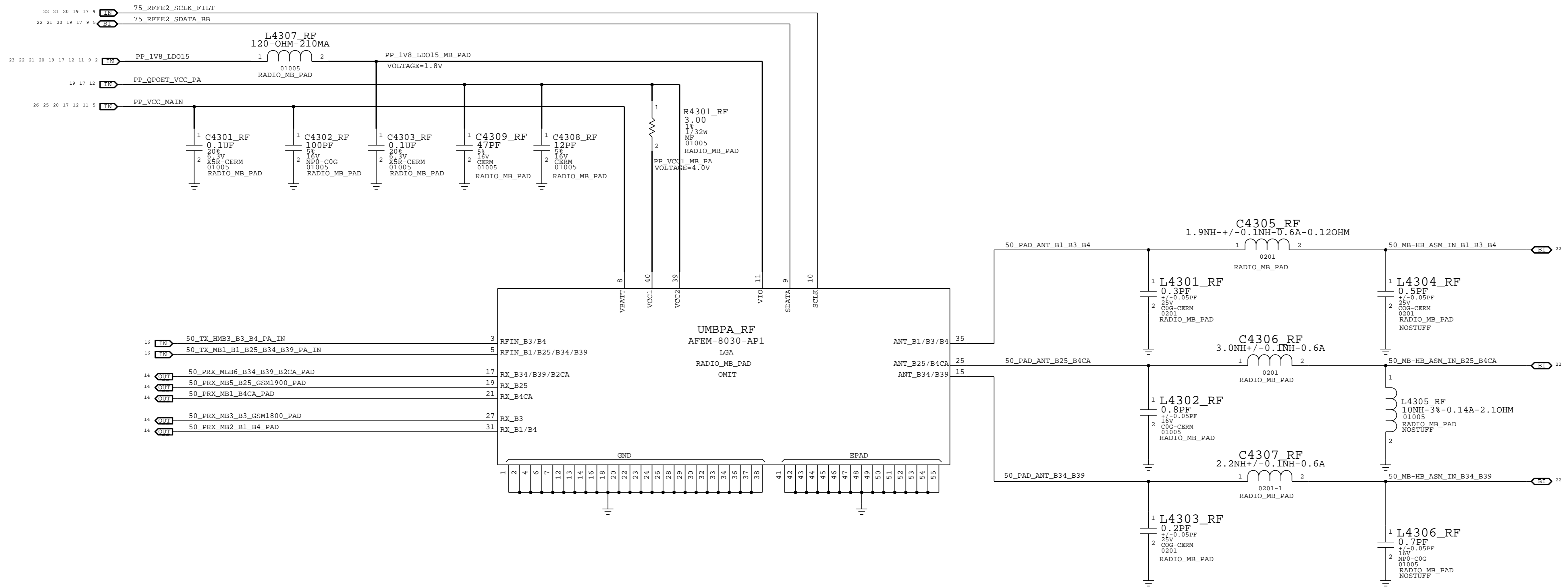
CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

PAGE TITLE		CELLULAR FRONT END: LB PAD	
DRAWING NUMBER		051-1902	SIZE D
REVISION		A.0.0	
BRANCH			
PAGE		42 OF 51	
SHEET		50 OF 59	

Apple Inc.

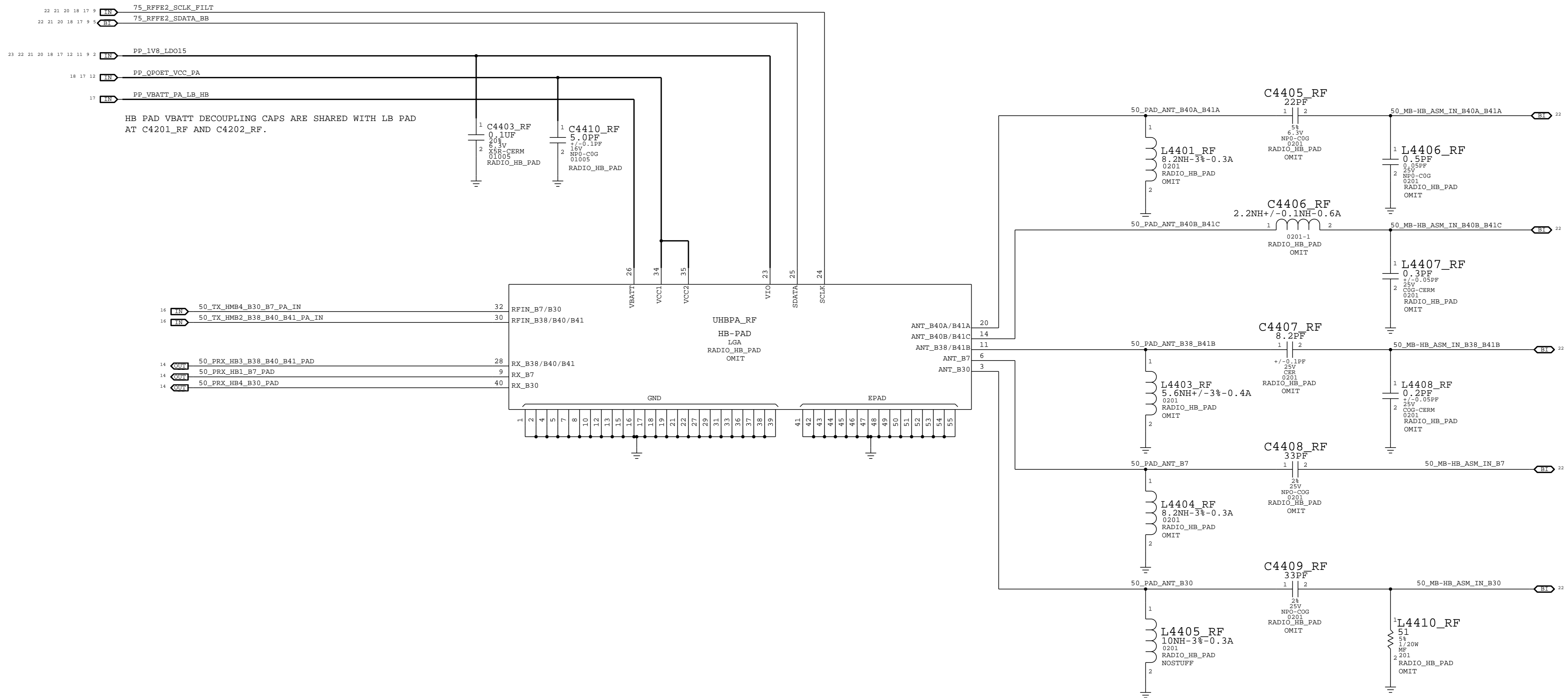
NOTICE OF PROPRIETARY PROPERTY:
 THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:
 I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE
 II NOT TO REPRODUCE OR COPY IT
 III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART
 IV ALL RIGHTS RESERVED

MID BAND PA+DUPLEXERS



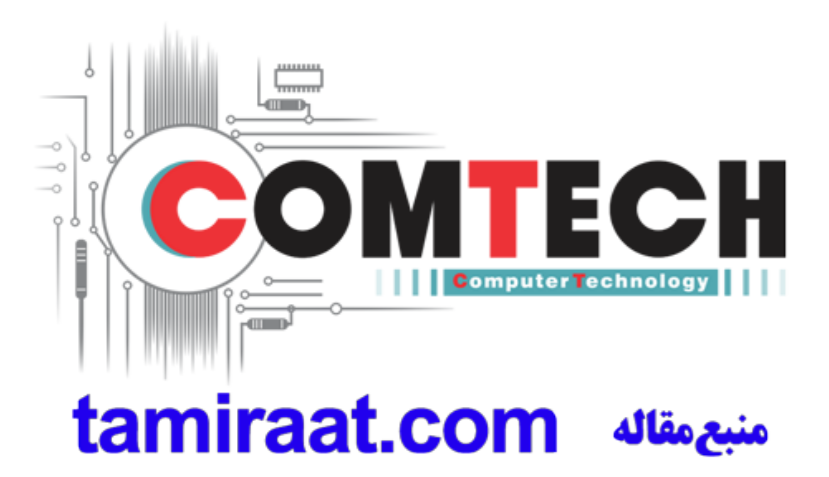
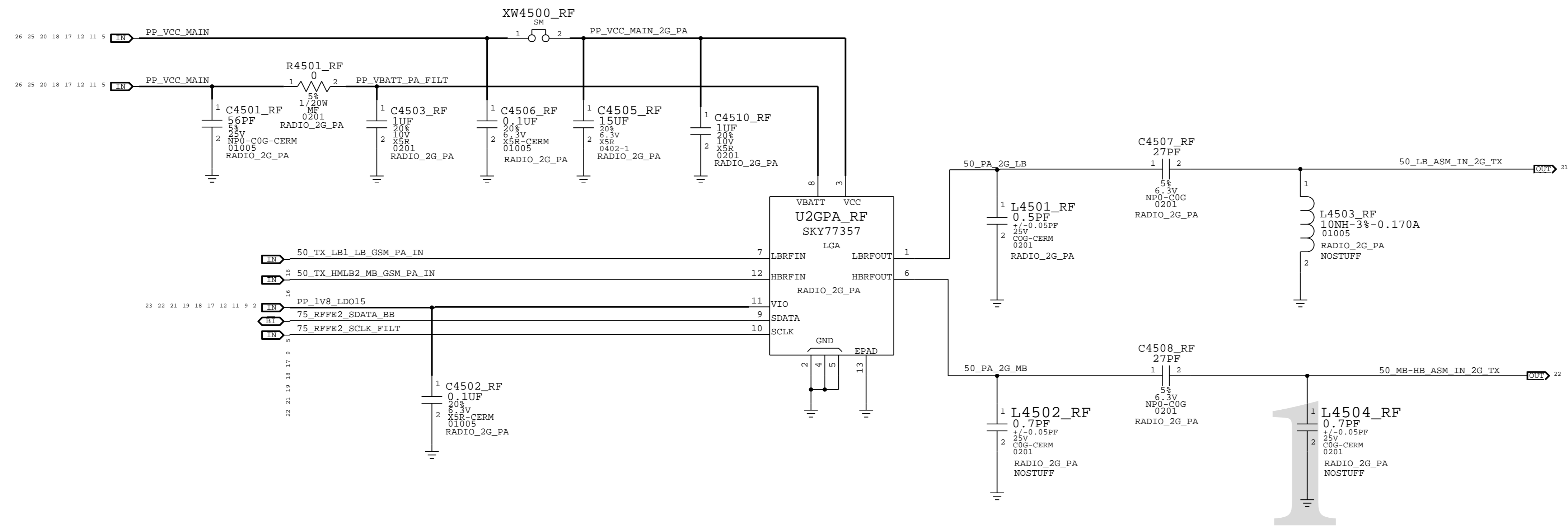
PAGE TITLE		CELLULAR FRONT END: MB PAD	
DRAWING NUMBER	051-1902	SIZE	D
	REVISION		A.0.0
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE	43 OF 51
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		SHEET	51 OF 59
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			

HIGH BAND PA+DUPLEXERS



PAGE TITLE CELLULAR FRONT END: HB PAD		
Apple Inc.	DRAWING NUMBER 051-1902	SIZE D
	REVISION A.0.0	BRANCH
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		
PAGE 44 OF 51	SHEET 52 OF 59	

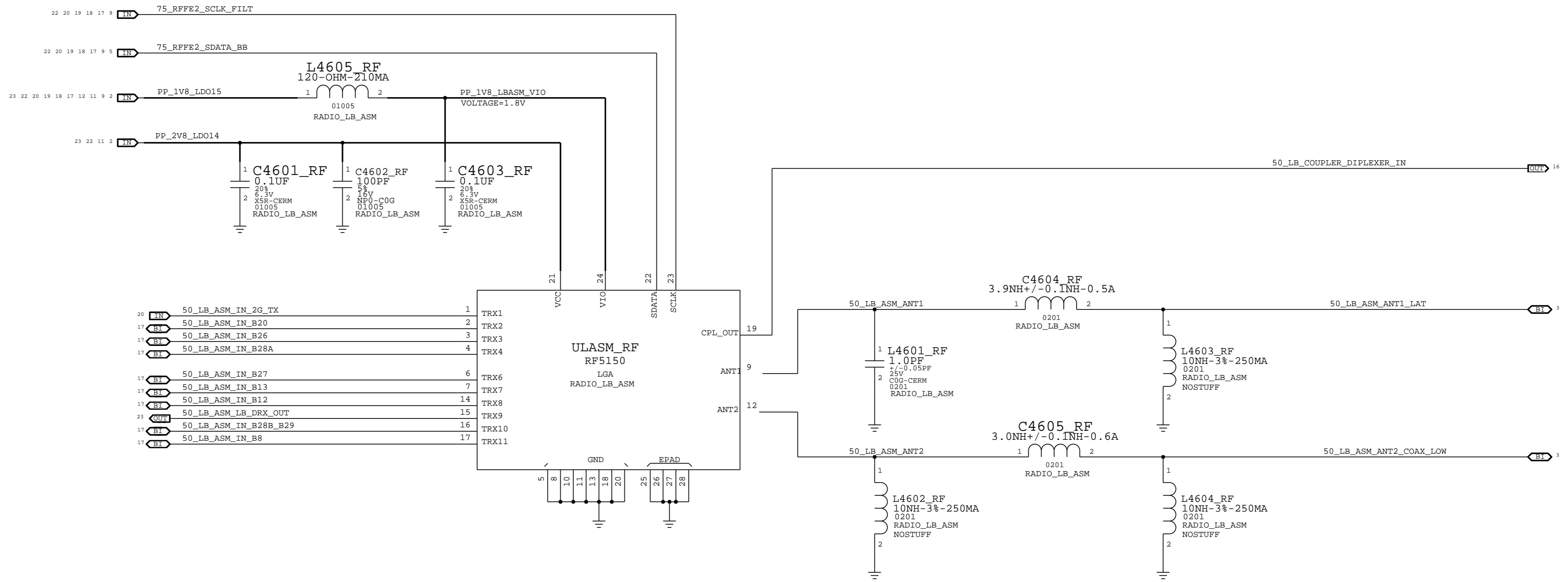
2G PA



CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

PAGE TITLE CELLULAR FRONT END: 2G PA		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH
		PAGE 45 OF 51
		SHEET 53 OF 59

LOW BAND ANTENNA SWITCH MODULE

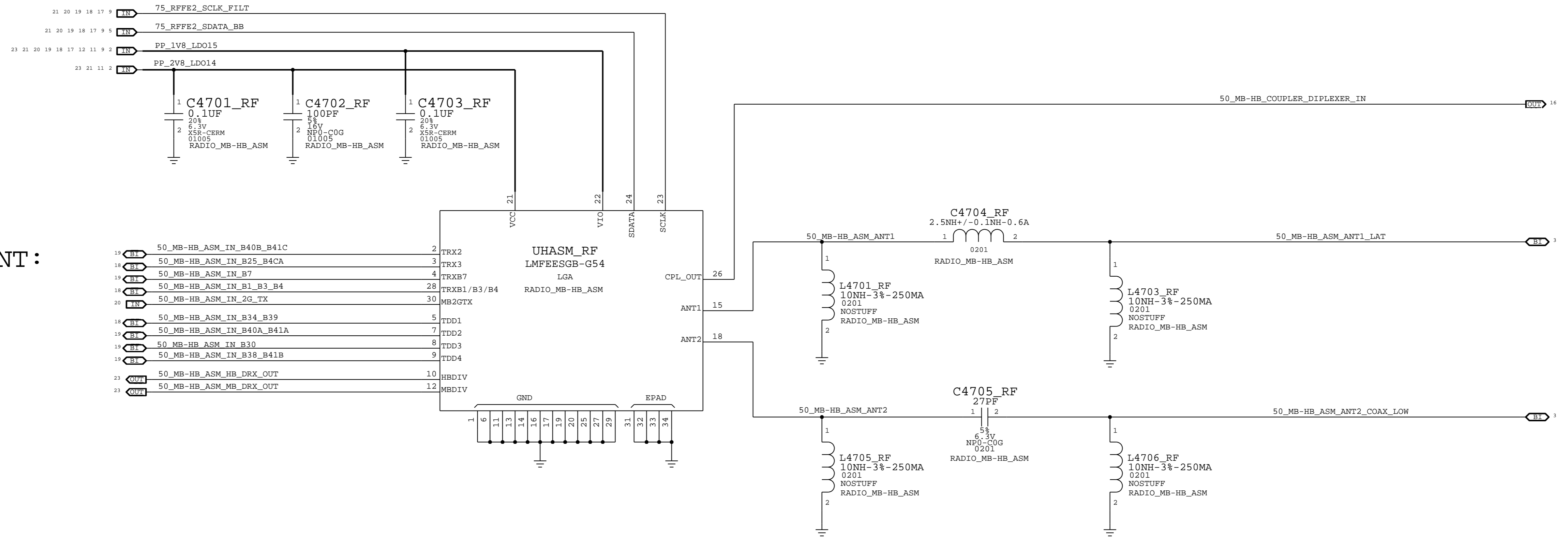


CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

PAGE TITLE		CELLULAR FRONT END: LB ASM	
DRAWING NUMBER		051-1902	SIZE D
REVISION		A.0.0	
NOTICE OF PROPRIETARY PROPERTY:		BRANCH	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:		PAGE	
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE		46 OF 51	
II NOT TO REPRODUCE OR COPY IT		SHEET	
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART		54 OF 59	
IV ALL RIGHTS RESERVED			

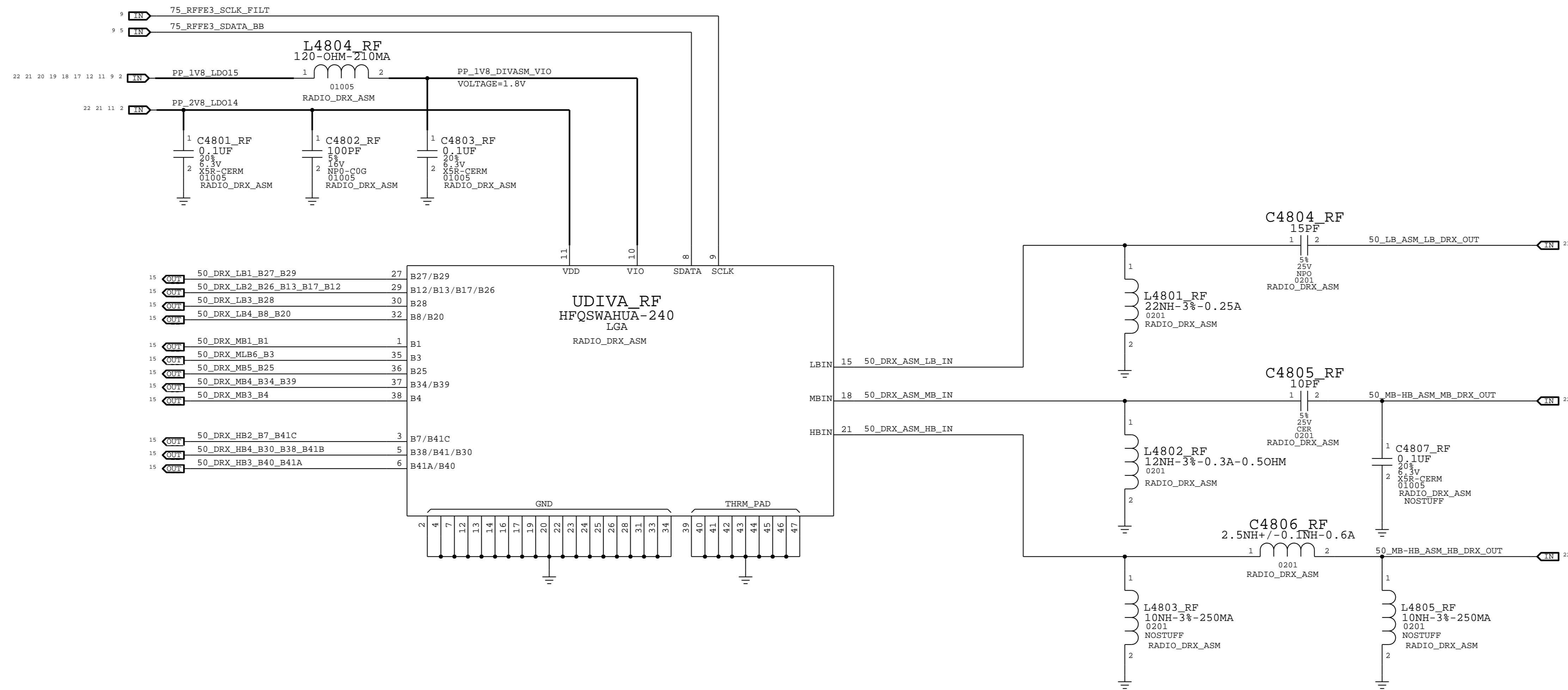
MID-HIGH BAND ANTENNA SWITCH MODULE

EVT ASM ASSIGNMENT:
 B40B/B41C - TRX2
 B30 - TDD3



PAGE TITLE		CELLULAR FRONT END: MB-HB ASM	
DRAWING NUMBER		051-1902	SIZE D
REVISION		A.0.0	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	PAGE
			47 OF 51
		SHEET	55 OF 59

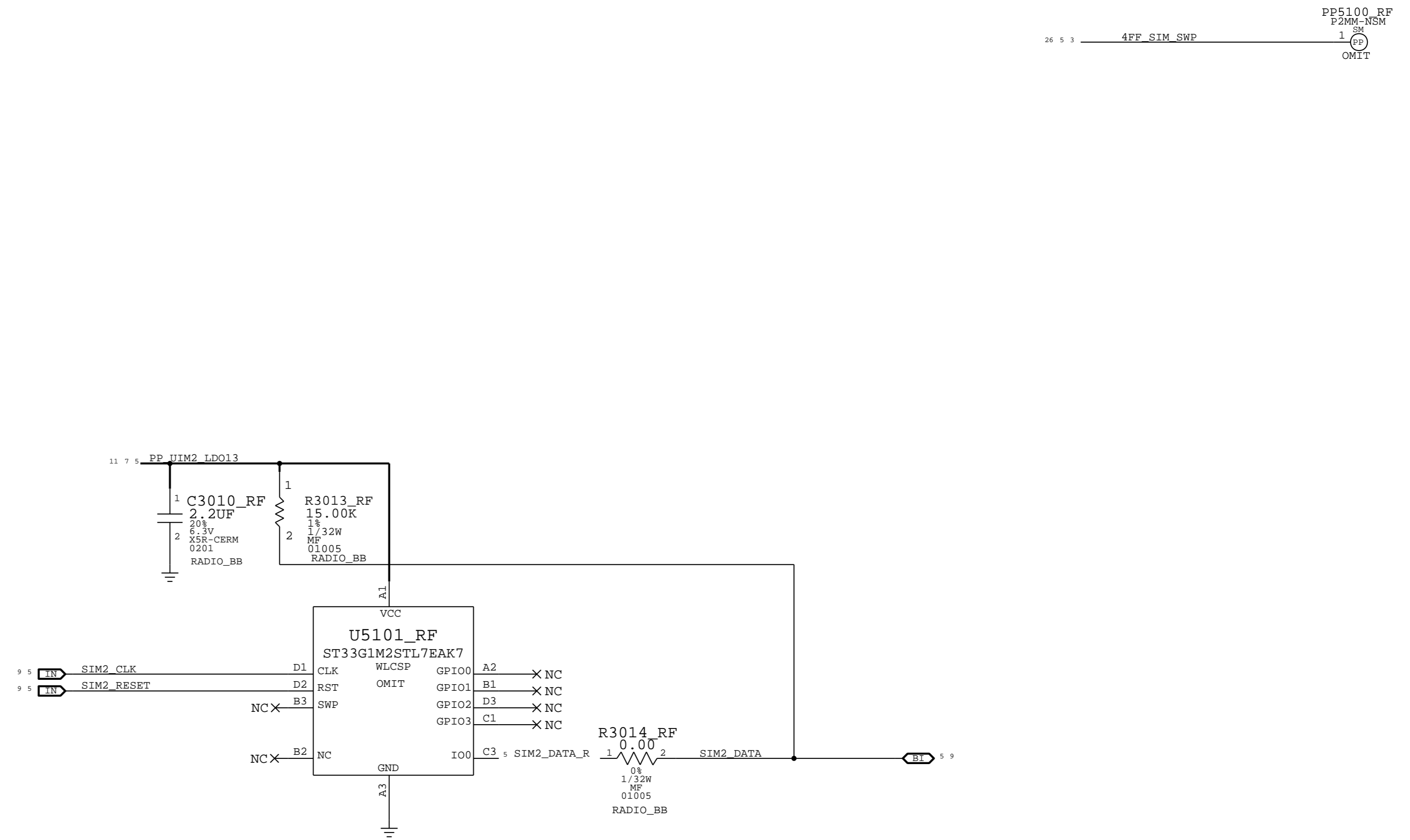
DIVERSITY MODULE



CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

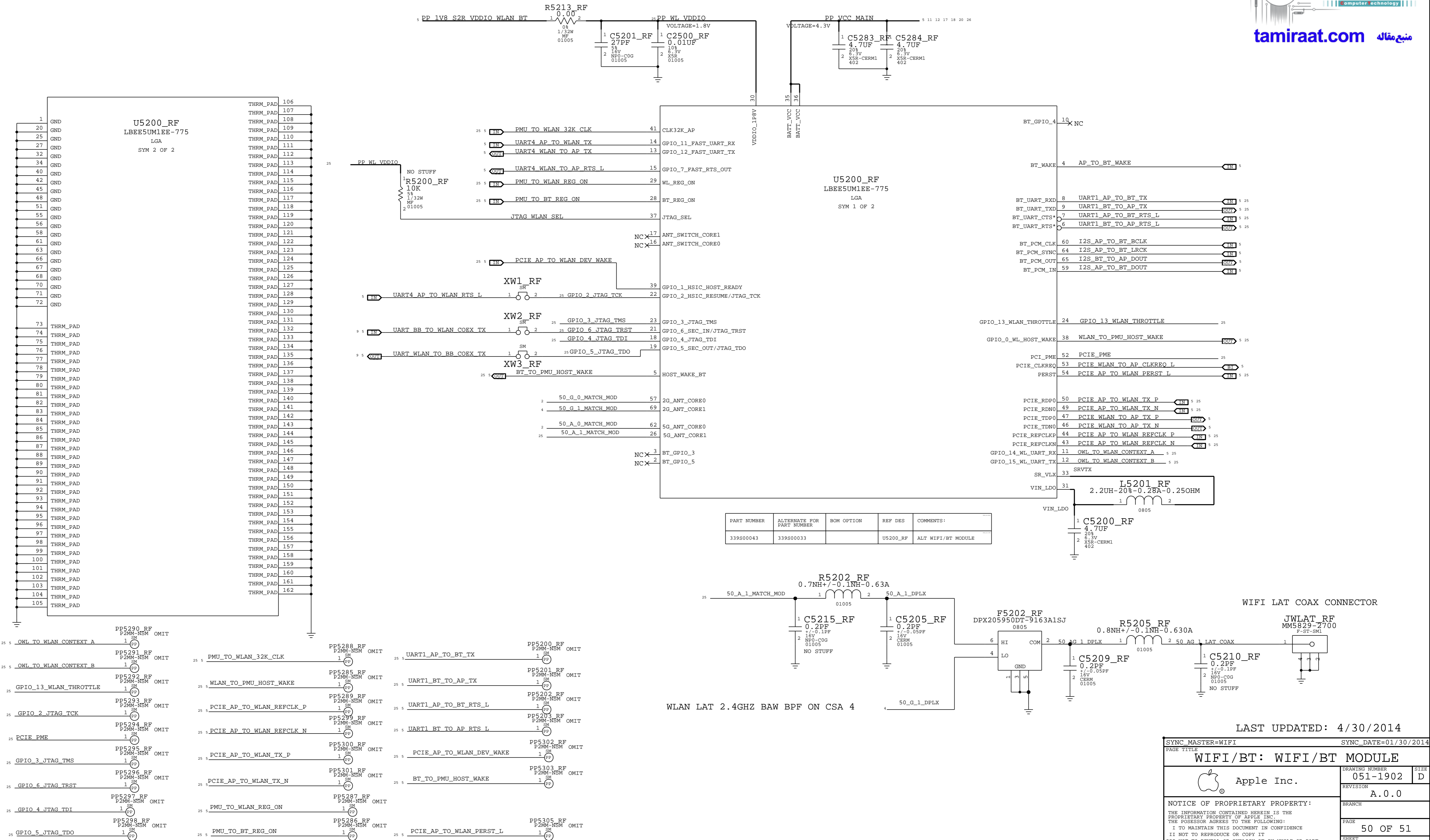
PAGE TITLE CELLULAR FRONT END: DIVERSITY		
Apple Inc.	DRAWING NUMBER	051-1902
	REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		
BRANCH	PAGE	48 OF 51
SHEET	56 OF 59	

SIM



PAGE TITLE		SIM	
Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
		PAGE	49 OF 51
		SHEET	57 OF 59

WIFI / BT



PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339S00043	339S00033		U5200_RF	ALT WIFI/BT MODULE

LAST UPDATED: 4/30/2014

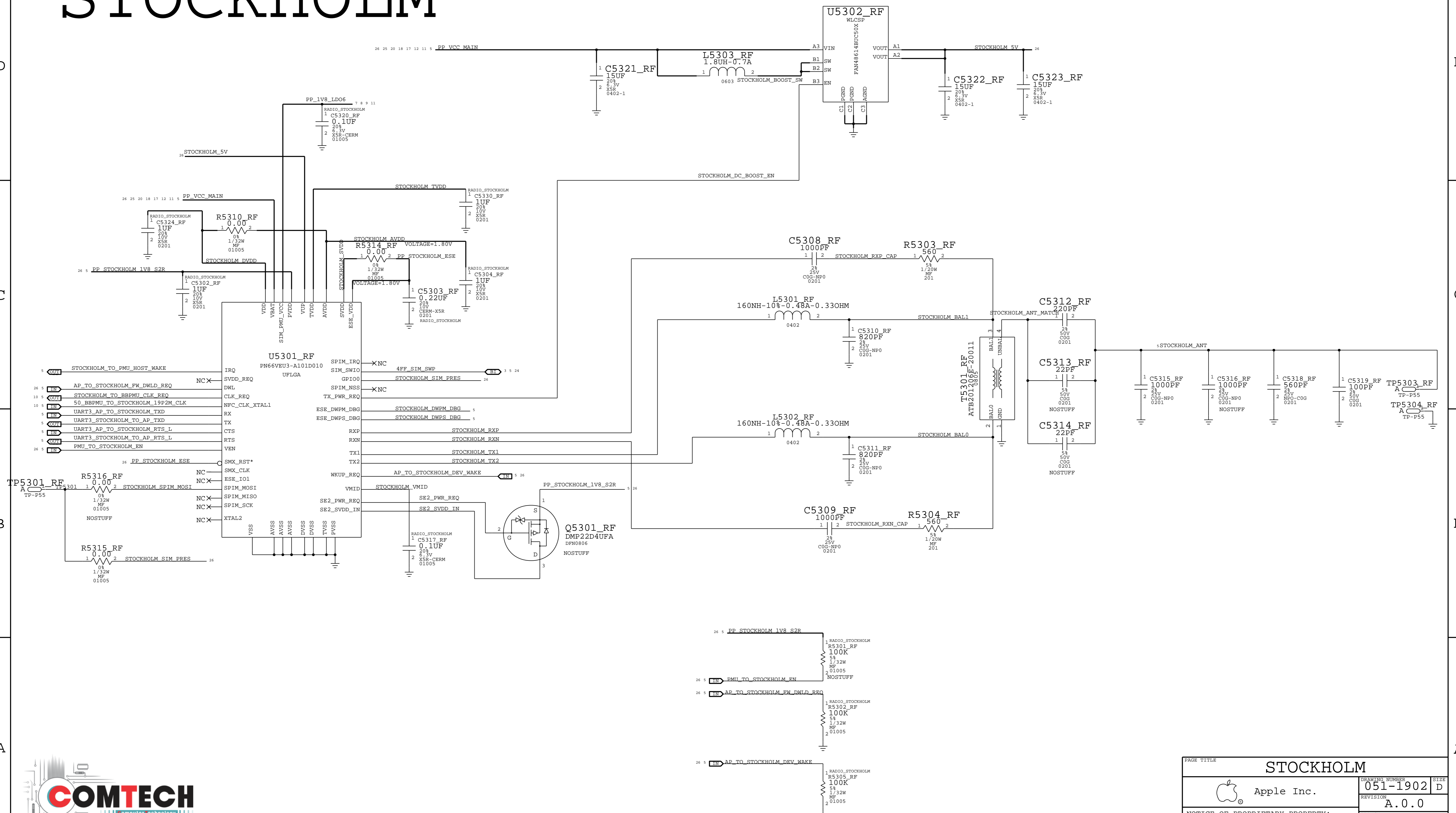
SYNC_MASTER=WIFI		SYNC_DATE=01/30/2014	
PAGE TITLE			
WIFI/BT: WIFI/BT MODULE			
Apple Inc.	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
NOTICE OF PROPRIETARY PROPERTY:			
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING:			
I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE			
II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			
BRANCH	50 OF 51		
PAGE	58 OF 59		
SHEET			

STOCKHOLM

ALL NETNAMES NEED TO BE CHECKED

D
C
B
A

D
C
B
A



PAGE TITLE		STOCKHOLM	
DRAWING NUMBER		051-1902	SIZE D
REVISION		A.0.0	
NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH	
PAGE	51 OF 51	SHEET	59 OF 59