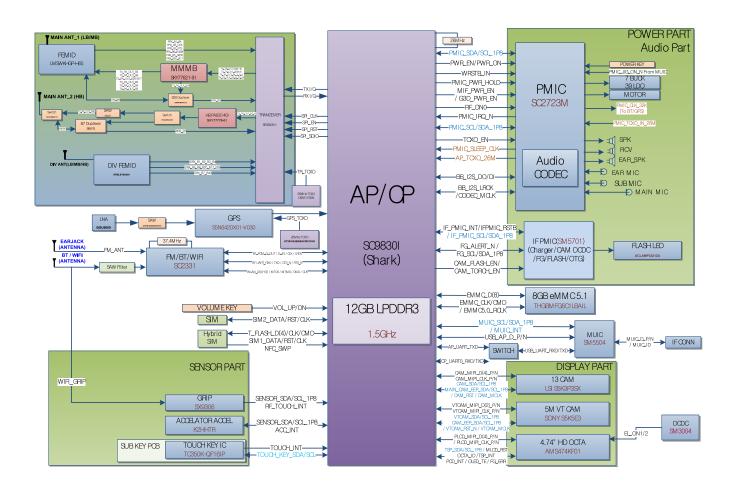
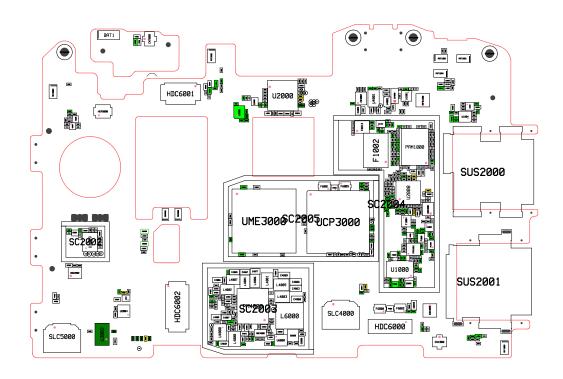


8-2. Block Diagram



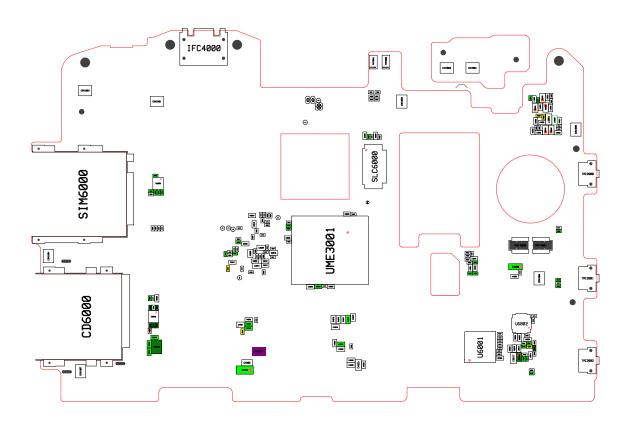


TOP



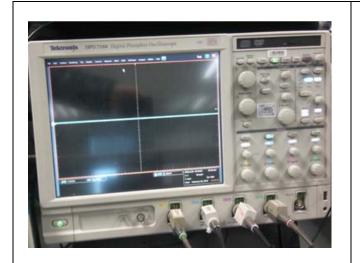


BOTTOM





8-3. Flow Chart of Troubleshooting





Oscilloscope

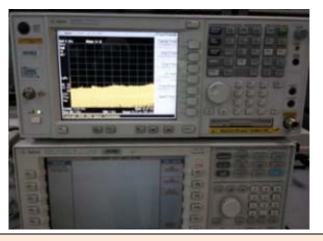
Digital Multimeter





Power Supply

+ driver, ESD Safe Tweezer



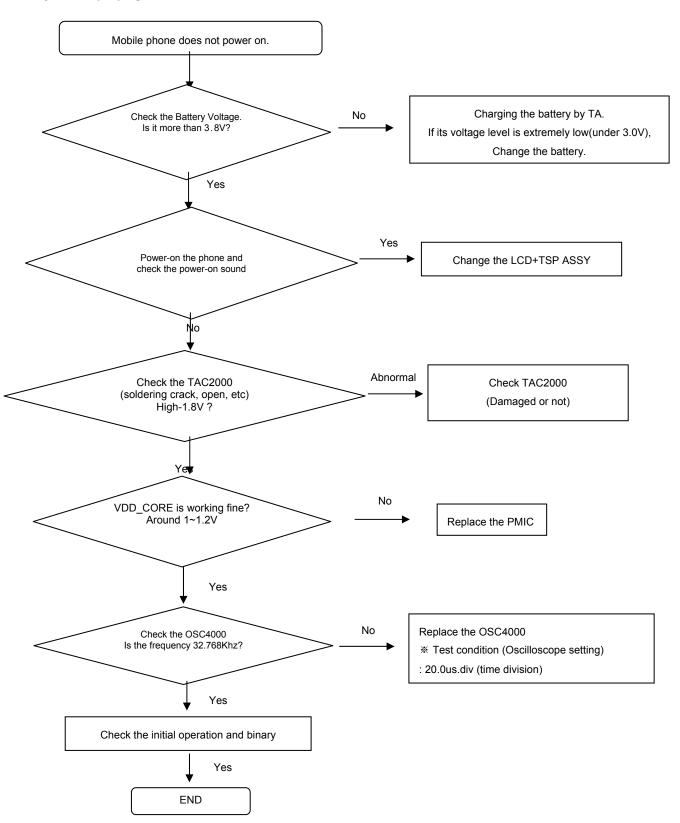


8960 & Spectrum Analyzer

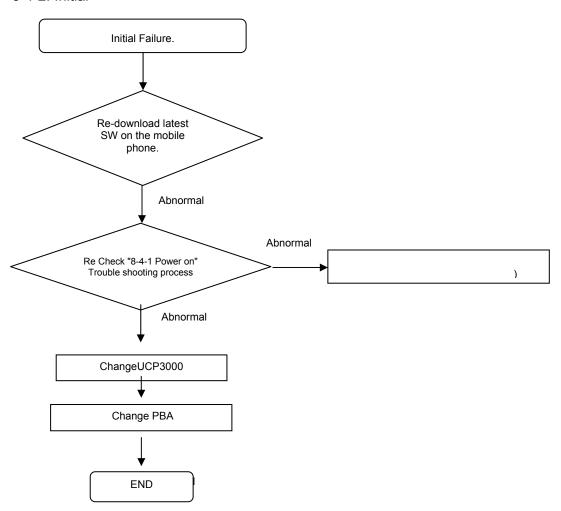
Soldering iron



8-4-1. Power On

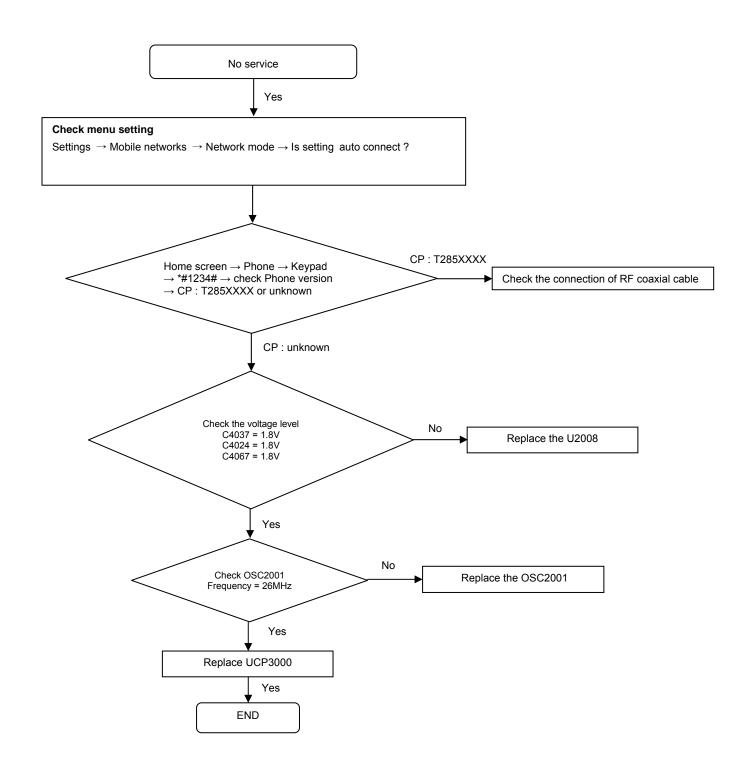


8-4-2. Initial



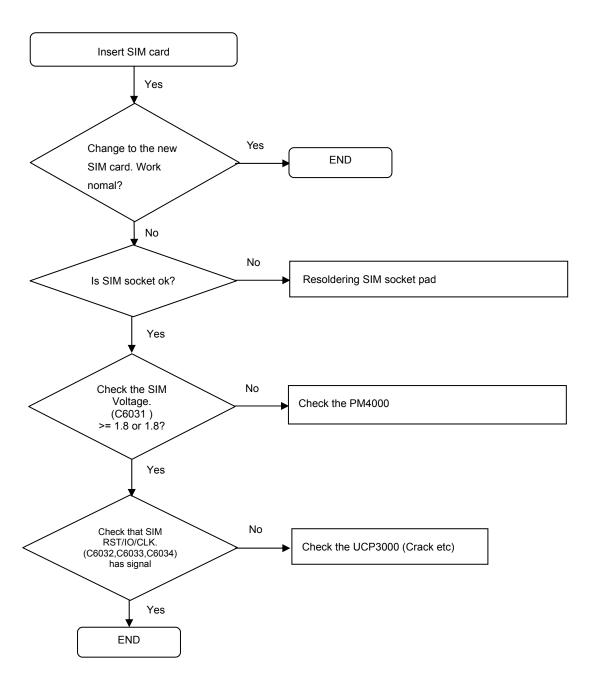


8-4-3. No Service



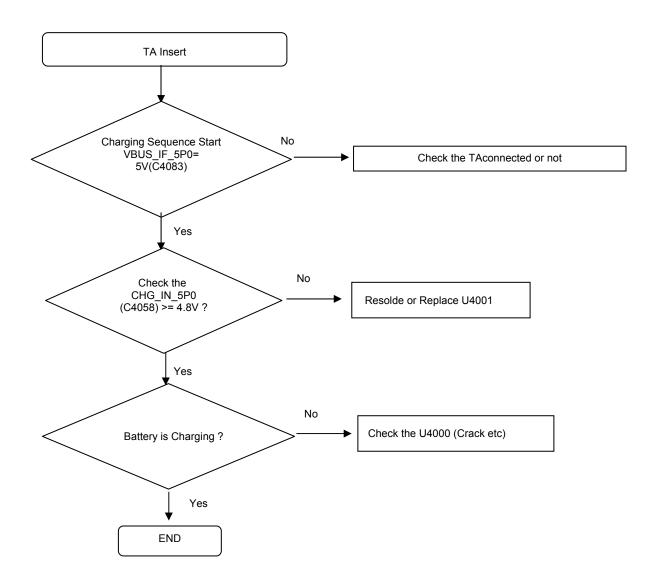


8-4-4. Sim Part



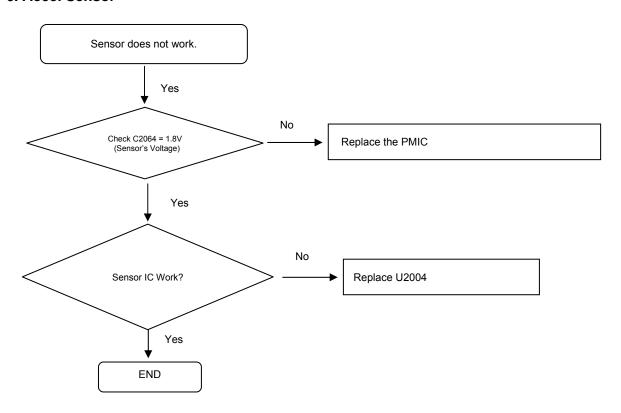


8-4-5. Charging Part



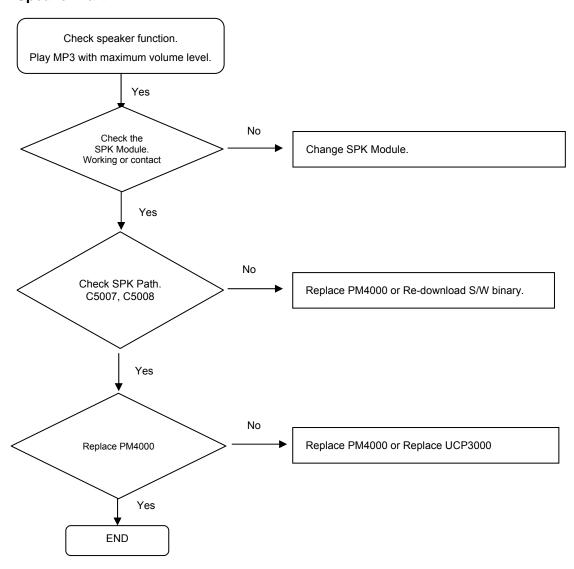


8-4-6. Accel Sensor



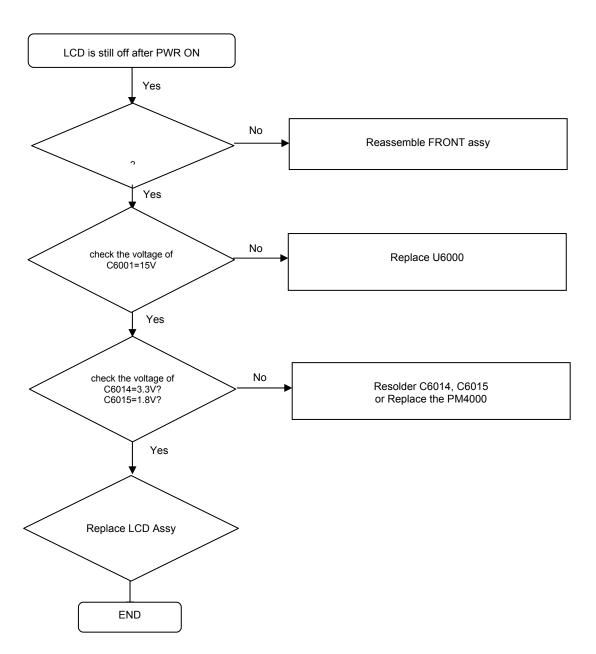


8-4-7. Speaker Part



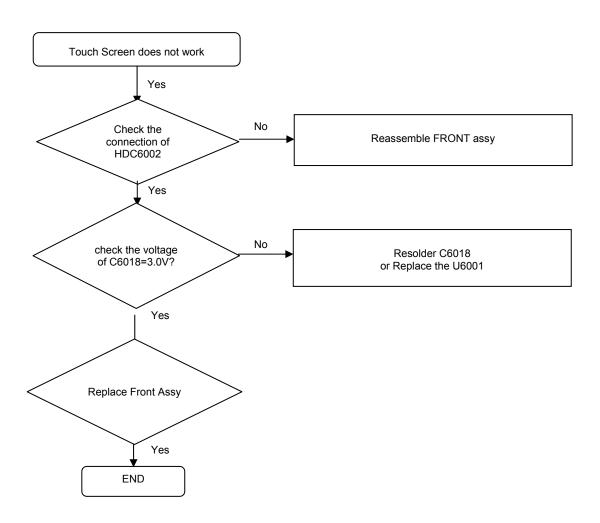


8-4-8. LCD



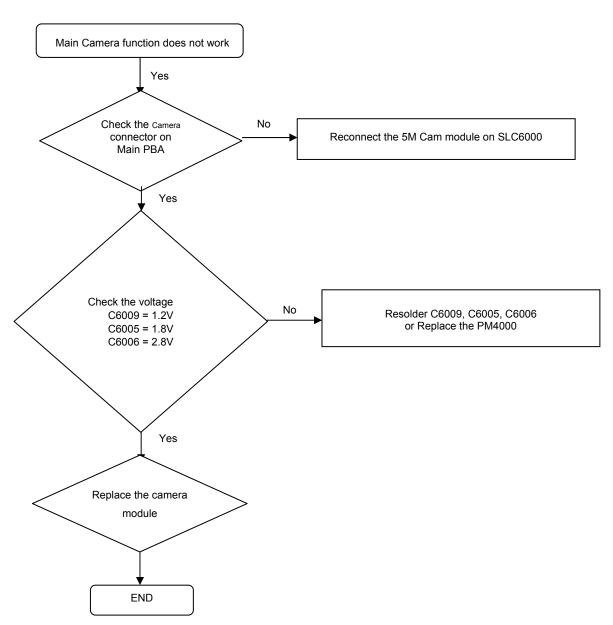


8-4-9. TSP



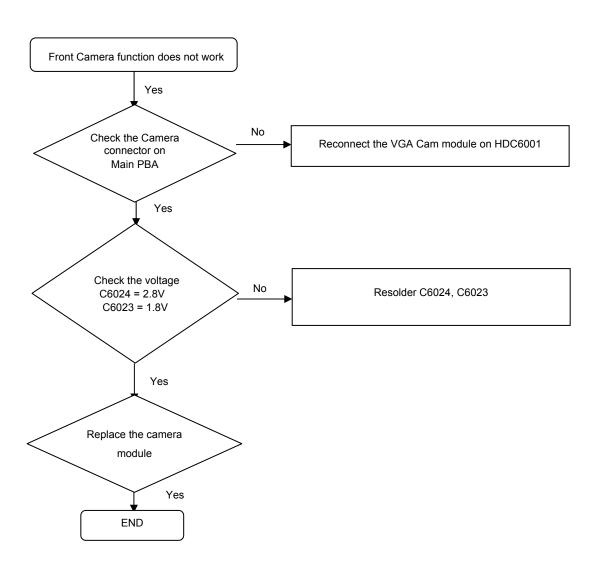


8-4-10. 5M CAMERA



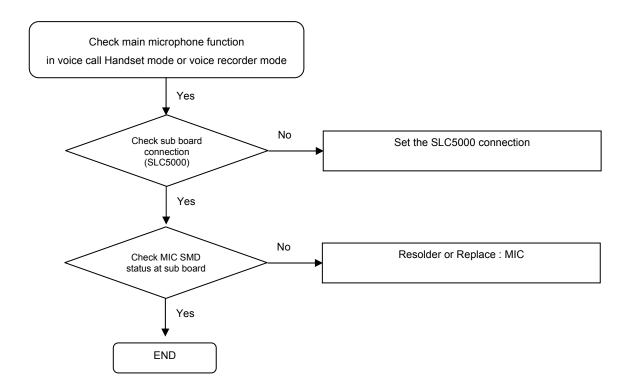


8-4-11. 2M CAMERA

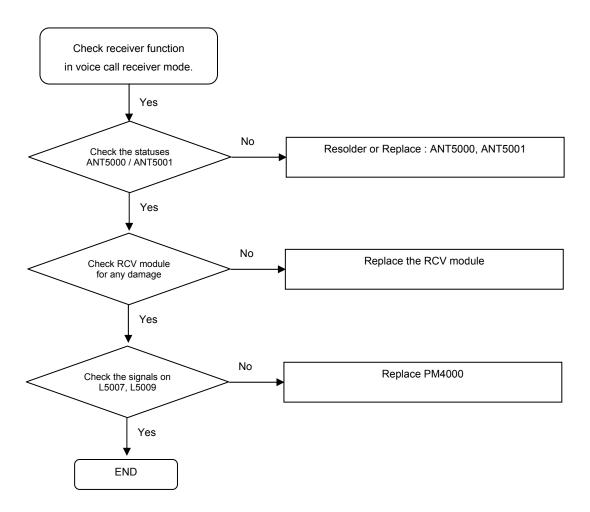




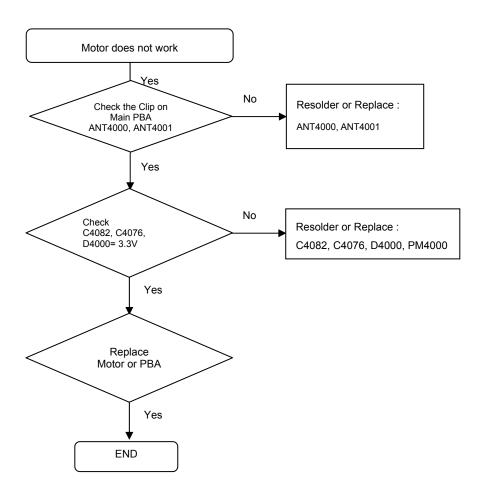
8-4-12. Microphone Part - Main MIC



8-4-13. Receiver Part

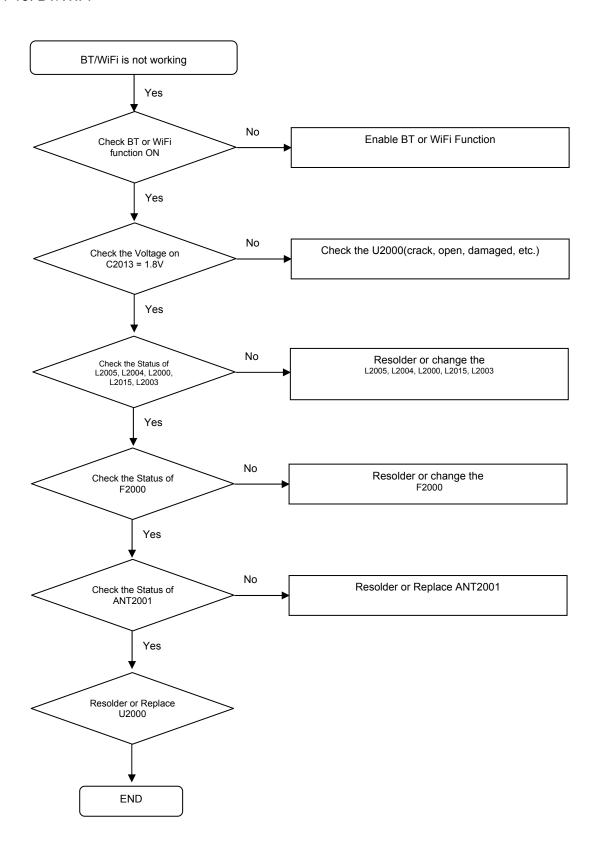


8-4-14. Motor

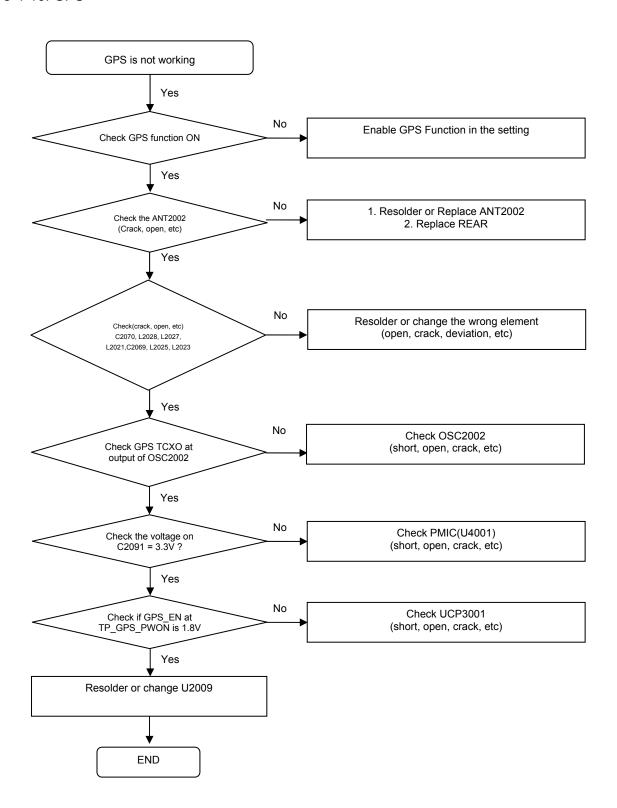




8-4-15. BT/WIFI

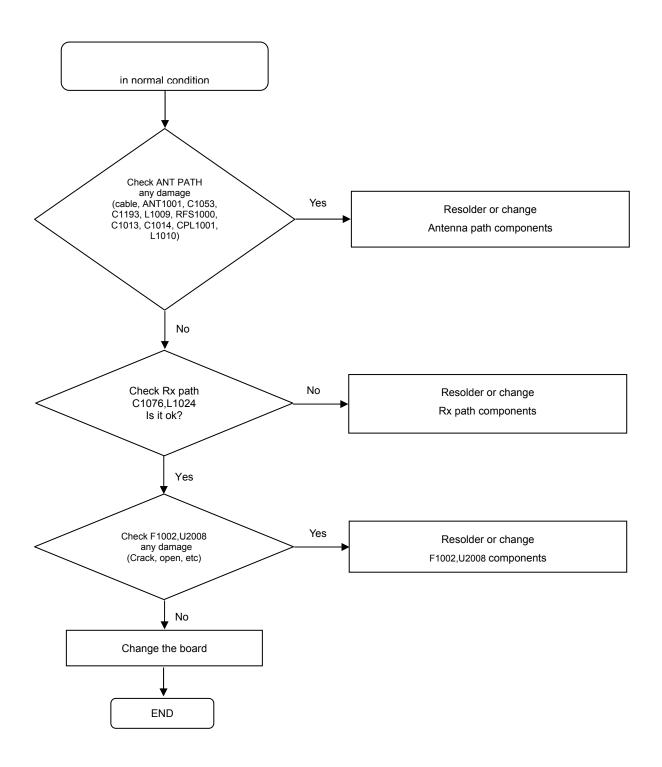


8-4-16. GPS

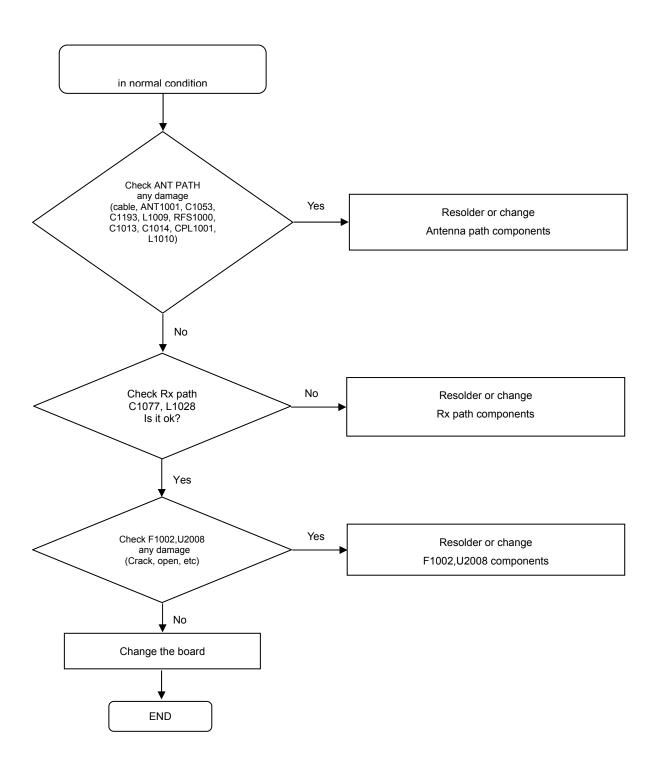




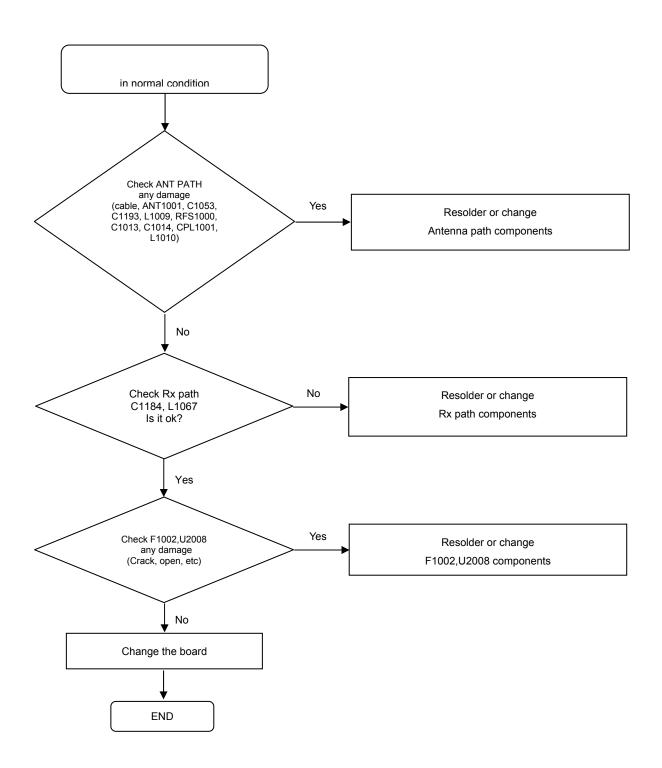
8-4-17. GSM 850 / WCDMA 850 / LTE B5 RX



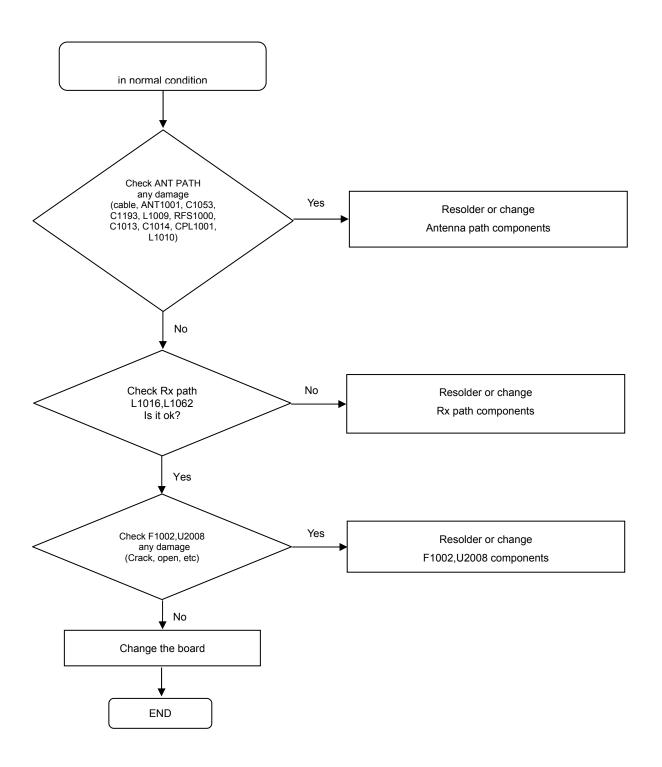
8-4-18. GSM 900 / WCDMA 900 RX



8-4-19. GSM 1800 / LTE B3 RX

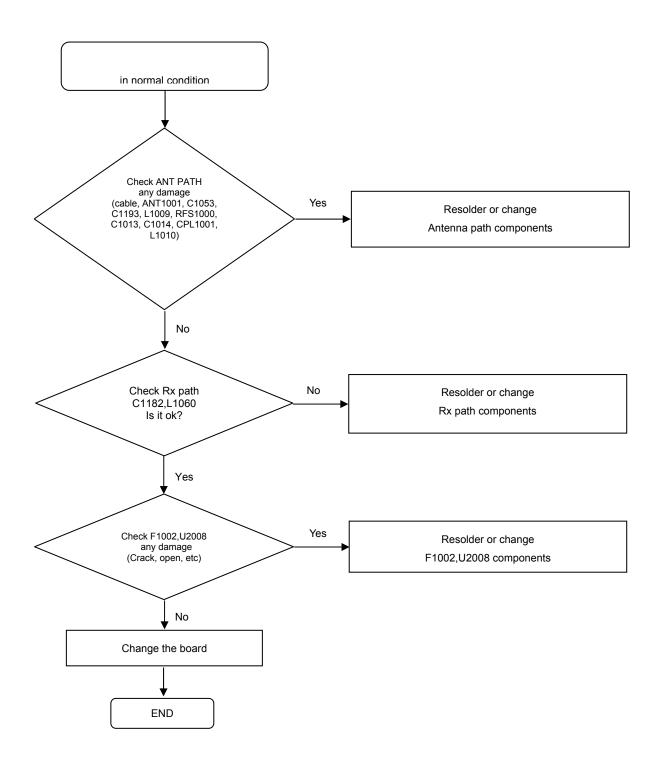


8-4-20. GSM 1900 / WCDMA 1900/LTE B2 RX

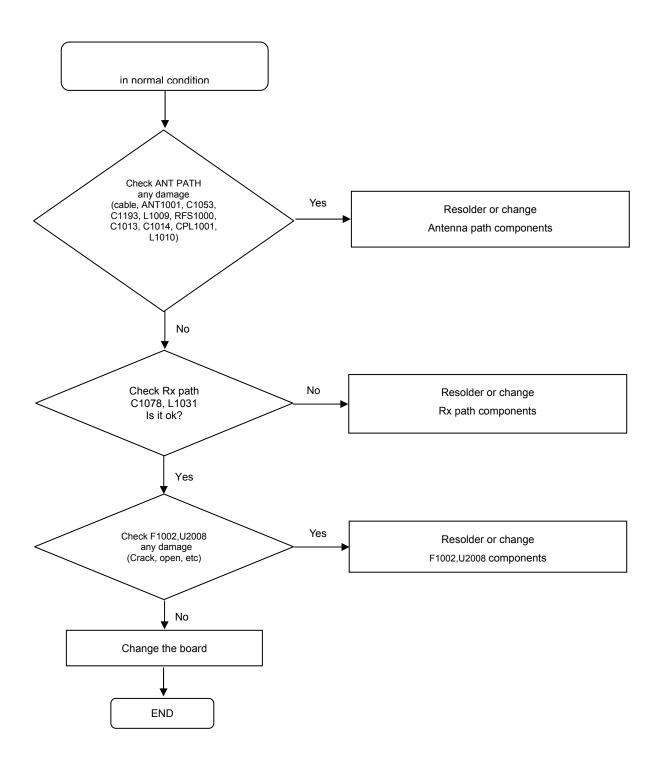




8-4-21. WCDMA 2100 / LTE B1 / B4 RX

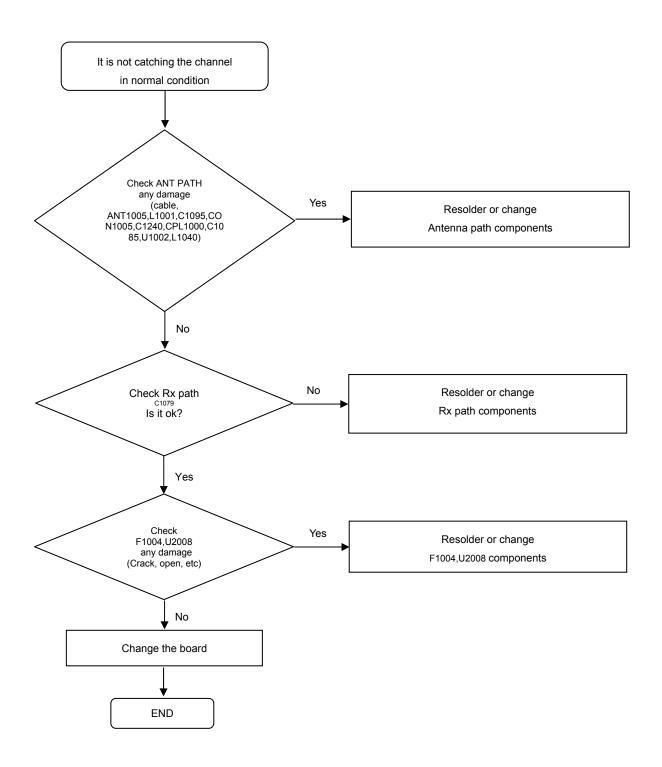


8-4-22. LTE B17 RX

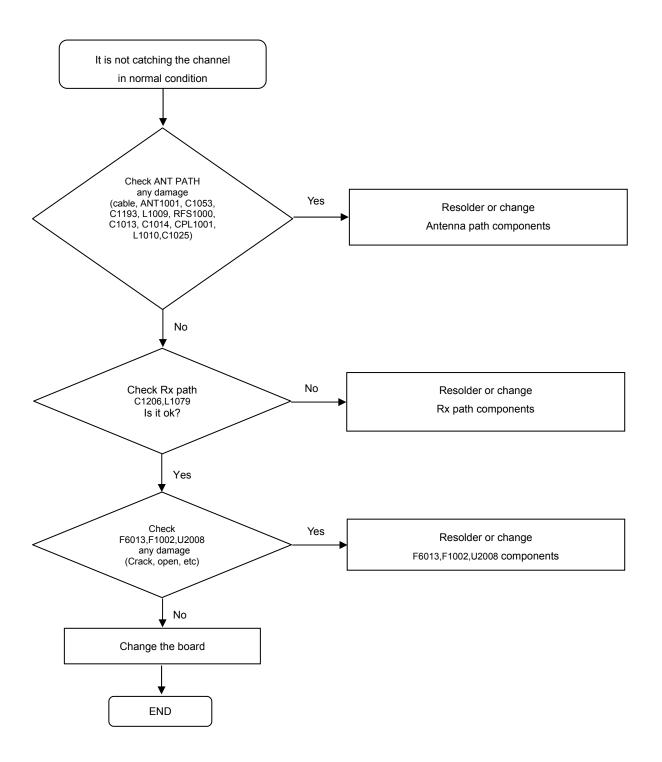




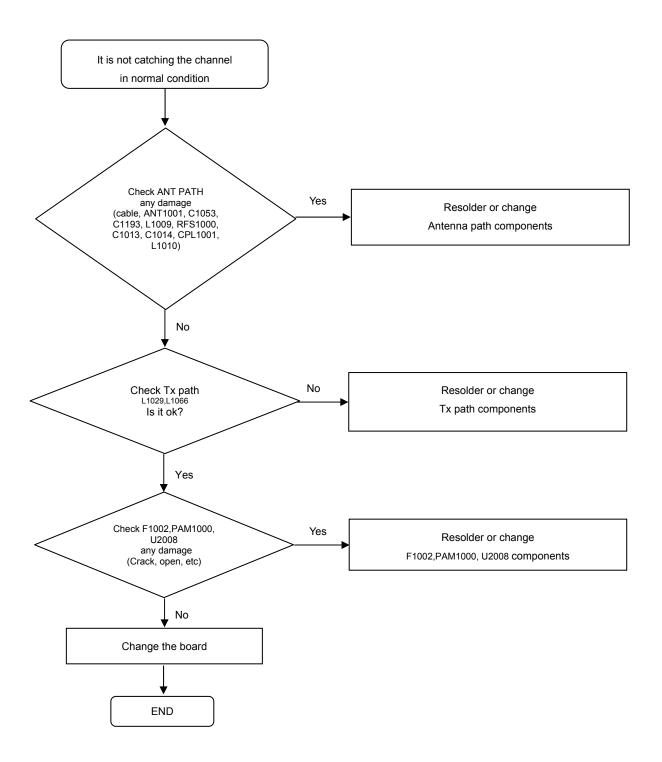
8-4-23. LTE B7 RX



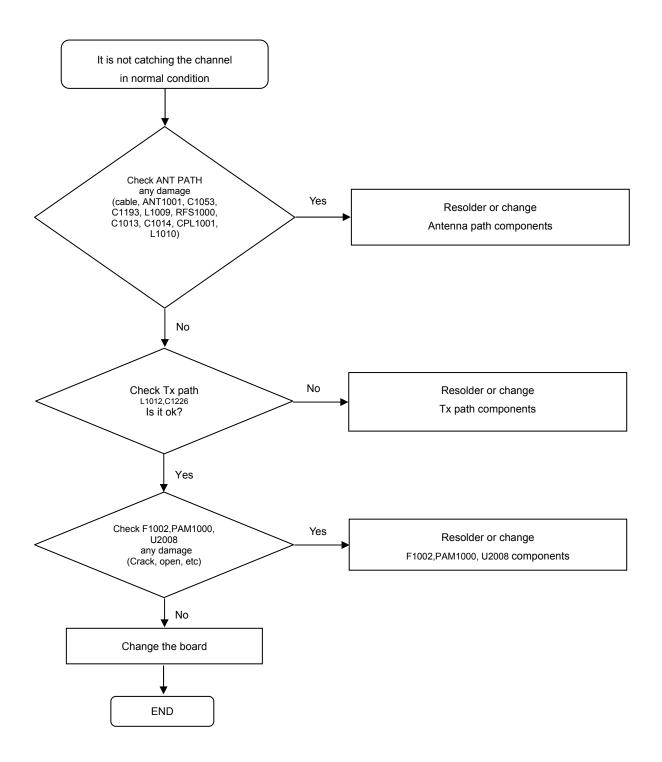
8-4-24. LTE B28 RX



8-4-25. GSM850/GSM900 TX

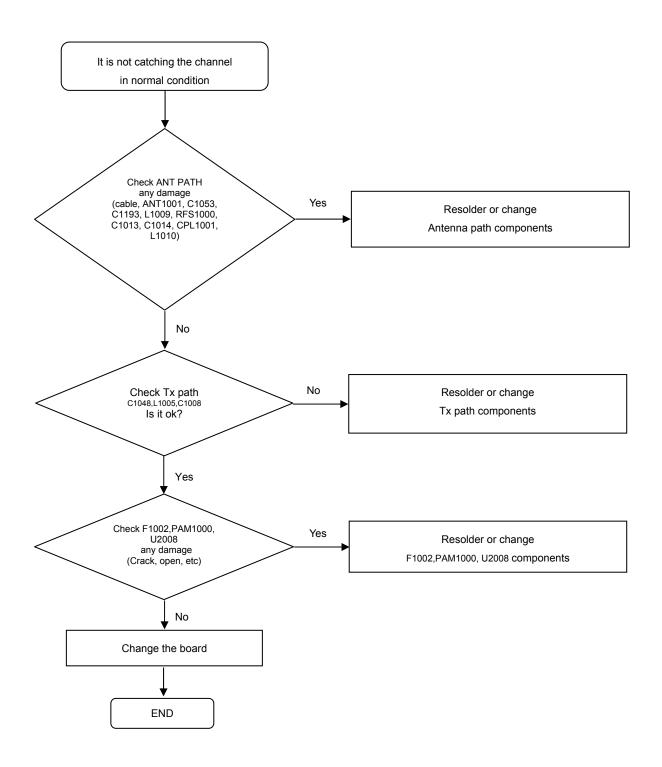


8-4-26. GSM1800/GSM1900 TX



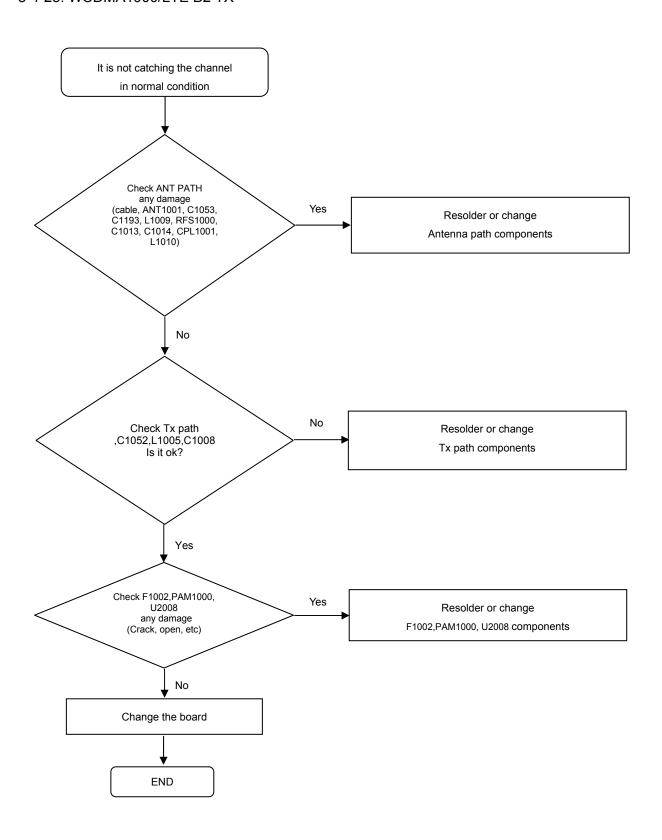


8-4-27. WCDMA2100/LTE B1 TX



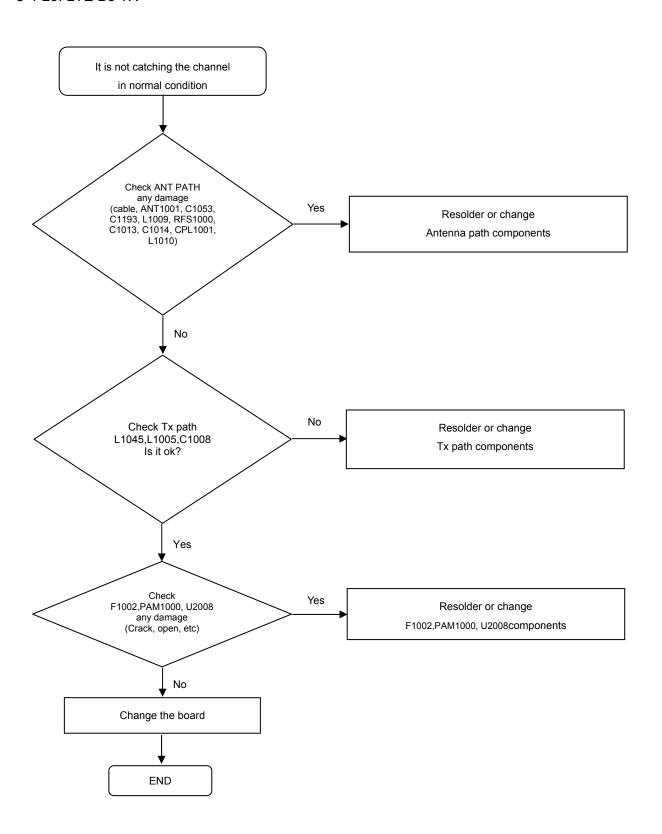


8-4-28. WCDMA1900/LTE B2 TX



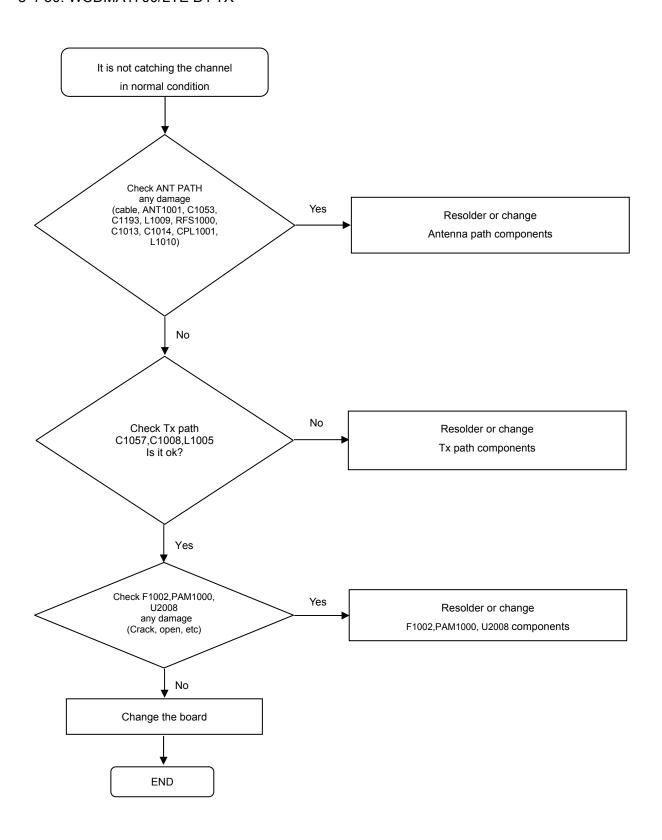


8-4-29. LTE B3 TX



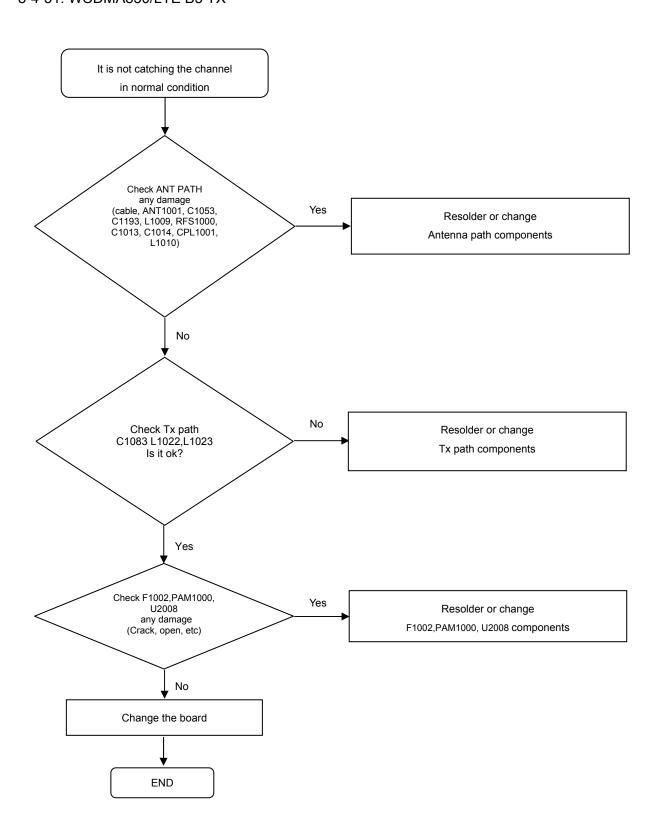


8-4-30. WCDMA1700/LTE B4 TX

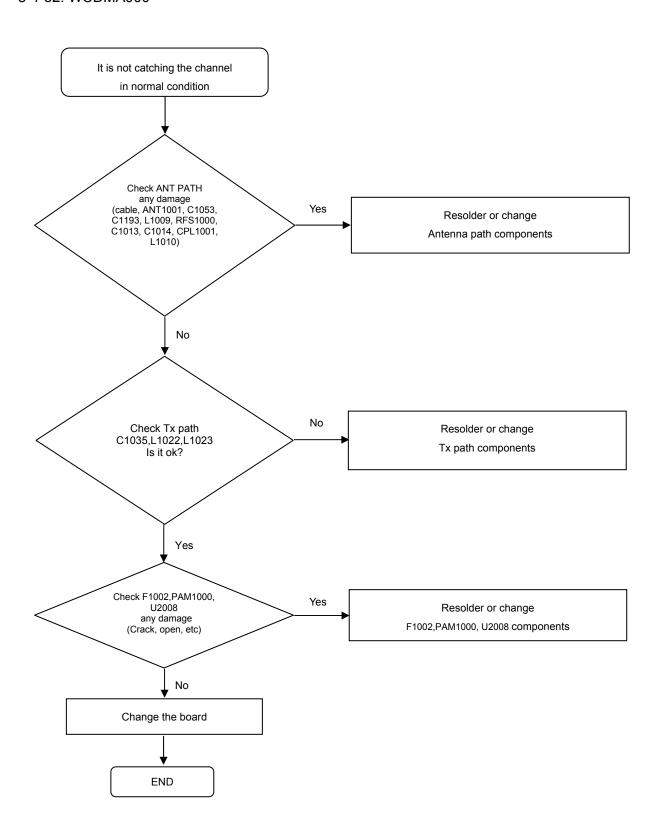




8-4-31. WCDMA850/LTE B5 TX

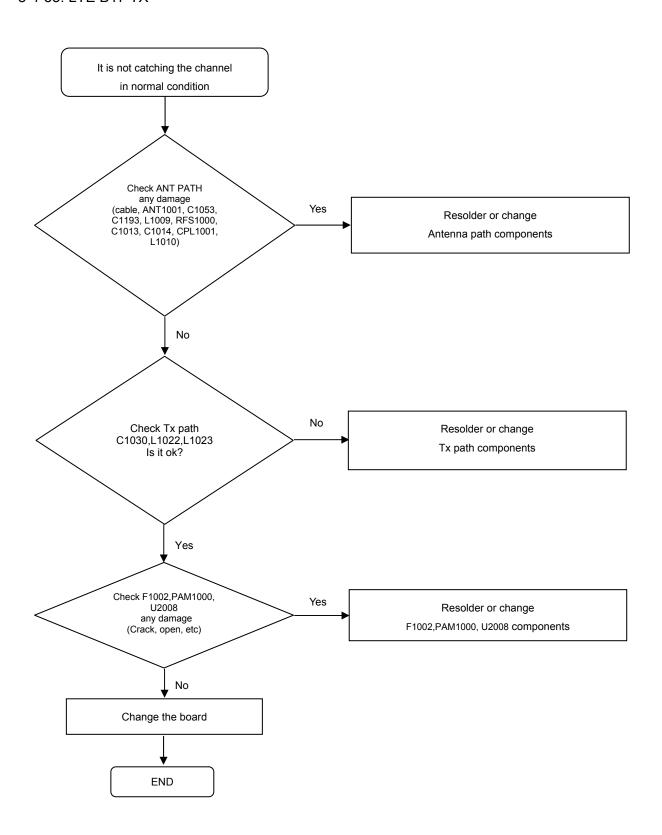


8-4-32. WCDMA900



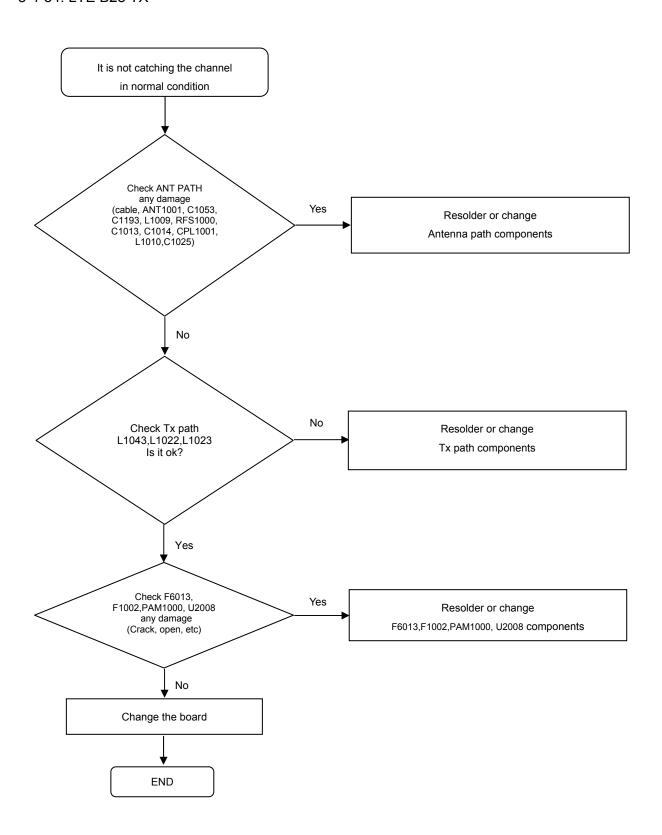


8-4-33. LTE B17 TX



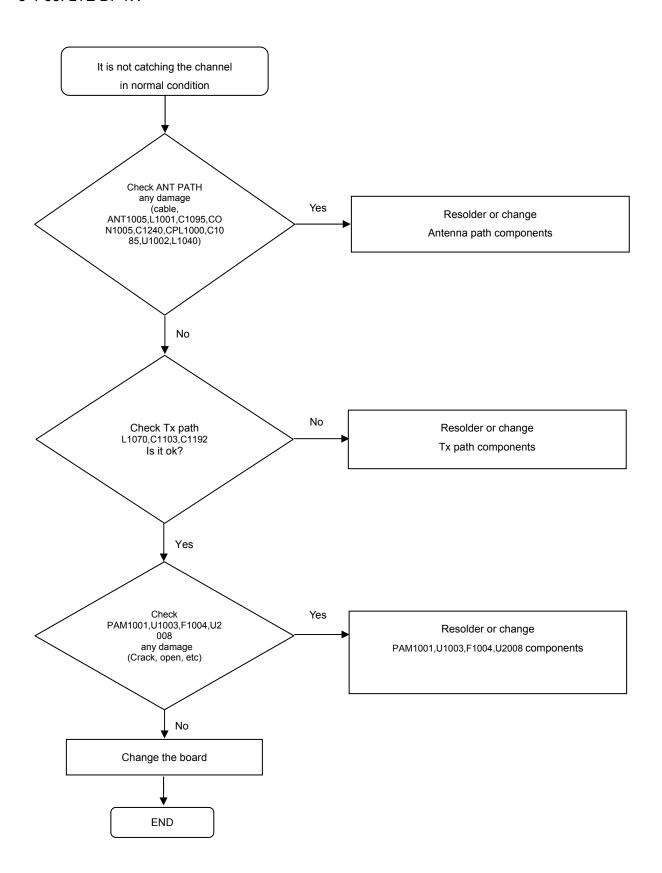


8-4-34. LTE B28 TX





8-4-35. LTE B7 TX



8. Level 3 Repair



8-5. Service Schematics

- NC Point(Top View)
UCP2000 (AP)

0	R	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27
Ă	EWVS	NC	NFWP		NFD[NFD[NFD[TEST		MCSI C DA	MCSI c da		MCSI		MCSI	MCSI		MCSI	KEYO		KEYI	IISO		IIS1	NC	VSSC
В	NC	EMMC	NFCL	NFRE	NFVE	NFD[NFD[NFD[TEST	CAM	MCSI	CAM PD1	SDAO	MCSI	MCSI	MCSI	MCSI	MCSI	MCSI	KEY0	KEYI	SDA2	IISO	IIS1	IISO	итск	NC
С		EMMC	NFRB	NFCE	NFD[NFD[NFD[MCSI C. CIK		CAM				MCSI	MCSI			KEY0	KEYI			IIS1	IIS1	MTDI	
D	VEXO	EMMC	EMMC		NFCE	NFD[NFD[MCSI C DA	MCSI C CIK		CAM			MCSI	MCSI				CLK_	SCL2	IIS0		IIS1	MTDO	TRAC	TRAC
E	EVANC	EMMC	EMMC	EMMC	EMMC	NFD[NFD[NFD[MCSI	TEST	TEST			AVSS	AVSS		VSSC				IIS0	MTRS	MTMS	TRAC	TRAC	TRAC	TRAC
F		EMVS	EMMC	ENANC	NFAL	VSSC	NFD[NFD[15]	TEST	SCLO		0	1		VSSC	VSSC		VSSC	VSSC			TRAC			TRAC	
G	EMD[EMDQ		EMD[EMD[~	Ĵ										USRT	TRAC	TRAC		UOTX	UORX
Н	EMD[EMDQ			EMVS					2	5									4		GPI0	USTX	U1TX	U1RX	UORT	UOCI
_		EMDQ	EMD[EWD[EMD[VNF (VDDA	VDDA	VCAM	AVDD	VDDA	VFB_	VSSC	VSSC	VIO_	VSSC	VSSC		/_		GPI0	USCT	USRX	U2RX	
K	EMDQ	EMD[EMD[EMD[EMD[>	EXIVD	VDDA	VDDA	VDDA	VDDA	VDDA	VFB_	VSSC	VSSC	VSSC	VSSC	VSSC) ,		AVDD	GPI0			U2TX	VIN_
L	EMD[EMDQ			EMVS	5)	EMVD	VDDA	VDDA	VDDA	VDDA	VDDA	VSSC	VSSC	VSSC	VSSC	VSSC	VSSC				AGN D. AE	VQP_		VQN_	VIP_
M		EMDQ		EMD[EMD[7		EMVD	VDDC	VDDC	VDDA	VDDA	VSSC	VSSC	VSSC	VSSC	VSSC	VDDC	VDDC		V	٩.	AVSS	VQN_	VIP_	VQP_	
N	EMCS		EMD[EWD[\			EMVD	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC	VSSC	VSSC	VDDC	VDDC	VDDC	\S)	Ġ.	AVDD	NC		VIN_	AFCO	LDO_
P	EMCS	EMA [EMA[EMA[EMVR			EMVD	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC	VSSC	VSSC	VDDC	VDDC	VDDC				NC			APCO	APCO
R	Ò,	EWA[CLKD			EWVD	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC	VSSC	VSSC	VDDC	VDDC	VDDC						NC	VQP_	
T	EMA[EMA[EMA[CLKD			EXIVD	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC	VSSC	VSSC	VDDC	VDDC	VDDC						NC	VQN_	VIN_
U	EWVS	EMCK			EMOD		-	EMVD	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC.	VSSC	VSSC	VDDC	VDDC	VDDC				CPO_	CPO_	RFSE	RFSD	VIP_
V		EMZQ	EMA[EMA[EMA[EMVD	VDDC	VDDC	VDDC	VDDC	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC	VDDC			VIO_	CPO_			RFSC	
W		EMDQ MEd1	EMD[EMVS		J	VLCD	VDDC	VFB_	VFB_	VDDC	VDDC	VDDC	VSSC	VSSC	VSSC	VSSC	VDDC				VIO_	CPO_		CPO_	CPO_
Y	EMD[EMD[EMD[EMD[EMVS					, ',	Ö),					L						CPO_	XTL_	CPO_	CPO_	CPO_
AA	EMDQ	EMDQ c[o]	7	EMD[Ш			<	×													VIO_	CPO_	CPO_	CPO_	Ш
AB		Y	EMD[EMD[Ш		AVDD)			AVSS					DM	VSSC ORF					CPO_			CPO_	CPO_
AC	EMDQ w[a]	00	EMD[EMD[_	AVSS			ANA_	CHIP	L	AUD_	CLK2	CLK2		DP	VIO_	SDO_	VSD1	40	SIMC	SDA1	EXTI		SPI0	SPI0
AD	EMD[EMD[EMD[×	5	Ĺ	MDSI	LCD_		XTL_	XTL_	AUD_			MEMS	MEMS		SDO_	SD1_	SIMC		SIMC	SCL1	EXTI	SPI0	Щ
AE		EMDQ	EMD[EMD[)*			MDSI	LCD_			CLK_	ADI_			MEMS	MEMS			SD1_	SD1_			SIMR	SIMD	SDAS	SPIO
AF	NC	c[o]:	EMD[MDSI	MDSI	MDSI	MDSI	MDSI	MDSI	EXT_	AUD_	AUD_	ADI_	ADI_	CLK2	VRES	SD0_	SDO_	_002	SD1_	SD1_	SIMR	VSIM	SIMD	VSIM	SCL3	NC
AG	EMVS	NC	/	MDSI		MDSI	MDSI		LCD_	LCD_		AUD_	AUD_		PTES	VDDU		SDO_	_OGS		VSD	SD1_		SIMR	VSIM	NC ?	VSSC