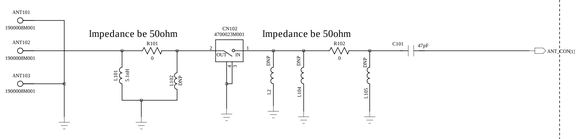
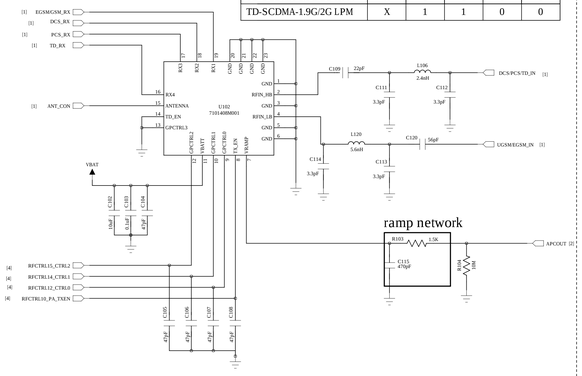
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

RF ANT

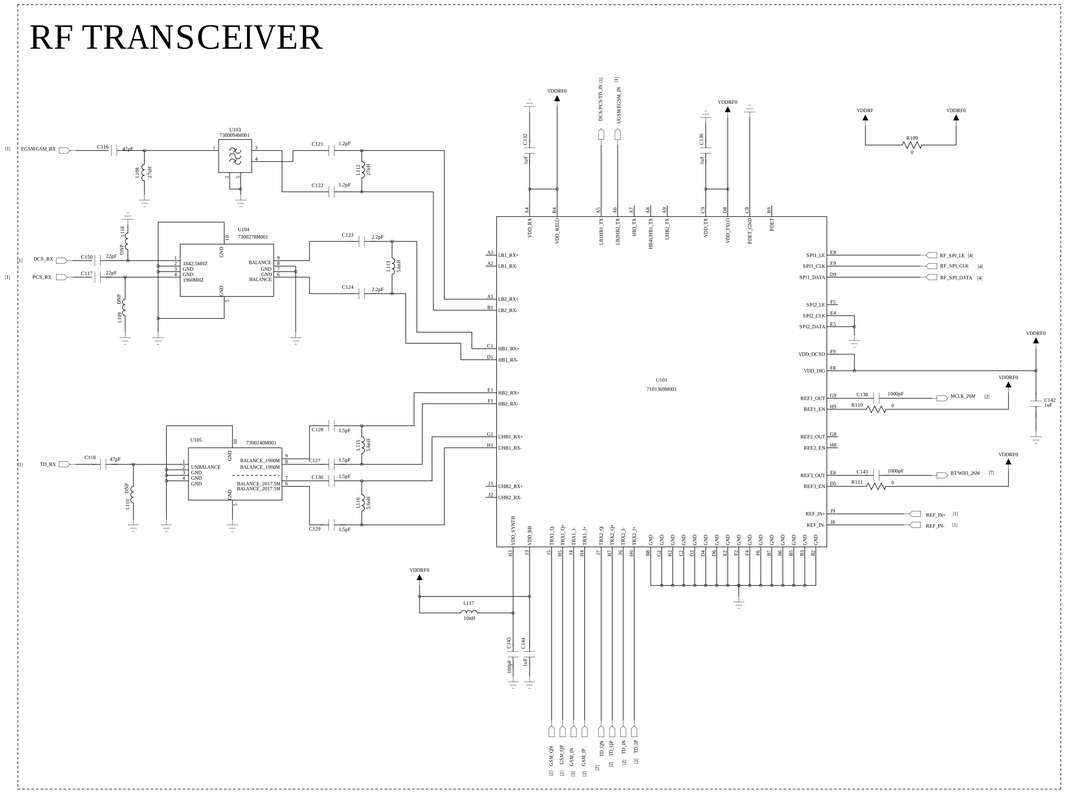


PA MODULE

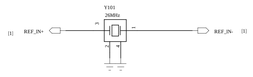
	VRAMP	TXEN	Ctrl_2	Ctrl_1	Ctrl_0
RX1 UGSM/EGSM	0	0	1	0	0
RX2 DCS	0	0	1	1	0
RX3 PCS	0	0	0	1	1
RX4 TD_1.9G/2G_RX	0	0	0	0	1
TX LB GSM	RAMP	1	0	1	0
TX HB GSM	RAMP	1	0	1	1
TX LB EDGE	1	1	0	0	0
TX HB EDGE	1	1	0	0	1
TD-SCDMA-1.9G/2G HPM	X	1	1	0	1
TD-SCDMA-1.9G/2G LPM	X	1	1	0	0



RF TRANSCEIVER



26MHz DCXO or TCXO



DCXO,TCXO selection

VALUE	R116	R115	R117	R118	C148	C149
DCXO	NF	NF	0ohm	0ohm	0ohm	NF
TCXO	0ohm	0ohm	NF	100pF	0.1uF	1000pF

COMPANY:			
TITLE:			
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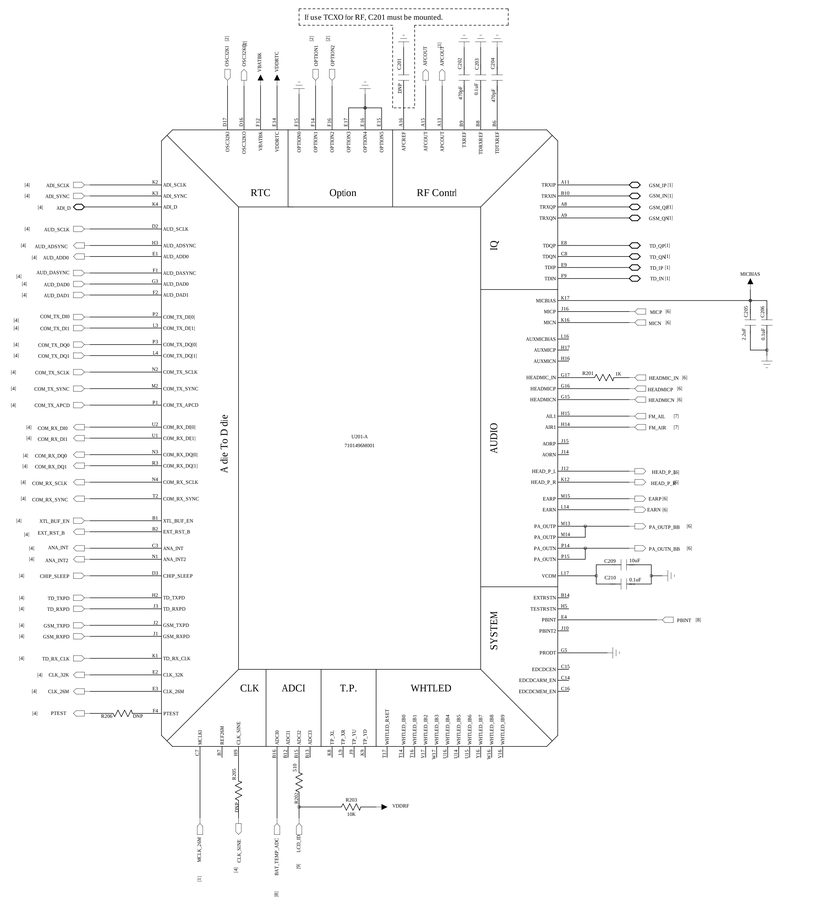
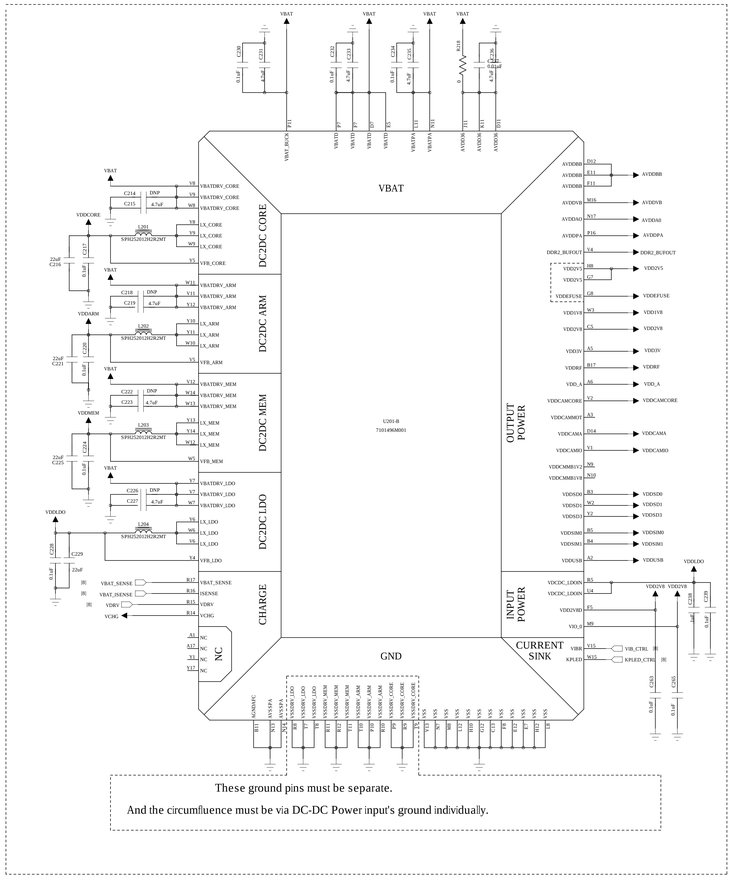
ABB_ANALOG



ABB_POWER

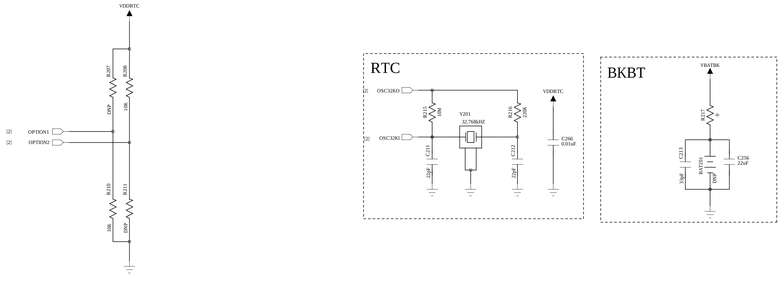


These ground pins must be separate.

And the circumfluence must be via DC-DC Power input's ground individually.

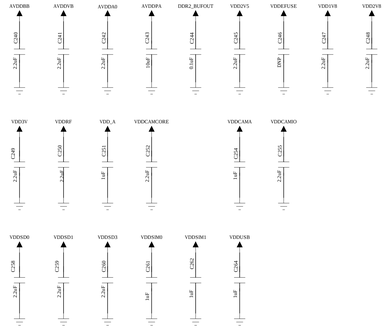
Power	Output Voltage (V)	Output Current	Used Voltage	Default
VDD1V8	1.2/1.3/1.5/1.8	250mA	1.8V	ON
VDD2V5	2.5/2.75/2.9/3.0	110mA	2.5V	ON
VDD2V8	1.8/2.65/2.8/3.0	250mA	2.8V	ON
VDDSIM0	1.8/2.9/3.0/3.1	80mA	1.8/3.0V	OFF
VDDSIM1	1.8/2.9/3.0/3.1	80mA	1.8/3.0V	OFF
VDDCMMB1V8	1.2/1.3/1.5/1.8	250mA	1.8V	OFF
VDDCMMB1V2	1.2/1.3/1.5/1.8	200mA	1.2V	OFF
VDDRF	1.8/2.5/2.85/2.95	250mA	2.85V	ON
VDD3V	1.8/2.5/2.8/3.0	200mA	3.0V	OFF
VDDSD0	1.8/2.5/2.8/3.0	150mA	2.8V	OFF
VDDSD1	1.2/1.3/1.5/1.8	100mA	1.8V	OFF
VDDSD3	1.2/1.3/1.5/1.8	100mA	1.8V	OFF
VDDCAMCORE	1.2/1.3/1.5/1.8	200mA	1.5V	OFF
VDDCAMIO	1.2/1.3/1.5/1.8	200mA	1.8V	OFF
VDDCAMA	1.8/2.5/2.8/3.0	100mA	2.8V	OFF
VDDCAMMOT	1.8/2.8/3.0/3.3	150mA	2.8V	OFF
VDDUSB	3.1/3.2/3.3/3.4	60mA	3.3V	OFF
VDD_A	1.8	80mA	1.8V	ON
AVDDBB	2.8/2.9/3.0/3.1	60mA	3.0V	ON
AVDDVB	2.9/3.1/3.2/3.3 3.4/3.5/3.6/3.8	20mA	3.3V	OFF
AVDDAO	2.9/3.1/3.2/3.3 3.4/3.5/3.6/3.8	100mA	3.3V	OFF
VDDRTC	1.5/1.6/1.7/1.8	0.5mA	1.8V	ON
VBATBK	2.6/2.8/3.0/3.2		2.8V	ON
MICBIAS	1.9/2.1/2.3/2.5		2.1V	OFF
AUXMICBIAS	1.9/2.1/2.3/2.5		2.1V	OFF
VDDMEM	1.7/1.8/1.9/2.0 1.1/1.2/1.3/1.4	800mA	1.8V(DDR1) 1.2V(DDR2)	ON
VDDARM	0.65/0.7/0.8/0.9 1.0/1.1/1.2/1.3	1100mA	1.2V	ON
VDDCORE	0.65/0.7/0.8/0.9 1.0/1.1/1.2/1.3	800mA	1.1V	ON
VDDLDO	1.8/1.9/2.0/2.1 2.2/2.3/2.4/2.5	800mA	2.2V	ON

OPTIONS



Option	Function	L	H
Option0	DCDC CORE OPTION OUTPUT	Internal DCDC	External DCDC
Option1	LDO POWER ON OPTION OUTPUT	LDO POWER ON BY SEQ	LDO POWER ON TOGETHER
Option2	NEW POWER ON RST OPTION OUTPUT	NEW POWER ON RST SCHEME(DIGITAL POR)	OLD POWER ON RST SCHEME(ANALOG POR)
Option3	DCDC ARM OPTION OUTPUT	Internal DCDC	External DCDC
Option4	DCDC MEM OPTION OUTPUT	Internal DCDC	External DCDC
Option5	DCDC MEM OUTPUT SELECT	DDR2 application (1.2V)	DDR1 application (1.8V)

LDO ouput



All capacitors must be close to ABB.
DDR2_BUFOUT must be protected.

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DBB_POWER

All capacitors must be close to DBB.

POWER

GND

U301-C
71914979001

DBB_MEMORY

U301-B
71914979001

MEM DDR2
(VDDMEM)

NAND
(VDDNF)

LANE-0

LANE-1

LANE-2

LANE-3

COMPANY:			
TITLE:			
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DBB_DIGITAL

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R401 NF: Crystal for RF 26M
R401 Mounted: TCXO for RF 26M

KEYPAD (VDDH01) | RF CTRL (VDDH02) | UART (VDDH02)

SDIO0 (VDDH04)

SDIO1 (VDDH01)

SDIO3 (VDDH03)

SIM0 (VDDH08)

SIM1 (VDDH01)

SPI0 (VDDH01)

SPI2 (VDDH1.5V)

JTAG (VDDH01)

IIS (VDDH02)

MIPI CAM (DCG_VDD0)

MIPI DISPLAY (MDSI_AVDD)

LCD (VDDLCM)

TRACE DATA (VDDH02)

USB

IIC (VDDH1C) (VDDH02)

D die To A die (VDDH08)

GPIO

XTL_EN (VDDH02)

CLK (AVDD_PLL)

CCIR (VDDCAMIO)

U201-A
7101497M001

I2C0: CTP
I2C1: Camera, ATV
I2C2: G Sensor, M Sensor, L+P Sensor, Gyroscope, FM
I2C3: LCM, NFC, MHL

COMPANY:			
TITLE:			
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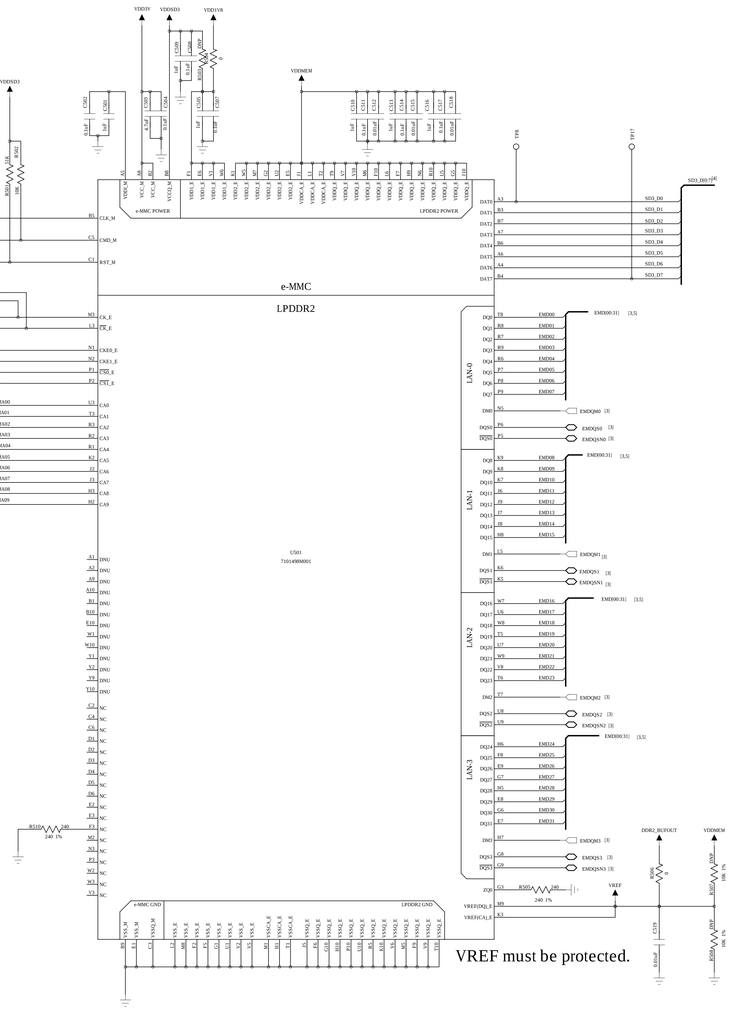
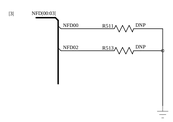
MCP

DDR1 Voltage is 1.8V, DDR2 Voltage is 1.2V.
EMMC Voltage is 3V, IO is 1.8V.





Strapping Pin

	0	1
NFD00	NAND:Small Page eMMC:VDD3V=1.8V	NAND:Large Page eMMC:VDD3V=3V
NFD01	NAND	eMMC
NFD02	eMMC IO 1.2V	eMMC IO 1.8V
NFD03	USB1.1 Download	USB2.0 Download

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QUALITY CONTROL:	DATED:	CODE: / SIZE: / DRAWING NO: / REV:	
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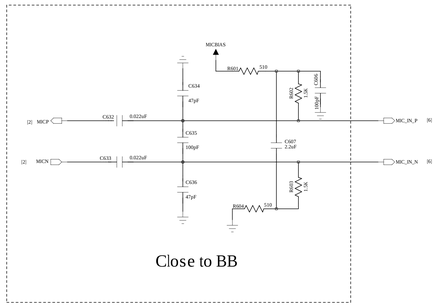
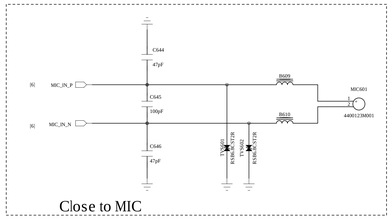
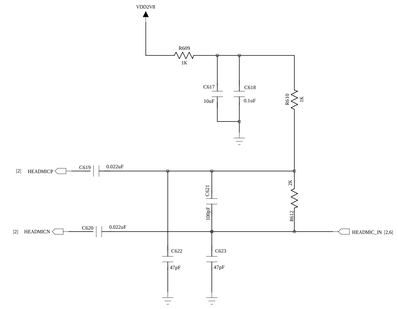
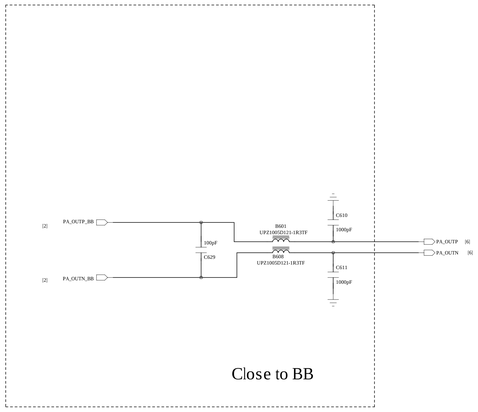
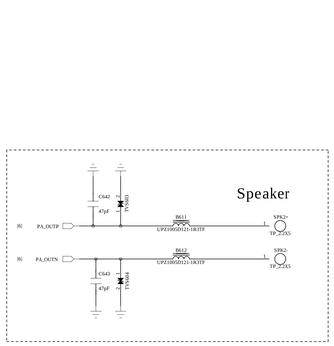




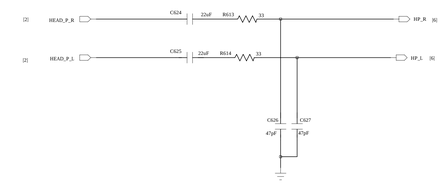
HEAD MIC



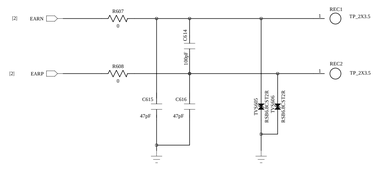




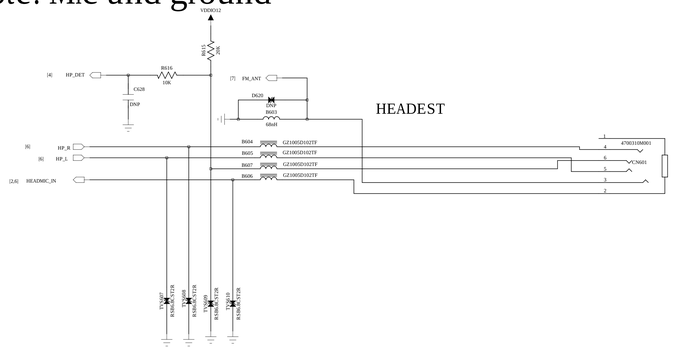
HEAD EAR



RECEIVER CIRCUIT



3.5mm Headphone Jack
Note: Mic and ground



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QUALITY CONTROL:	DATED:		
RELEASED:	DATED:	SCALE:	SHEET: OF

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	RF_SW_CTRL[0:2]		
BT_TX	H	L	L
WL/BT_RX	L	H	L
WL_TX	L	L	H

WLAN RF
WLAN RF Control
WLAN SDIO Bus
JTAG interface
HSIC interface
WLAN Supplies
Clocks
WLAN GPIOP
Bluetooth Suppliers
FM Transceiver
FM transceiver Suppliers
Bluetooth UART
Bluetooth Test Mode
Miscelaneous Suppliers
Bluetooth
Bluetooth/FM I2S
Bluetooth PCM
Bluetooth GPIO
Miscelaneous
Ground
Integrated LDOs
Integrated switching Regulators

To CPU interrupt for WLAN wakeup CPU
To CPU interrupt for BT wakeup CPU

A4 ,C3 two branch,C922 close to A4Pin<

DRAWN:	DATED:	COMPANY:	
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QUALITY CONTROL:	DATED:	CODE: / SIZE: / DRAWING NO: / REV:	
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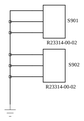
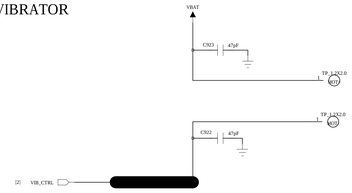
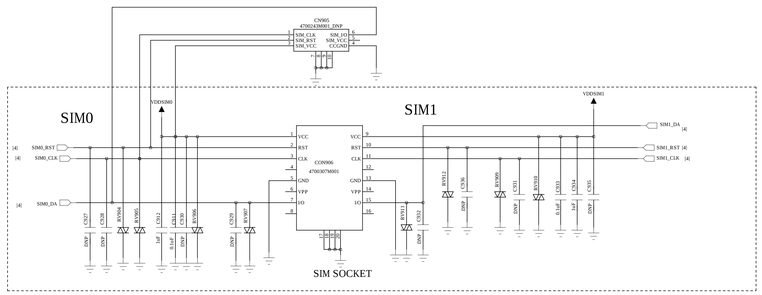


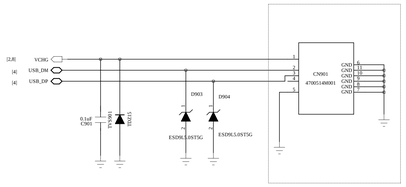
VIBRATOR



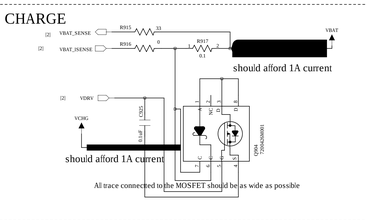
should afford 130mA current

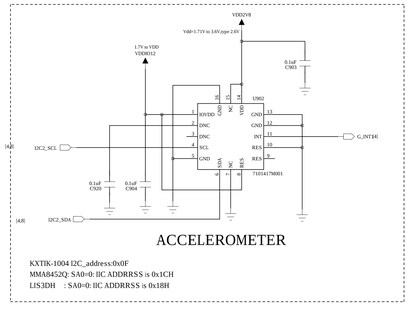


USB Connector



CHARGE

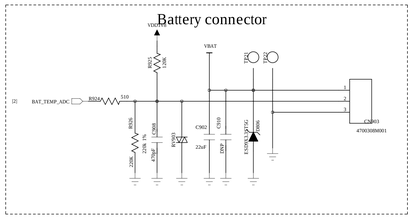




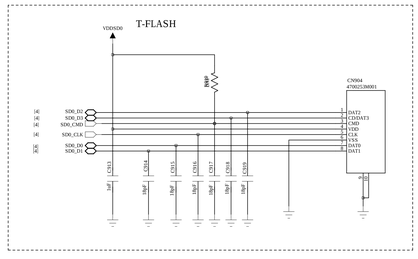
ACCELEROMETER

KXTIK-1004 I2C_address:0x0F
MMA8452Q: SA0=0: IIC ADDRRSS is 0x1CH
LIS3DH : SA0=0: IIC ADDRRSS is 0x18H

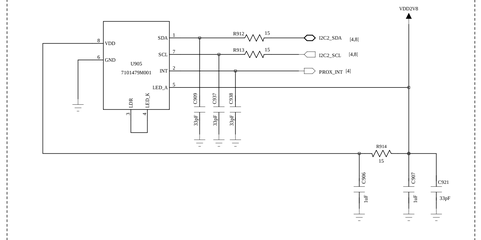
Battery connector



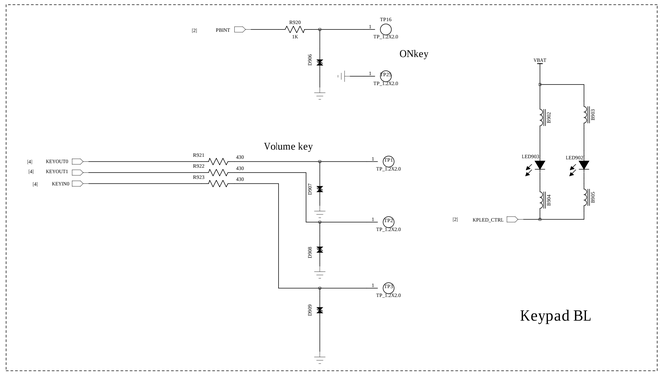
T-FLASH



Ambient Light Sensor with Proximity Sensor



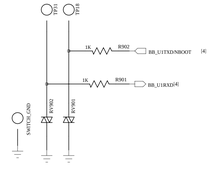
Keypad BL



Press KEYIN0+KEYOUT0 when power up
will make the phone boot from USB

USB BOOT CONTROL

NBOOT = Float----->NAND BOOT
NBOOT = 0 ----->USB BOOT



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QUALITY CONTROL:	DATED:		
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REVISION RECORD			
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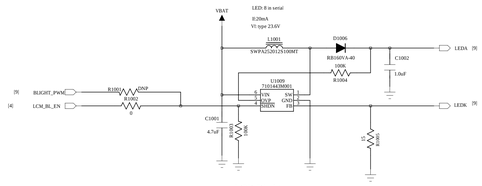
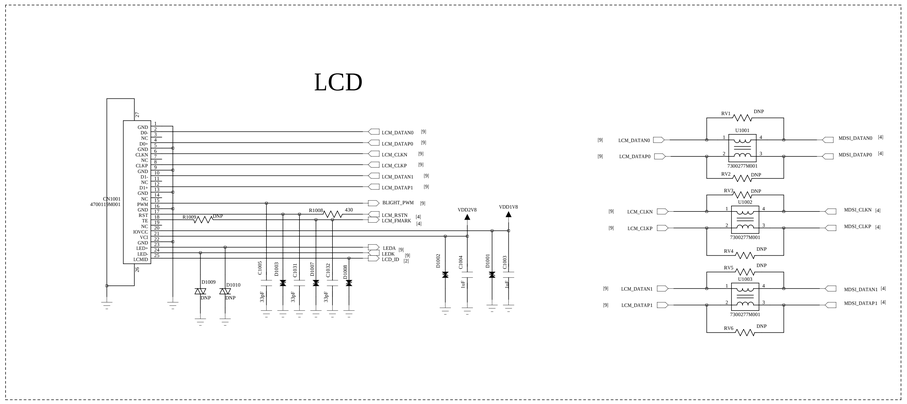
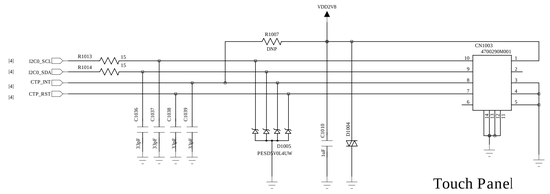
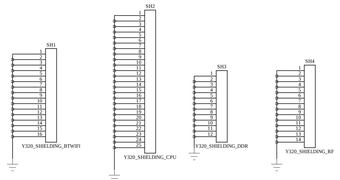


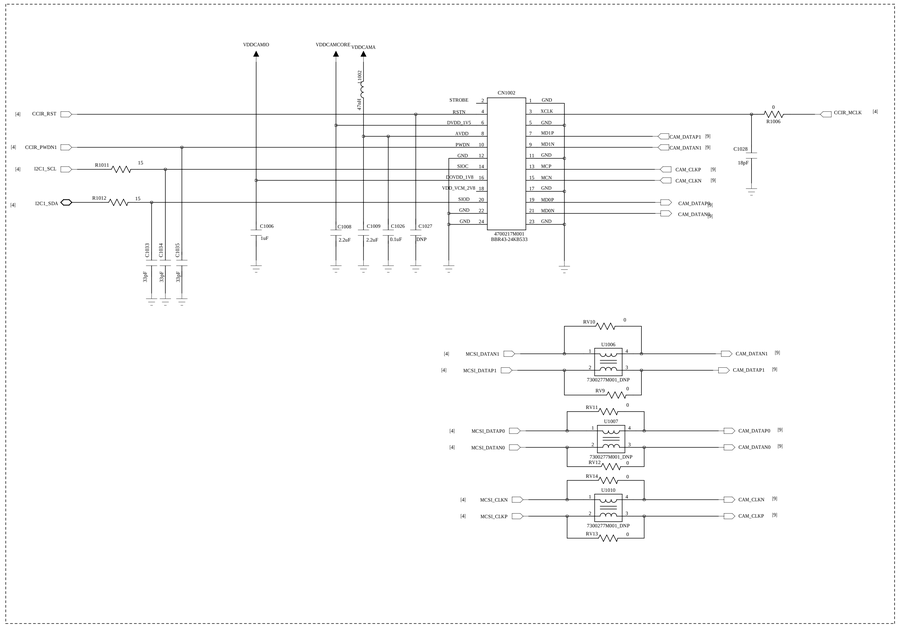
LCD BL driver

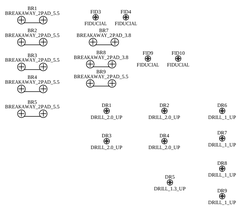




Cap Touch panel







DRAWN:	DATED:	COMPANY:			
		TITLE:			
CHECKED:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
QUALITY CONTROL:	DATED:				
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